

A B C D E F G H J K L M N

VOLTAGE POLARITIES SHOWN ARE FOR MOTORING DAL(+)

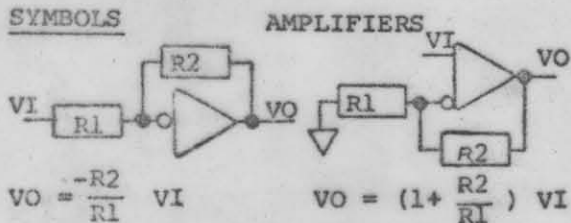
SIGNAL DEFINITIONS AND LOCATIONS

HARDWARE ABBREVIATIONS

MCC MAIN CONTROL CARD
 IFC INTERFACE CARD
 PSC POWER SUPPLY CARD
 SCR THYRISTOR ASSEMBLY
 DGC DIAGNOSTIC CARD
 MFC MOTOR FIELD CONTROL

MDR MODIFICATION RACK

SYMBOLS



CASE GROUND
 VI VO
 ABS
 VO = SIGN () X ABSOLUTE VALUE OF VI
 STAB ON TERMINAL

□ TERMINAL AT 2TB, 3TB, 4TB, RTB.
 EX: 9 [2] - 2TB9; X2 [R] - RTBx2

○ TERMINAL AT T.B.'s
 POTENTIOMETER ARROWS ON THE CARD
 ELEMENTARY DIAGRAMS INDICATE THE
 WIPER DIRECTION AS THE POTENTIOMETER
 SHAFT IS ROTATED CLOCKWISE TO INCREASE
 FUNCTION.

FUNCTION	USE	LOC	JUMPERS
60HZ		MCC	AA-AS, BA-BG, CA-CS
		MFC	ZA-ZB (IF USED)
50HZ	X	IFC	0.0047uF RT1-RT2
		IFC	0.0047uF RT2-RT3
		IFC	0.0047uF RT3-RT1
		MCC	AA-AF, BA-BF, CA-CF
IOC-400%	X		NONE
-500%		IFC	I-IH1
-300%		IFC	I-ILO
SR5 - 9v	X		(NONE)
9 - 20v		MCC	SRH-COM
JOGR 10v			(NONE)
20v	X	MCC	JH - COM
LT.3-7sec.	X		(NONE)
2 - 60sec		MCC	332FROM LT1TOCOM
VREG		IFC	NT-CMF, CC-COM
DC TACHO	X		(NONE)
AC TACHO		MCC	AT1-AT2
TACHO FILT		IFC	TC-TC
TACHO V.		IFC	NT-NT1, PT-PT1
24-64vdc		IFC	NT-NT1, PT-PT1
27-71vac		IFC	NT-NT2, PT-PT2
60-160vdc		IFC	NT-NT2, PT-PT2
66-177vac		IFC	NT-NT2, PT-PT2
110-300vdc	X	IFC	NT-NT3, PT-PT3
120-300vac		IFC	NT-NT3, PT-PT3
GL34 G256		MFC	NONE
1.3 1.7		MFC	YB-YD
2.4 2.8		MFC	YA-YB
4.0 5.0		MFC	YA-YB, YC-YD
7.0 13		MFC	YA-YC
13 25	X	MFC	YA-YC, YB-YD
L/R < .25S		MFC	QA-QB
INH RUN		DGC	D1-D2 (IF USED)

CAPACITORS TO BE
 REMOVED FOR 60HZ

* CEMF COUNTER EMF (16)
 * CFB CURRENT FEEDBACK (16)
 CMFA ABSOLUTE VALUE CEMF (08)
 CRM CROSSOVER MODIFY (11)
 DFP DELAYED FIRING POWER (25)
 * DR DRIVER REFERENCE (33)
 * EAO ERROR AMP OUTPUT (33)
 EST EXTERNAL FLT STOP INPUT (14)
 FALT FAULT (14)
 * FC FIELD CURRENT (NS26)
 FDR FIELD DIAGNOSTIC REFERENCE (08)
 FEA FIELD ECONOMY ADJUST (25)
 FF FIELD FAULT (28)
 IABS MOTOR CURRENT ABSOLUTE (09)
 IL⁺ CURRENT LIMIT ADJUST (23)
 IMET CURRENT SIGNAL FOR METER (10)
 * IPU INITIAL PULSE (20)
 * LR LOCAL REF. FROM DGC (33)
 * JOG JOG SWITCH INPUT (23)
 * JOGR JOG REFERENCE INPUT (31)
 * MAC MAX/MA CONTROL SIGNAL (20)
 MSW MODE SWITCH (30)
 * OSC OSCILLATOR (17)
 * PCR PHASE CONTROL REF. (26)
 * PRE DRIVE PRECONDITION (21)
 ØSEQ PHASE SEQUENCE (14)
 RERR REGULATOR ERROR (27)
 RIJ INTEGRATOR SUMMING JUNCTION (27)
 RJ REGULATOR SUMMING JUNCTION (31)
 RRA REGULATOR RESPONSE ADJUST (30)
 RSET RESET (16)
 * RTR READY TO RUN (16)
 * RUN RUN SWITCH INPUT (21)
 * SA-C PHASE SYN OUTPUT (16)
 * SFB SPEED FEEDBACK (20)
 SMET SPEED SIGNAL FOR METER (12)
 * SR SYSTEM REFERENCE INPUT (29)
 * SYS SYSTEM FAULT TRIP (13)
 * TA OUTPUT FOR TACHO TRIP ADJUST (20)
 TF TACHO FAULT (NS28)
 * TFB TACHOMETER FEEDBACK (20)
 TFR AC TACHO FREQUENCY OUTPUT (13)
 * TR TIMED REFERENCE (33)
 * VFB VOLTAGE FEEDBACK (19)
 * WFR WEAK FIELD REFERENCE (20)

(* = TEST POINT ON DOOR FRONT)

MAPPING SYSTEM

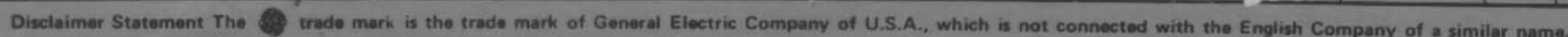
(NS/PS/TS) PS - PAST SHEET
 NS - NEXT SHEET
 TS - THIS SHEET

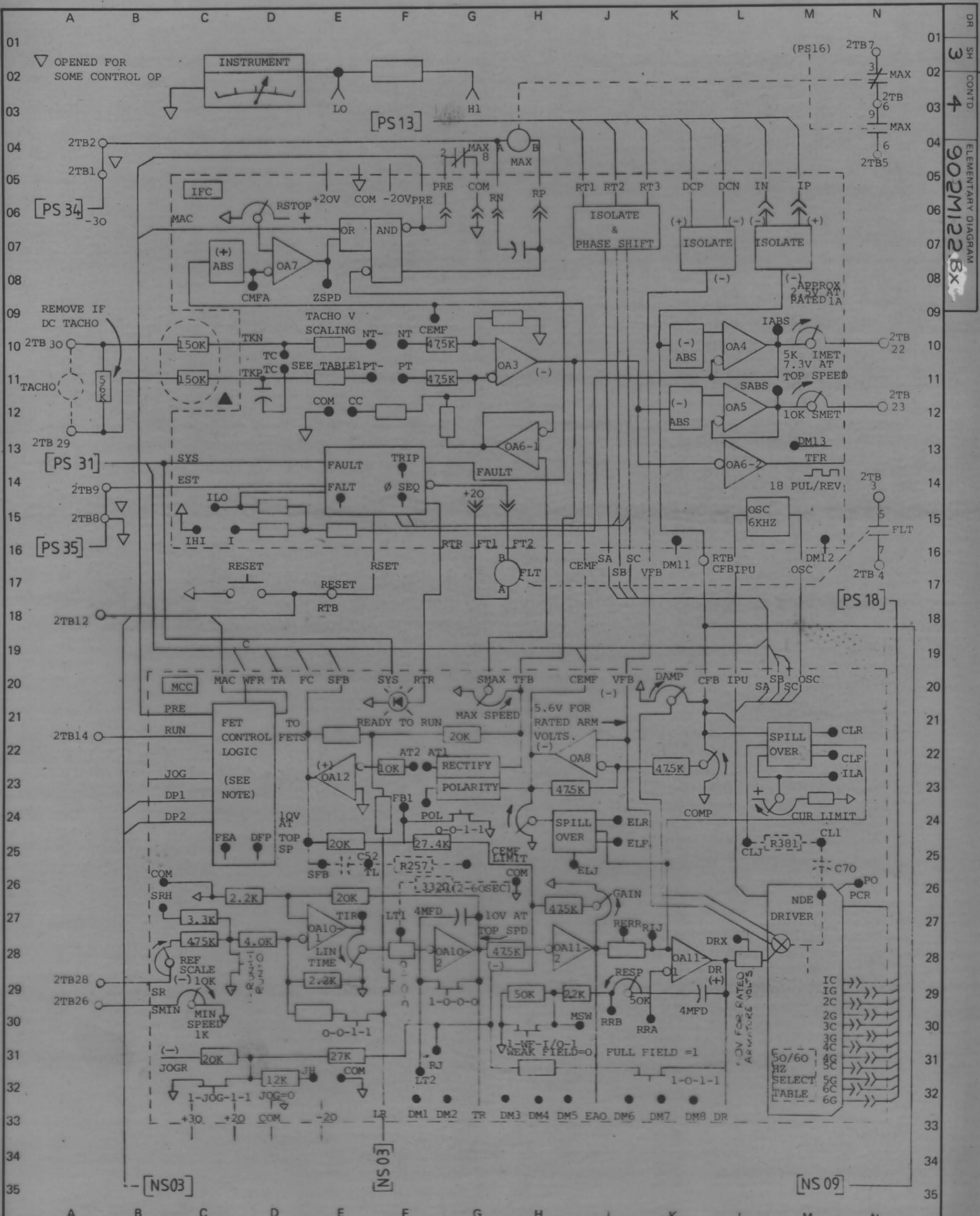
HENCE [PS - 12] DENOTES LOCATION ON PAST SHEET LINE 12. OTHER LOCATIONS ARE
 DENOTED BY SHEET NUMBER AND LINE, E.G. [1A16] SIGNIFIES LOCATION ON SHEET
 1A, LINE 16 ETC.

NOTE: FIELD EFFECT TRANSISTOR: THE
 CLOSED/OPEN (I/O) STATE OF THESE
 SWITCHED FOR "PRECONDITION" - "RUN"
 OR JOG" - "DIAGNOSTIC STATIC" -
 "DIAGNOSTIC RUN" IS SHOWN BY A
 FOUR DIGIT WORD WITH STATE SEQUENCE.

THESE RESISTORS ARE CRIMPED IN WIRE HARNESS

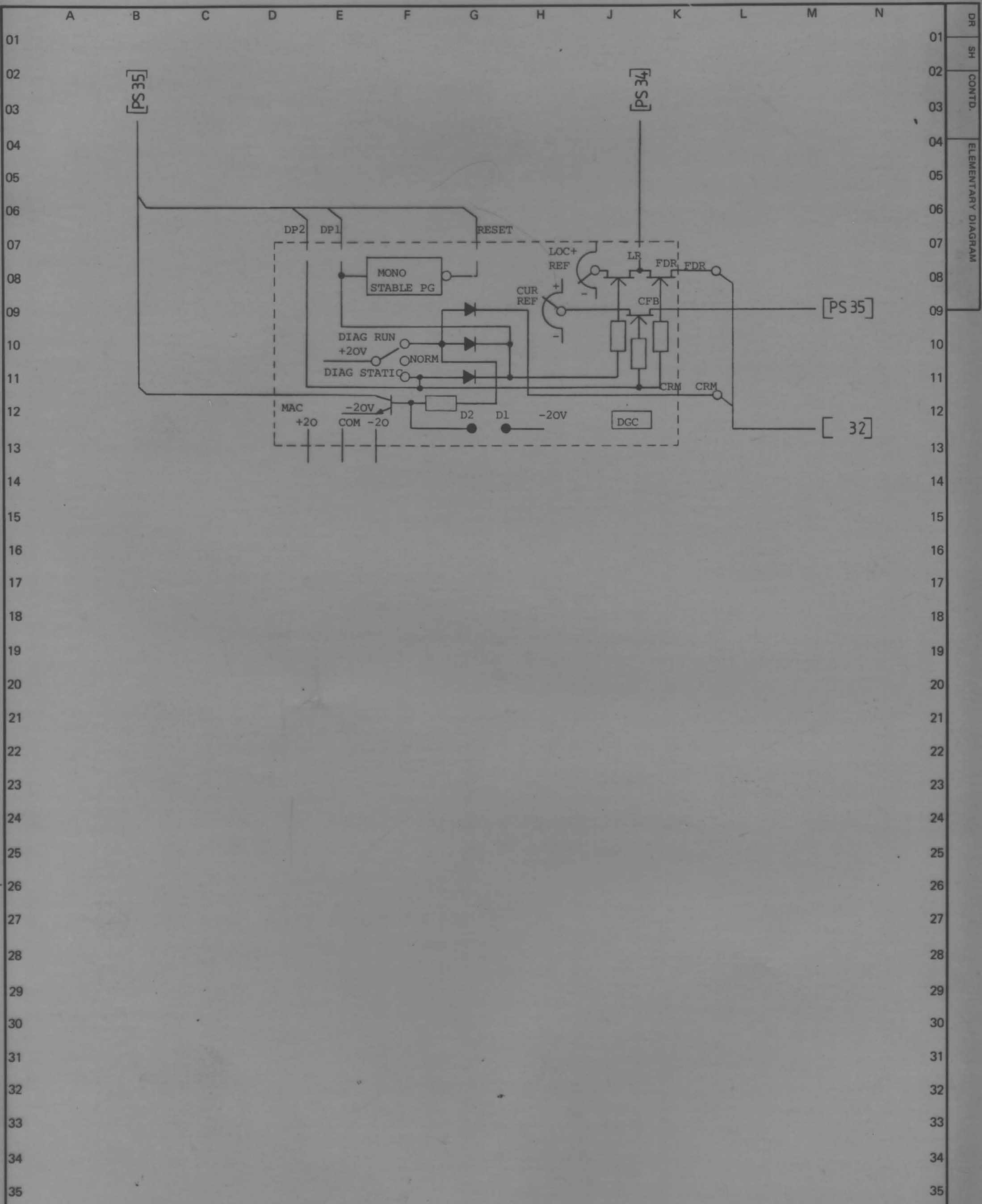
TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	3-7-79			WEBSTAR & BENNETT LTD			IDENT	
										75 HP BDC 3034R SPINDLE DRIVE			DR SH	
										ELEMENTARY DIAGRAM			1	
										GO NUMBER			2	
										921N08			902M122BX	
										ELEMENTARY DIAGRAM			2	
										CONTD.			1	



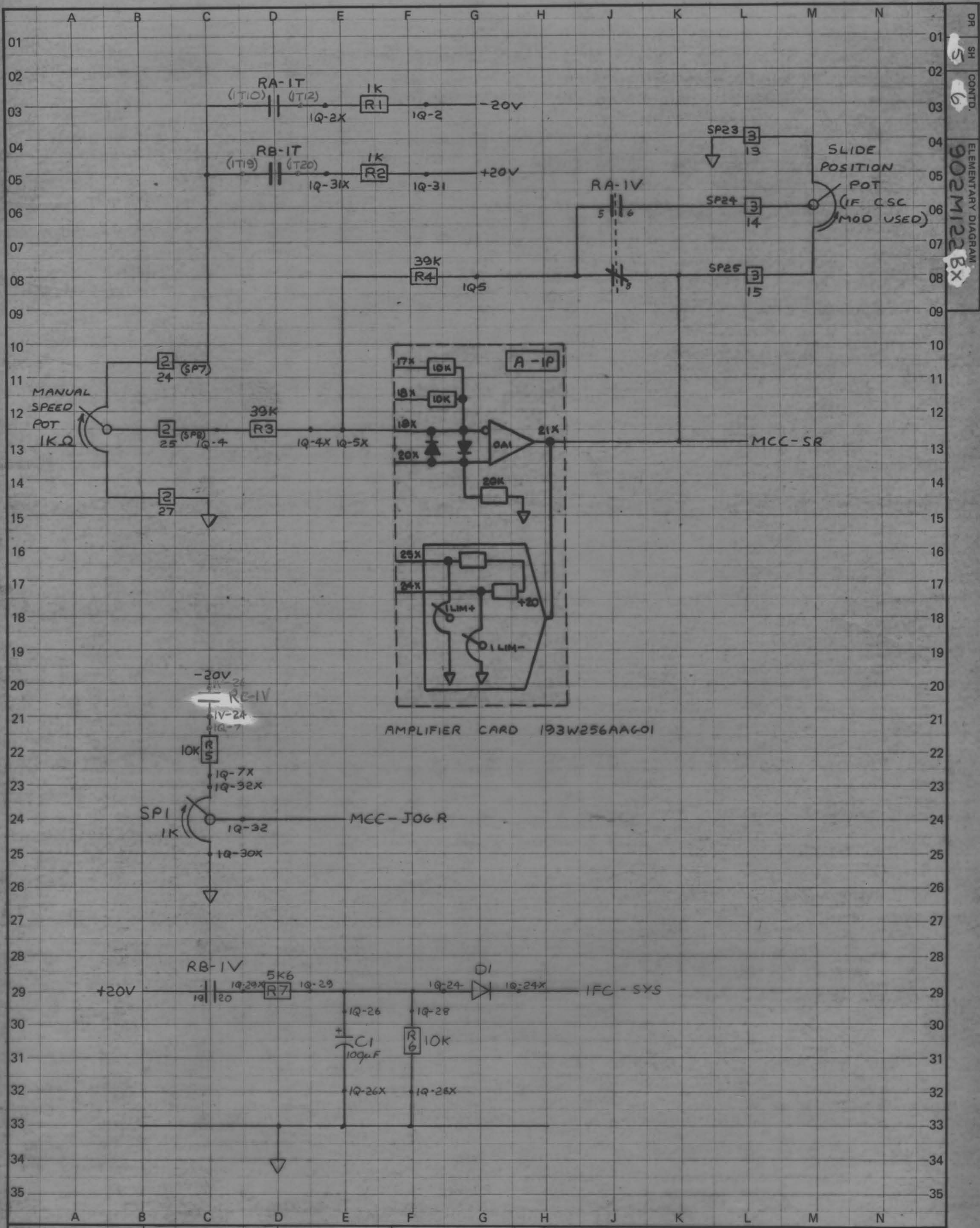


TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	3-7-79		WEBSTER & BENNETT LTD.		75HP BDC 3034R SPINDLE DRIVE, ELEMENTARY DIAGRAM		IDENT	
						TECHN.	AP		GO NUMBER		921N06		DR	
						ENG.	AP		ELEMENTARY DIAGRAM		902M122BX		SH	
						APPD.	11/1		CONTO		4		3	

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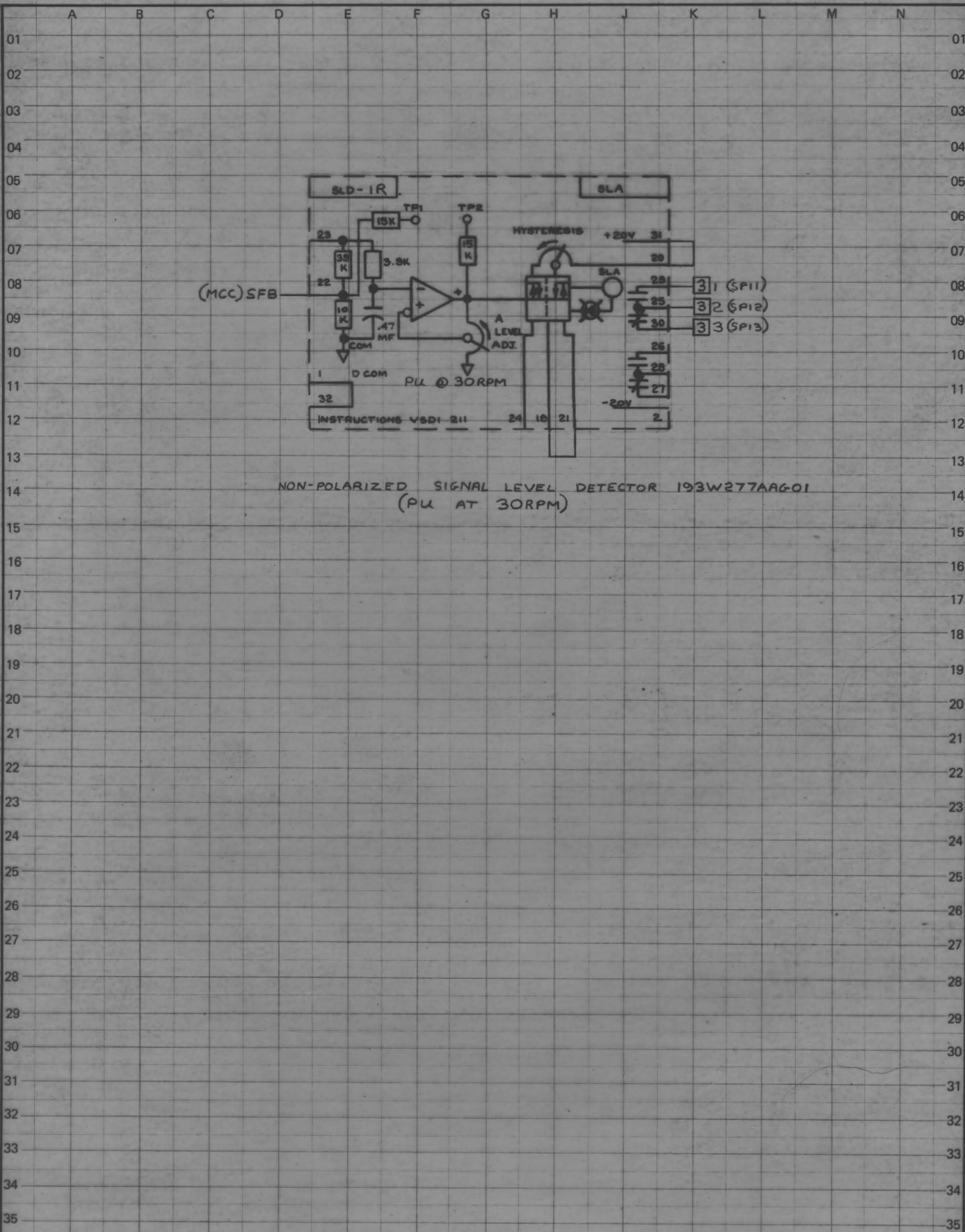
A			B			C			D			E			F			G			H			J			K			L			M			N						
TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE											WEBSTER + BENNETT LTD.									IDENT																
						3-7-79											75 HP BDL									DR SH																
						TECHN.																																				
						ENG.																																				
						APPD.																																				
																GO NUMBER									ELEMENTARY DIAGRAM									CONTD.								
																921N08									902M122BX									5								
																																		4								



DR SH CONTD. 5 6 902M122BX

TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	 Simplex VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.		WEBSTER & BENNETT LTD. 75HP BDC 3034R SPINDLE DRIVE, ELEMENTARY DIAGRAM.		IDENT DR SH	
				NGM	AP	3-7-79			GO NUMBER	ELEMENTARY DIAGRAM	CONTD.	
			2						921N08	902M122BX	6	5

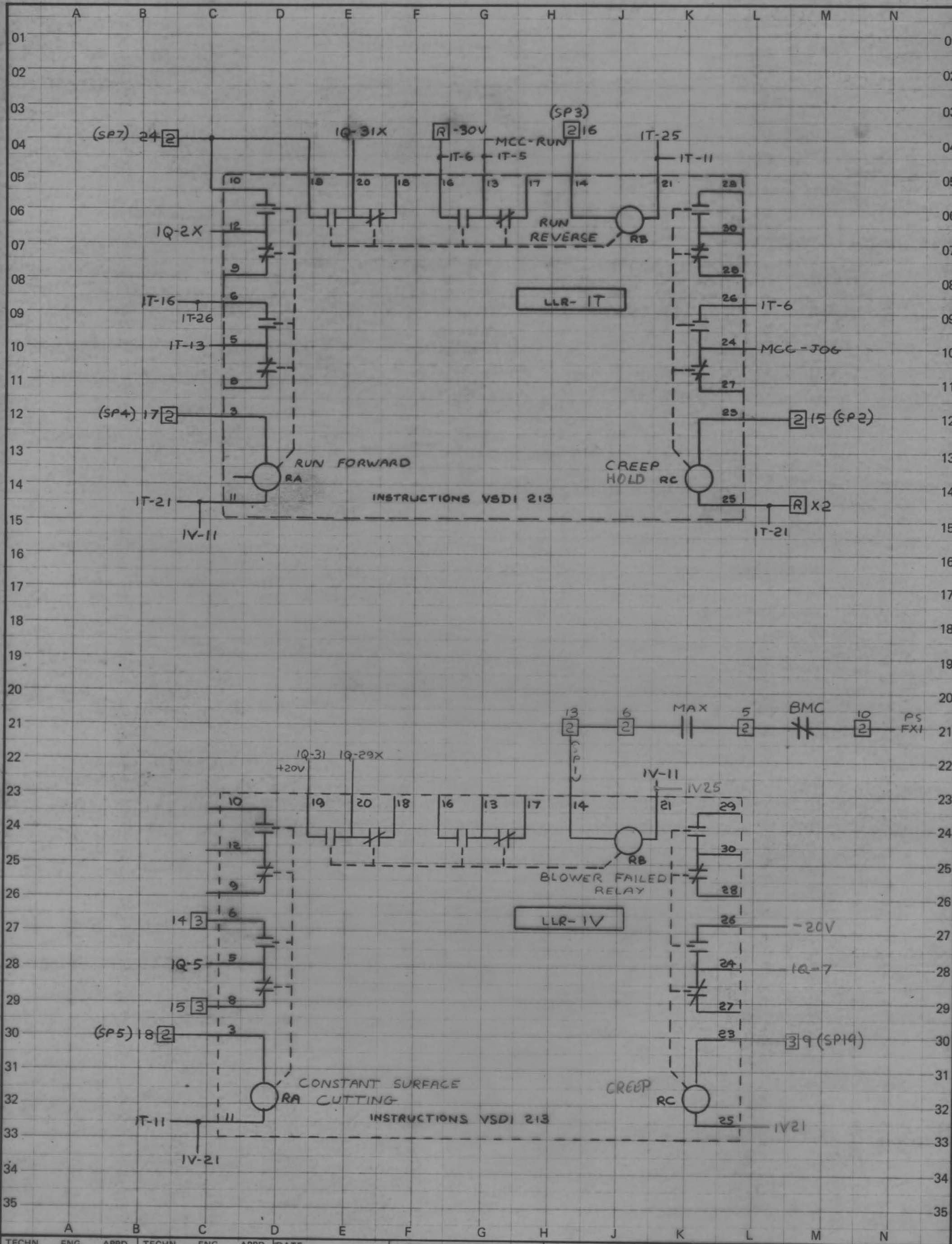
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DR SH CONTD. ELEMENTARY DIAGRAM 902M122BX

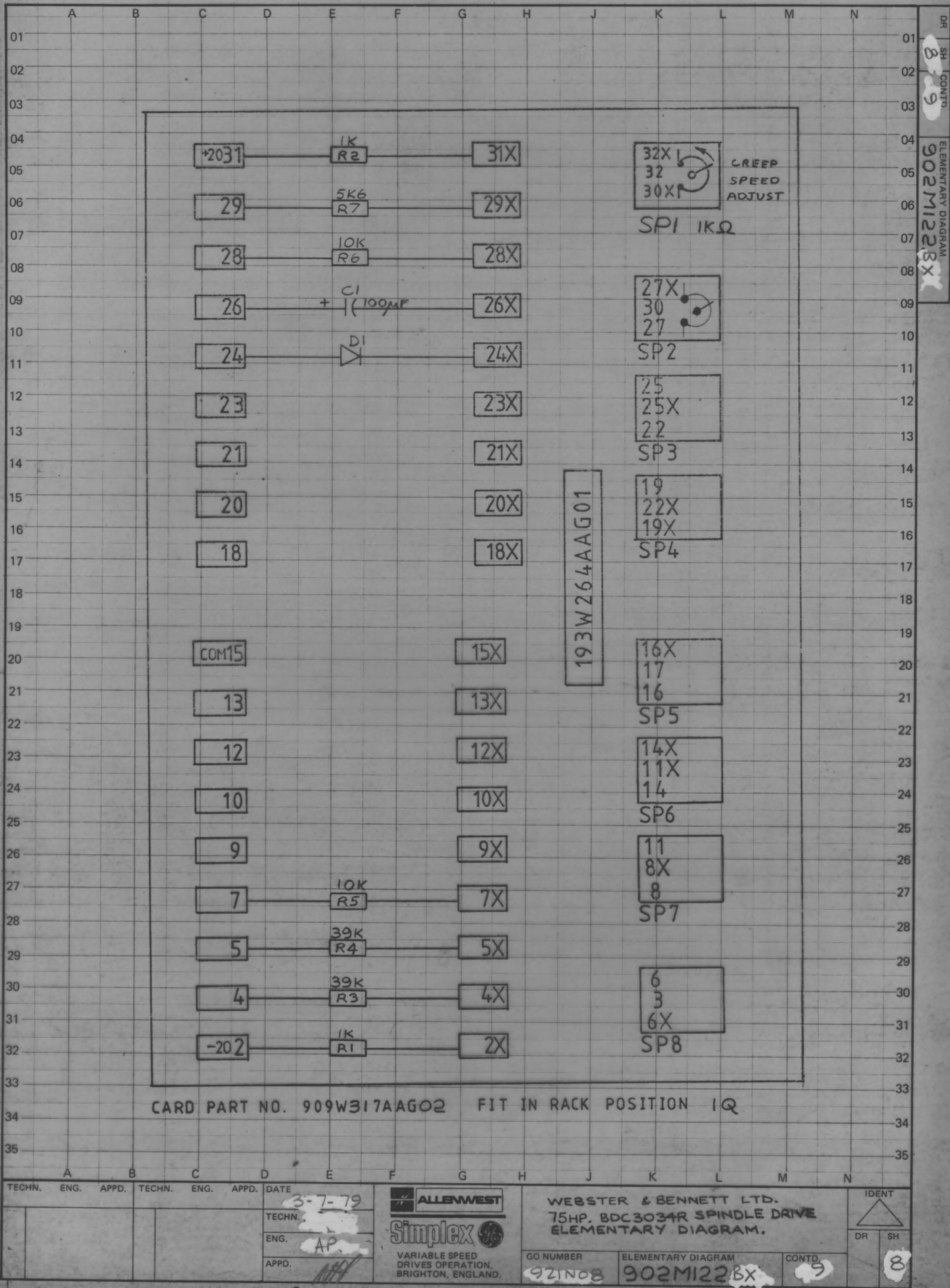
TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	3-7-79	 Simplex VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.	WEBSTER & BENNETT LTD. 75HP BDC3034R SPINDLE DRIVE ELEMENTARY DIAGRAM.		IDENT	 DR SH	
						TECHN.			GO NUMBER	921N08	ELEMENTARY DIAGRAM		902M122BX
						ENG.	AP		CONTD.	7			
						APPD.							

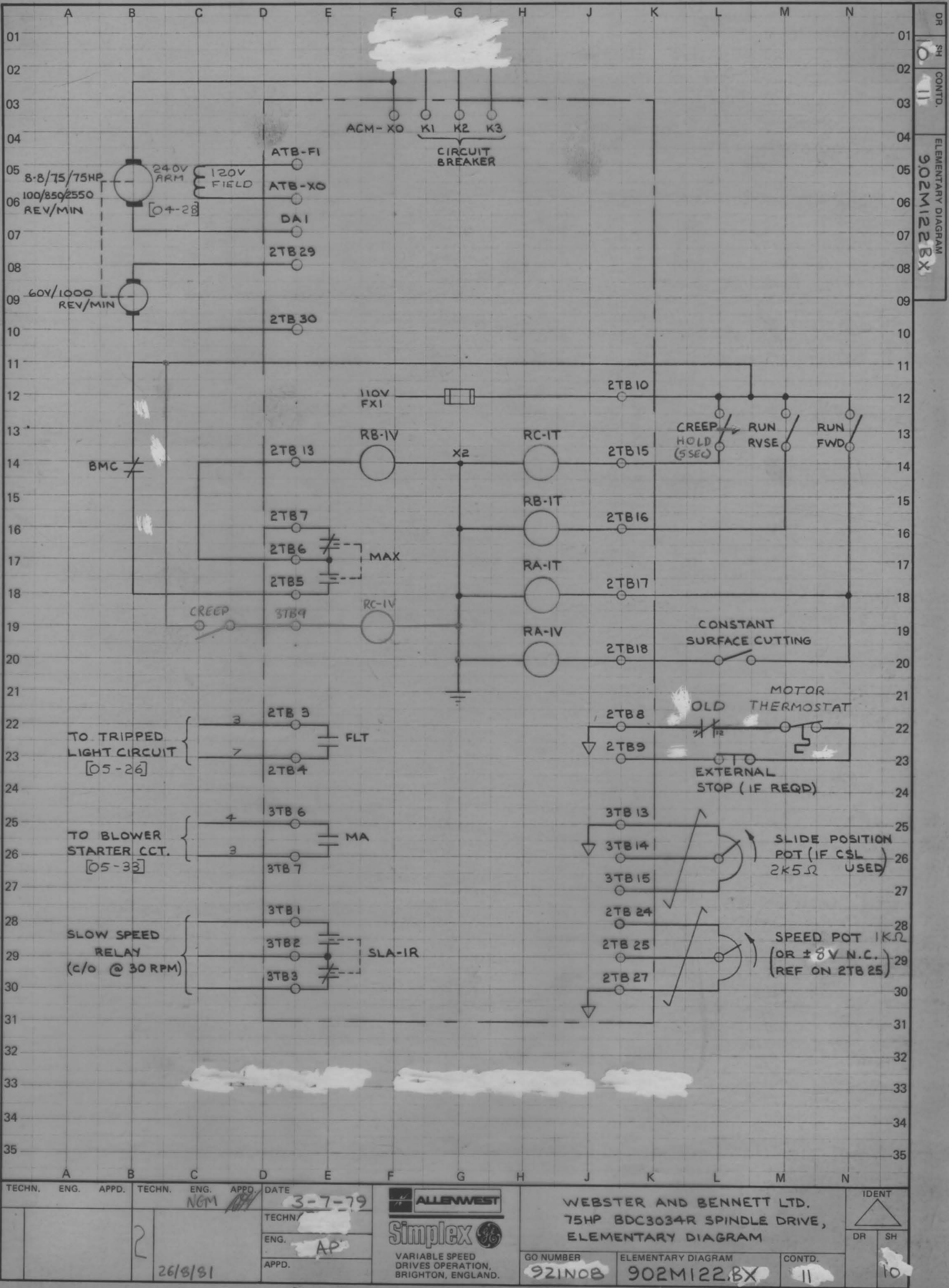
DR SH CONTD. ELEMENTARY DIAGRAM 902M122BX



TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	ALLENWEST Simplex VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.		WEBSTER & BENNETT LTD. 75HP BDC 3034R SPINDLE DRIVE ELEMENTARY DIAGRAM.		IDENT DR SH	
						3-7-79			GO NUMBER		ELEMENTARY DIAGRAM	
									921N08		902M122BX	
											CONTD.	
											7	

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VOLTAGE POLARITIES SHOWN ARE FOR MOTORING DA1(+)

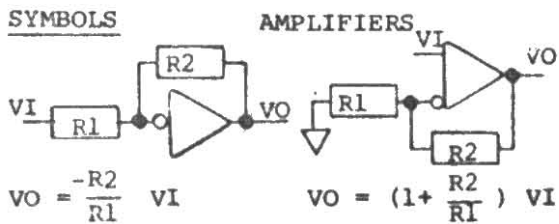
SIGNAL DEFINITIONS AND LOCATIONS

HARDWARE ABBREVIATIONS

MCC MAIN CONTROL CARD
 IFC INTERFACE CARD
 PSC POWER SUPPLY CARD
 SCR THYRISTOR ASSEMBLY
 DGC DIAGNOSTIC CARD
 MFC MOTOR FIELD CONTROL
 MFE MOTOR FIELD EXCITER
 MDR MODIFICATION RACK
 ACC AUXILIARY CONTROL CARD

SYMBOLS

AMPLIFIERS



CASE GROUND



VO = SIGN () X ABSOLUTE VALUE OF VI



STAB ON TERMINAL



TERMINAL AT 2TB, 3TB, 4TB, RTB.
 EX: 9 [2] - 2TB9; X2 [R] - RTBX2



TERMINAL AT T.B.'s



POTENTIOMETER ARROWS ON THE CARD
 ELEMENTARY DIAGRAMS INDICATE THE
 WIPER DIRECTION AS THE POTENTIOMETER
 SHAFT IS ROTATED CLOCKWISE TO INCREASE
 FUNCTION.



THESE RESISTORS ARE CRIMPED IN WIRE
 HARNESS.

FUNCTION	USE	LOC	JUMPERS
60HZ		MFC	ZA-ZB (IF USED)
50HZ	X	MCC	HZA - PHA
IOC-400%	X		(NONE)
-500%		IFC	I - IHI
-300%		IFC	I-ILO
SR5 - 9v	X		(NONE)
9 - 20v		MCC	SRH - COM
JOGR 10v			(NONE)
20v	X	MCC	JH - COM
LT.3-7sec	X		(NONE)
2 - 60sec			332Ω FROM LTI TO COM
VREG			NT-CEMF CC-COM
DC TACHO	X		(NONE)
AC TACHO		MCC	ATI - AT2
TACHO FILT		IFC	TC - TC
TACHO V.			
24-64vdc		IFC	NT-NT1 PT - PT1
27-71vac		IFC	NT-NT1 PT - PT1
60-160vdc		IFC	NT-NT2 PT - PT2
66-177vac		IFC	NT-NT2 PT - PT2
110-300vdc	X	IFC	NT-NT3 PT - PT3
120-300vac		IFC	NT-NT3 PT - PT3
G26		IFC	MFC OR MFE
1.8 1.7		ME	NONE
1.3 2.8		ME	YB - YD
2.4 5.0		ME	YA - YB
4.0 8.0		ME	YA-YB, YC-YD
7.0 13	X	ME	YA - YC
13 25		ME	YA-YC, YB-YD
L/R < .25S		MFC	QA - QB
INH RUN		DGC	D1-D2 (IF USED)
INH DRV CL		MCC	DC1 - COM
FUSELESS		ACC	CFY - CFX
20% DA	X	IFC	DLX - DCY
IFC CL	X	IFC	ILA DHI
FLD & DLY	X	FEA	DA13

* CEMF COUNTER EMF (16)
 * CFB CURRENT FEEDBACK (16)
 CMFA ABSOLUTE VALUE CEMF (08)
 CRM CROSSOVER MODIFY (11)
 DFP DELAYED FIRING POWER (25)
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 * VFB VOLTAGE FEEDBACK (19)
 * WFR WEAK FIELD REFERENCE (20)

(* - TEST POINT ON DOOR FRONT)

MAPPING SYSTEM

(NS/PS/TS) PS - PAST SHEET
 NS - NEXT SHEET
 TS - THIS SHEET

HENCE (PS - 12) DENOTES LOCATION ON PAST SHEET LINE 12. OTHER LOCATIONS ARE
 DENOTED BY SHEET NUMBER AND LINE? E.G. (1A16) SIGNIFIES LOCATION ON SHEET
 1A, LINE 16 ETC.

NOTE: FIELD EFFECT TRANSISTOR: THE
 CLOSED/OPEN (I/O) STATE OF THESE
 SWITCHED FOR "PRECONDITION" - "RUN"
 OR JOG" - "DIAGNOSTIC STATIC" -
 "DIAGNOSTIC RUN" IS SHOWN BY A
 FOUR DIGIT WORD WITH STATE SEQUENCE.

**SUPPLIED BY AW FYTED AND
 WIRED BY CUSTOMER**

TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE
						9.1.82

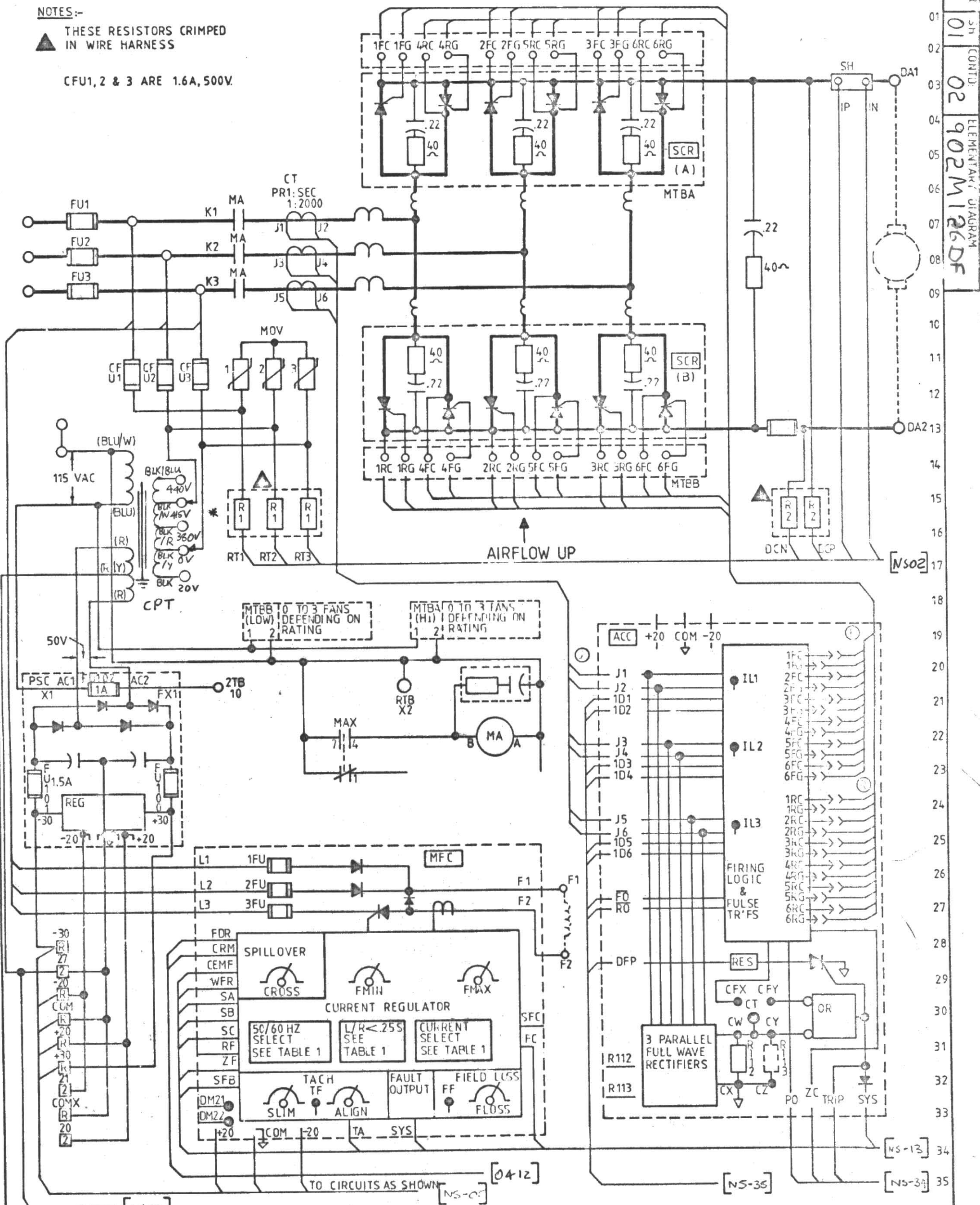


BDC 3064R 75 HP		IDENT
for WEBSTER + BENNETT LTD		DR SH
GO NUMBER	ELEMENTARY DIAGRAM	CONTO.
150N02	902M126DF	2

NOTES:-

▲ THESE RESISTORS CRIMPED
IN WIRE HARNESS

CFU1, 2 & 3 ARE 1.6A, 500V.



TECHN: ENG: APPD: TECHN: ENG: APPD: DATE: 9.2.82

TECHN:

ENG:

APPD:

ALLENWEST
VARIABLE SPEED
DRIVES OPERATION
BRIGHTON, ENGLAND.

BDC 3064R, 75 HP

WEBSTER & BENNETT

Q.D. NUMBER

150N02

ELEMENTARY DIAGRAM.

902M126DF

CONV:

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IDENT.

DR SH

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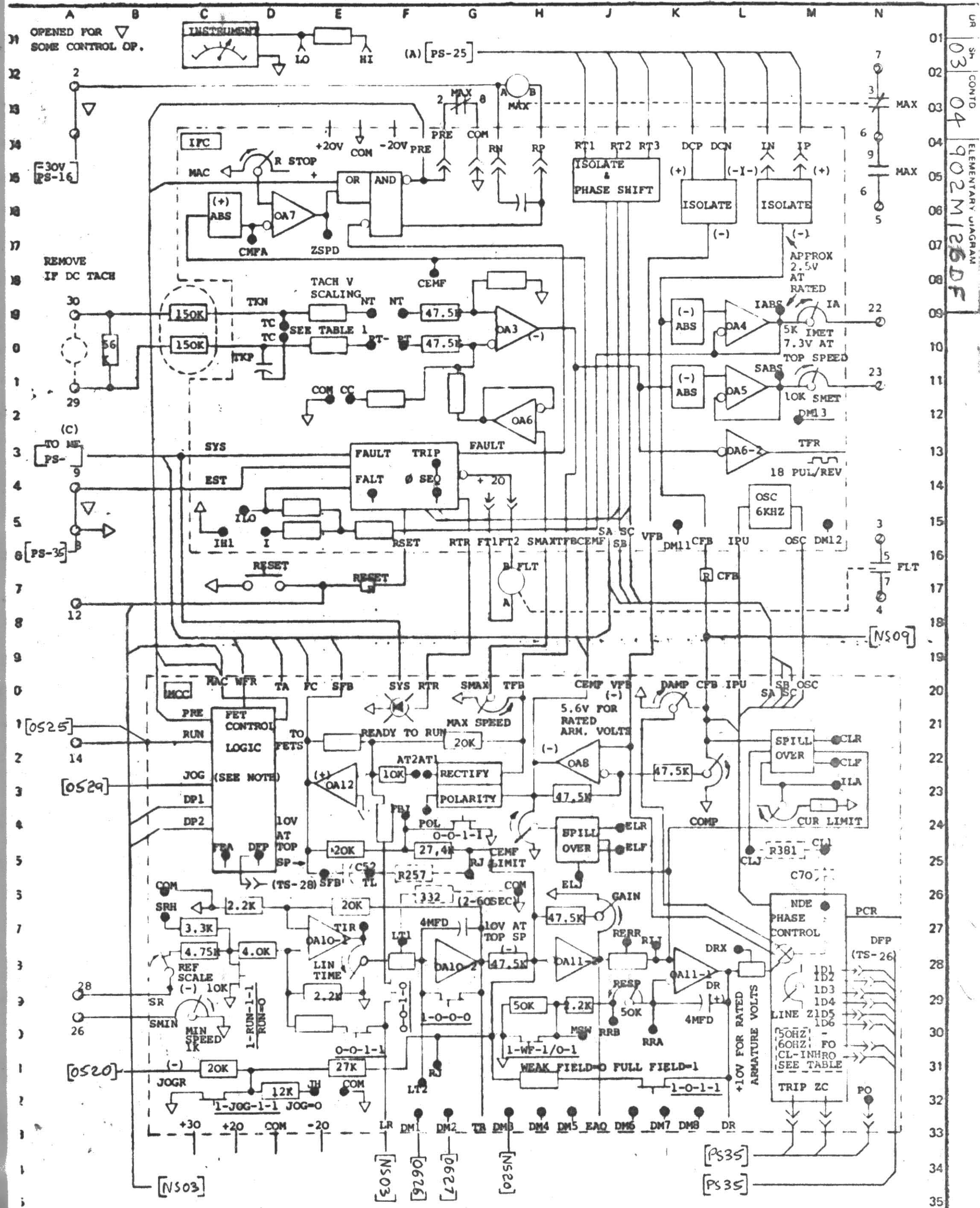
31

32

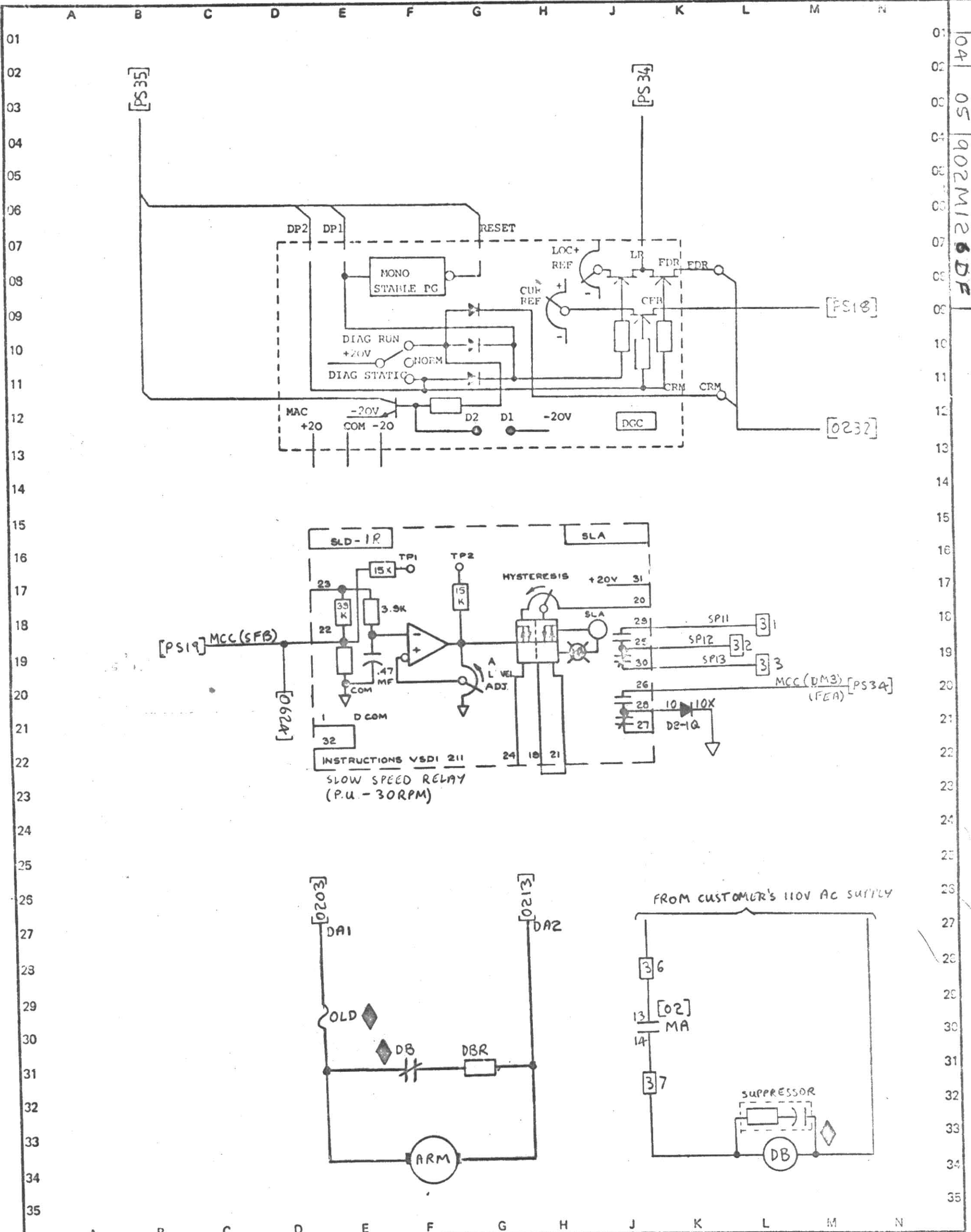
33

34

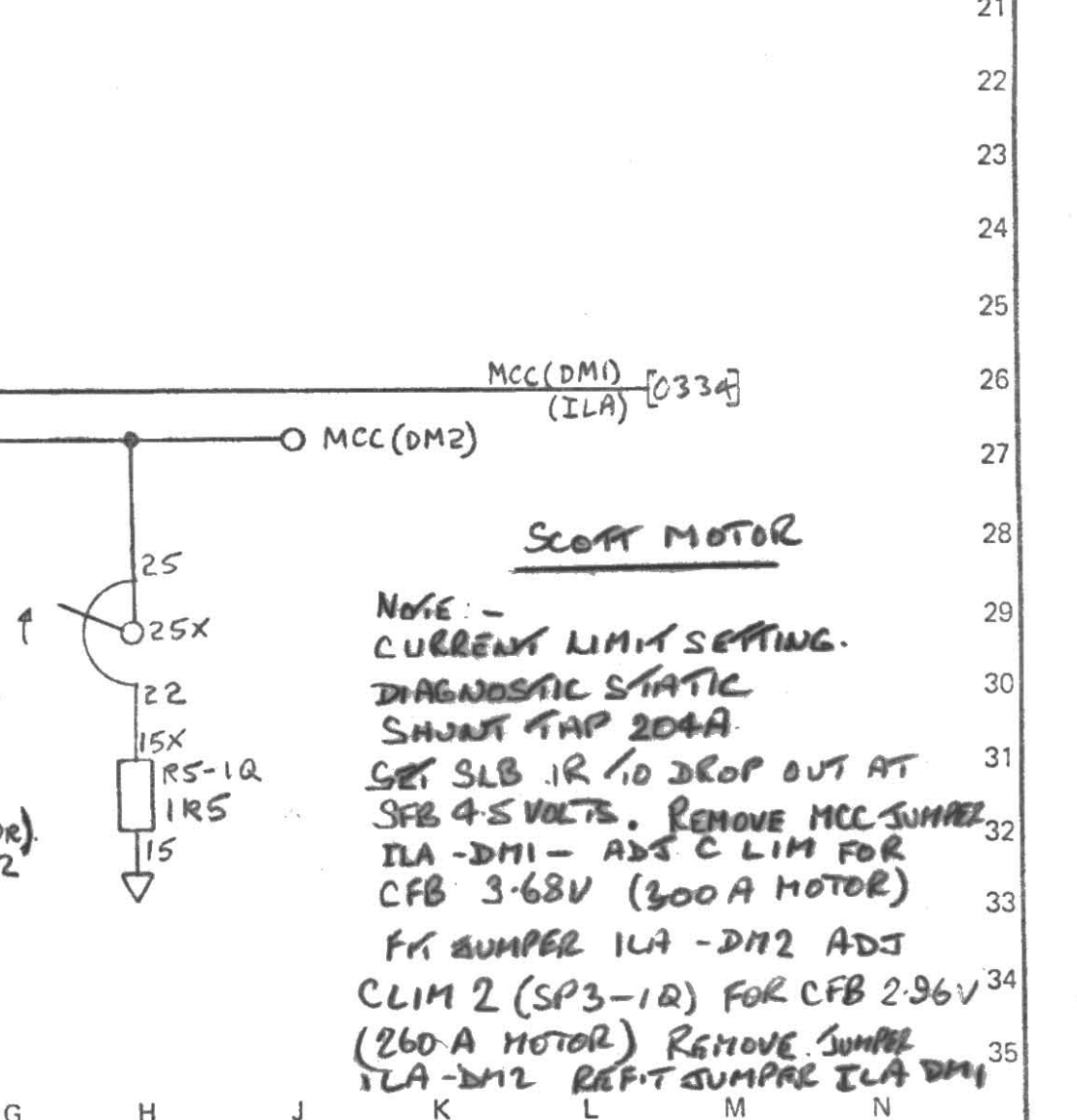
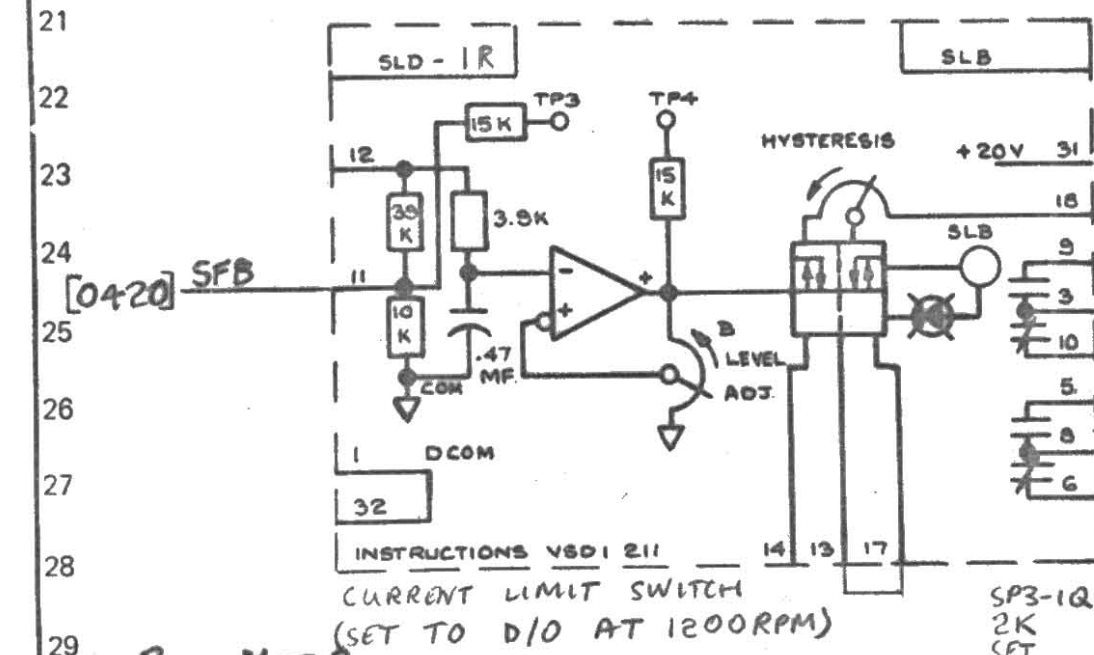
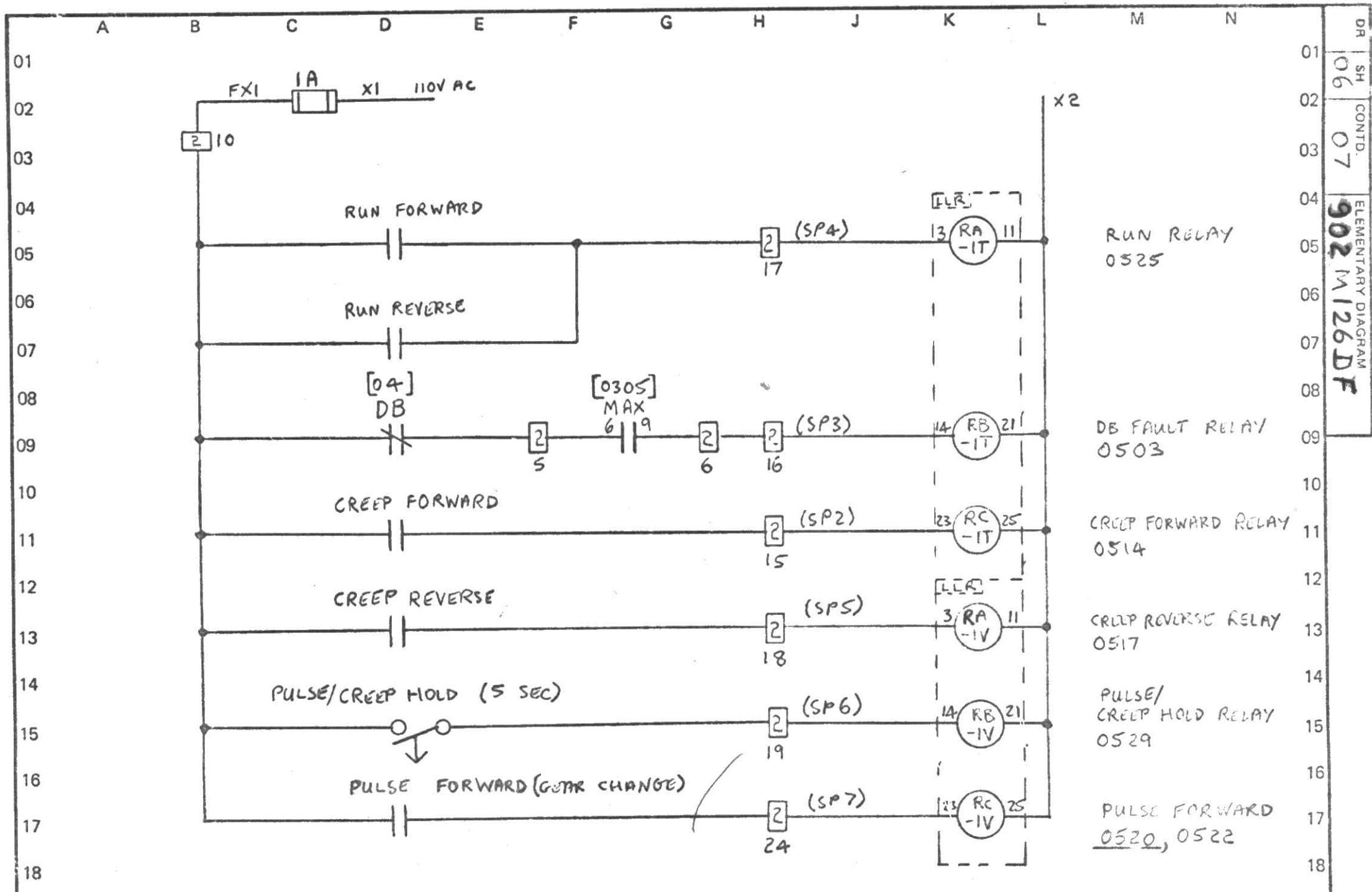
35



CHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	BDC 3064R, HP		WEBSTER & BENNETT		GO NUMBER		ELEMENTARY DIAGRAM		CONTD		IDENT	
						9/12/82					150N02		902M126DF		04		03	
TECHN.							VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND							DR				
ENG.														SH				
APPD.																		



TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE			BDC 306AR, 75 HP		
						9.2.82			WEBSTER & BENNETT		
									GON. MBLR 150N02		
									ELEMENTARY DIAGRAM 902M126DF		
									05		04



BULL MOTOR
 NOTE: CURRENT LIMIT SETTING (DIAGNOSTIC STATIC)
 SHUNT TAP 204A
 SET SLB-IR TO DROP OUT AT SFB=3.66V.
 REMOVE MCC JUMPER ILA-DM1. ADJUST 'CUR. LIMIT' (MCC) FOR CURRENT LIMIT AT CFB=3.31 (270 MOTOR).
 (SEE INSTRUCTION BOOK FOR PROCEDURE). FIT JUMPER ILA-DM2 AND ADJUST 'CUR. LIMIT 2' (SP3-1Q) FOR CURRENT LIMIT AT CFB=2.96 (240 AMP MOTOR).
 REMOVE JUMPER ILA-DM2.
 REFIT JUMPER ILA-DM1

SCOTT MOTOR
 NOTE: -
 CURRENT LIMIT SETTING.
 DIAGNOSTIC STATIC
 SHUNT TAP 204A
 SET SLB-IR TO DROP OUT AT SFB 4.5 VOLTS. REMOVE MCC JUMPER ILA-DM1 - ADJ C LIM FOR CFB 3.68V (300 A MOTOR)
 FIT JUMPER ILA-DM2 ADJ CLIM 2 (SP3-1Q) FOR CFB 2.96V (260 A MOTOR) REMOVE JUMPER ILA-DM2 REFIT JUMPER ILA-DM1

TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	9.2.82	ALLENWEST	BDC 3064R, 75 HP	IDENT	OR	SH
								Simplex	WEBSTER + BENNETT			
								VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.	GO NUMBER 150N02	ELEMENTARY DIAGRAM 902M126DF	CONTD. 07	06

[illegible]

ON PRINTED CIRCUIT CARDS USED IN THIS RACK THE LETTERS 'AA' AFTER BASIC CATALOGUE NUMBER INDICATES ORIGINAL DESIGN. SUBSEQUENT DESIGNS WITH THE SAME BASIC NUMBERS AND GROUP NUMBER WITH THE SECOND LETTER CHANGED, SUCH AS: AB, AC, AD, ETC., ARE DIRECTLY INTERCHANGEABLE AND MAY BE SUPPLIED IN PLACE OF THE 'AA' CARDS.

THE PRINTED CIRCUIT CARD SHOULD ALWAYS BE REMOVED WITH THE CARD EXTRACTOR WHICH IS ATTACHED ON TOP OF THE CARD RACK. SOME CARDS CONTAIN PARTS WHICH WILL BE THERMALLY HOT AFTER BEING IN OPERATION. CARE SHOULD BE EXERCISED IN HANDLING ALL CARDS AFTER REMOVAL UNTIL THESE PARTS HAVE COOLED. DO NOT REMOVE OR INSERT CARDS WITH POWER APPLIED.

FRONT VIEW OF 64 PIN
RECEPTACLE AS SEEN
IN RACK CLOSED
POSITION.

SYMBOLS :

TEST POST



POT ADJUSTMENT



INDICATING LIGHT

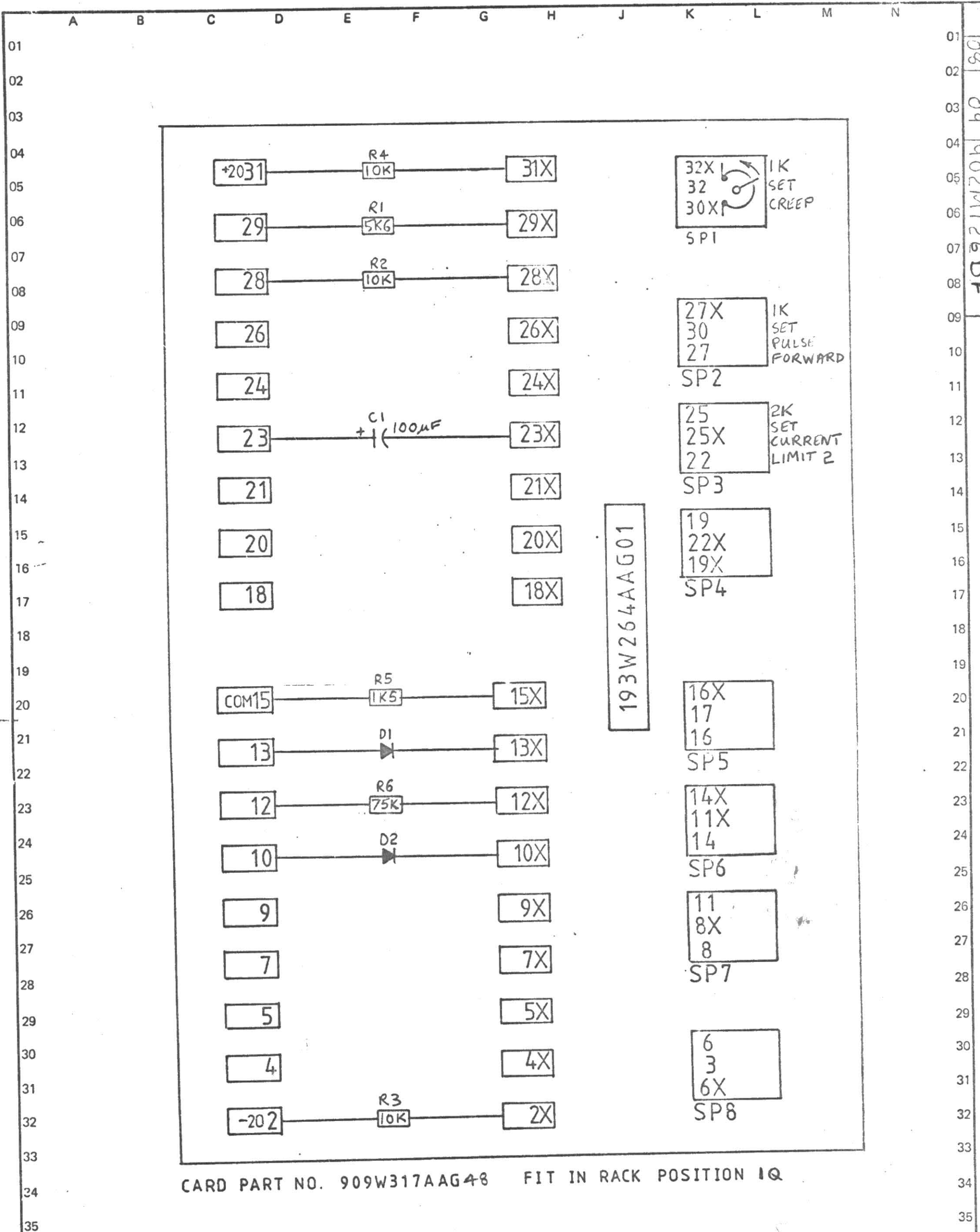
CARD RACK WIFE JUMPER TABLE

RTB(-20V)-1Q02	MCC(SFB)-1R22	1R22-1R11	
1Q02-1R02	1R19-1R21	1V25-1V21	SP2-1T23
RTB(COM)-1Q15	MCC(DM3)-1R26	1V21-1V11	SP3-1T14
1Q15-1R15	1R28-1Q10	1V11-1T11	SP4-1T03
RTB(+20V)-1Q31	1R31-1T13	1T11-1T21	SP5-1V03
1Q31-1R31	1T16-1Q29	1T21-1T25	SP6-1V14
RTB(-30V)-1T05	1Q29X-1Q28	1Q02-1Q12	SP7-1V23
1T05-1V13	1Q28-1Q23	1Q12X-1Q27X	2TB6-2TB16
RTB(X2)-1T25	1Q23-1Q13	1Q30-1V26	SP11-1R29
	1Q29X-1Q23X	1Q27-1Q23X	SP12-1R25
	1Q23X-1Q15	1V24-MCC(506R)	SP13-1R30
	1Q13X-MCC(SYS)	1R08-1Q25	
	1Q2X-1T26	1Q25-1Q25X	
	1Q31X-1V06	1Q22-1Q15X	
	1V05-1T24	1R13-1R17	MA13-3TB6
	1T24-1Q32X	1Q25X-MCC(DM2)	MA14-3TB7
	1Q30X-1Q28X	1Q10X-1Q27	
	1Q32-1V27		
	1T06-MCC(RUN)		
	1V16-MCC(506)		
	MCC(DM1)-1R05		

30/0-25
PVC
RED 31

NOTE: RECEPTACLE PINS MAY
BE NUMBERED AS SHOWN
IN EITHER SKETCH. (PIN
33 CORRESPONDS TO PIN
1X, 34 TO 2X, ETC.)

A			B			C			D			E			F			G			H			J			K			L			M			N			
TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE																																	
						9. 2. 82				ALISTWINT																													
						TECHN.				Simplex																													
						ENG.				VARIABLE SPEED																													
						APPD.				DRIVES OPERATION.																													
										BRIGHTON, ENGLAND.																													



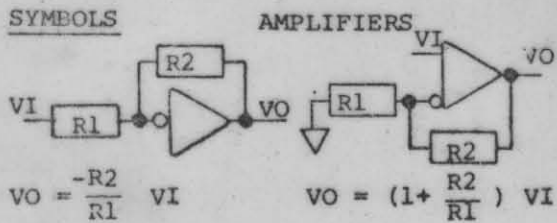
A		B		C		D		E		F		G		H		J		L		M		N	
TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	9.2.82				BDC 3064R, 75HP				WEBSTER & BENNETT		GO NUMBER		ELEMENTARY DIAGRAM		CONT'D.		
						2						150N02		902M126DF		09		08					

VOLTAGE POLARITIES SHOWN ARE FOR MOTORING DA1(+)

HARDWARE ABBREVIATIONS

MCC MAIN CONTROL CARD
 IFC INTERFACE CARD
 PSC POWER SUPPLY CARD
 SCR THYRISTOR ASSEMBLY
 DGC DIAGNOSTIC CARD
 MFC MOTOR FIELD CONTROL
 MFE MOTOR FIELD EXCITER
 MDR MODIFICATION RACK
 ACC AUXILIARY CONTROL CARD

SYMBOLS



CASE GROUND



VO = SIGN () X ABSOLUTE VALUE OF VI

STAB ON TERMINAL

TERMINAL AT 2TB, 3TB, 4TB, RTB.
EX: 9 [2] - 2TB9; X2 [2] - RTBX2

TERMINAL AT T.B.'s

POTENTIOMETER ARROWS ON THE CARD
ELEMENTARY DIAGRAMS INDICATE THE
WIPER DIRECTION AS THE POTENTIOMETER
SHAFT IS ROTATED CLOCKWISE TO INCREASE
FUNCTION.THESE RESISTORS ARE CRIMPED IN WIRE
HARNESS.

FUNCTION	USE	LOC	JUMPERS
60HZ		MFC	ZA-ZB (IF USED)
50HZ		MCC	HZA - PHA
I/O-400%	X		(NONE)
-500%		IFC	I - IHI
-300%		IFC	I-ILO
SR5 - 9v	X		(NONE)
9 - 20v		MCC	SRH - COM
JOG 10v			(NONE)
20v	X	MCC	JH - COM
LT. 3-7sec			(NONE)
2 - 60sec	X		332Ω FROM LTI TO COM
VREG			NT-CEMF CC-COM
DC TACHO	X		(NONE)
AC TACHO		MCC	AT1 - AT2
TACHO FILT		IFC	TC - TC
TACHO V.			
24-64vdc		IFC	NT-NT1 PT - PT1
27-71vac		IFC	NT-NT1 PT - PT1
60-160vdc		IFC	NT-NT2 PT - PT2
66-177vac		IFC	NT-NT2 PT - PT2
110-300vdc	X	IFC	NT-NT3 PT - PT3
120-300vac		IFC	NT-NT3 PT - PT3
G134 G256		IFC	MFC OR MFE
1.7		ME	NONE
1.3		ME	YB - YD
2.4		ME	YA - YB
4.0		ME	YA-YB, YC-YD
7.0		ME	YA - YC
13		ME	YA-YC, YB-YD
13		ME	YA-YC, YB-YD
L/R < .25S		MFC	QA - QB
INH RUN		DGC	D1-D2 (IF USED)
INH DRV CL		MCC	DC1 - COM
FUSELESS		ACC	CFY - CFX
175/200 DCL		MCC	DCX DCY

SEE NOTE SH9

SIGNAL DEFINITIONS AND LOCATIONS

* CEMF COUNTER EMF (3 16)
 * CFB CURRENT FEEDBACK (3 16)
 CMFA ABSOLUTE VALUE CEMF (3 08)
 CRM CROSSOVER MODIFY (4 11)
 DFP DELAYED FIRING POWER (3 25)
 * DR DRIVER REFERENCE (3 33)
 * EAO ERROR AMP OUTPUT (3 33)
 EST EXTERNAL FLT STOP INPUT (3 14)
 FALT FAULT (3 14)
 * FC FIELD CURRENT (NS26)
 FDR FIELD DIAGNOSTIC REFERENCE (3 08)
 FEA FIELD ECONOMY ADJUST (3 25)
 FF FIELD FAULT (3 28)
 IABS MOTOR CURRENT ABSOLUTE (3 09)
 IIA CURRENT LIMIT ADJUST (3 23)
 ILET CURRENT SIGNAL FOR METER (3 10)
 * IPU INITIAL PULSE (3 20)
 * LR LOCAL REF. FROM DGC (3 33)
 * JOG JOG SWITCH INPUT (3 23)
 * JOGR JOG REFERENCE INPUT (3 31)
 * MAC MAX/MA CONTROL SIGNAL (3 20)
 MSW MODE SWITCH (3 30)
 * OSC OSCILLATOR (3 17)
 * PCR PHASE CONTROL REF. (3 26)
 * PRE DRIVE PRECONDITION (3 21)
 ØSEQ PHASE SEQUENCE (3 14)
 RERR REGULATOR ERROR (3 27)
 RIJ INTEGRATOR SUMMING JUNCTION (3 27)
 RJ REGULATOR SUMMING JUNCTION (3 31)
 RRA REGULATOR RESPONSE ADJUST (3 30)
 RSET RESET (3 16)
 * RTR READY TO RUN (3 16)
 * RUN RUN SWITCH INPUT (3 21)
 * SA-C PHASE SYN OUTPUT (3 16)
 * SFB SPEED FEEDBACK (3 20)
 SMET SPEED SIGNAL FOR METER (3 12)
 * SR SYSTEM REFERENCE INPUT (3 29)
 * SYS SYSTEM FAULT TRIP (3 13)
 * TA OUTPUT FOR TACHO TRIP ADJUST (2 20)
 TF TACHO FAULT (NS28)
 * TFB TACHOMETER FEEDBACK (3 20)
 TFR AC TACHO FREQUENCY OUTPUT (3 13)
 * TR TIMED REFERENCE (3 33)
 * VFB VOLTAGE FEEDBACK (3 19)
 * WFR WEAK FIELD REFERENCE (3 20)

(* - TEST POINT ON DOOR FRONT)

MAPPING SYSTEM

(NS/PS/TS) PS - PAST SHEET
 NS - NEXT SHEET
 TS - THIS SHEET

HENCE (PS - 12) DENOTES LOCATION ON PAST SHEET LINE 12. OTHER LOCATIONS ARE
 DENOTED BY SHEET NUMBER AND LINE? E.G. (1A16) SIGNIFIES LOCATION ON SHEET
 1A, LINE 16 ETC.

NOTE: FIELD EFFECT TRANSISTOR: THE
 CLOSED/OPEN (I/O) STATE OF THESE
 SWITCHED FOR "PRECONDITION" - "RUN"
 OR JOG - "DIAGNOSTIC STATIC" -
 "DIAGNOSTIC RUN" IS SHOWN BY A
 FOUR DIGIT WORD WITH STATE SEQUENCE.

TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	12.1.83	 VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.	BDC 3064R 100HP			IDENT	
							TECHN.		WEBSTER + BENNETT			DR SH	
							ENG.						
							APPD.						
							GO NUMBER		ELEMENTARY DIAGRAM		CONTD.		
							248N01		902M126XA		2		

▲ THESE RESISTORS CRIMPED
IN WIRE HARNESS

DR SH CONTO: 3 ELEMENTAR DIAGRAM 902M126X4

NOTES:-
▲ THESE RESISTORS CRIMPED IN WIRE HARNESS
CFU1, 2 & 3 ARE 1.6A, 500V.

280A
CT
PR1: SEC 1:2000

300A
FU1
FU2
FU3

115 VAC
CPT

50V
PSC AC1
AC2
REG
F1
F2

1FU
2FU
3FU

SPILLOVER
CROSS
FMIN
FMAX
CURRENT REGULATOR
50/60 HZ SELECT
L/R < .25S
CURRENT SELECT
TACH
FAULT OUTPUT
FIELD LOSS
SLIM
ALIGN
FF
FLOSS

TO CIRCUITS AS SHOWN

1FC 1FG 4RC 4RG 2FC 2FG 5RC 5RG 3FC 3FG 6RC 6RG
1RC 1RG 4FC 4FG 2RC 2RG 5FC 5FG 3RC 3RG 6FC 6FG

SCR (A)
SCR (B)

239A TAP
SH
IP
IN
DA1
DA2

22
40
300A
R2
R2
DCN
DCP

1FC 1FG 2FC 2FG 3FC 3FG 4FC 4FG 5FC 5FG 6FC 6FG
1RC 1RG 2RC 2RG 3RC 3RG 4RC 4RG 5RC 5RG 6RC 6RG

IL1
IL2
IL3
FIRING LOGIC & PULSE TR'FS
RES
CFX
CFY
CT
CW
CY
CX
CZ
R112
R113
3 PARALLEL FULL WAVE RECTIFIERS
PO
ZC
TRIP
SYS

NS-16
NS-05
NS-35
NS-34

			TECHN: <i>YRL</i>
			ENG: <i>YRL</i>
			APPD:

ALLENWEST
VARIABLE SPEED
DRIVES OPERATION
BRIGHTON, ENGLAND.

BDC 3064R 100 HP

G.O. NUMBER

ELEMENTARY DIAGRAM.

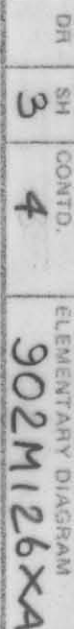
CONTD:

IDENT.

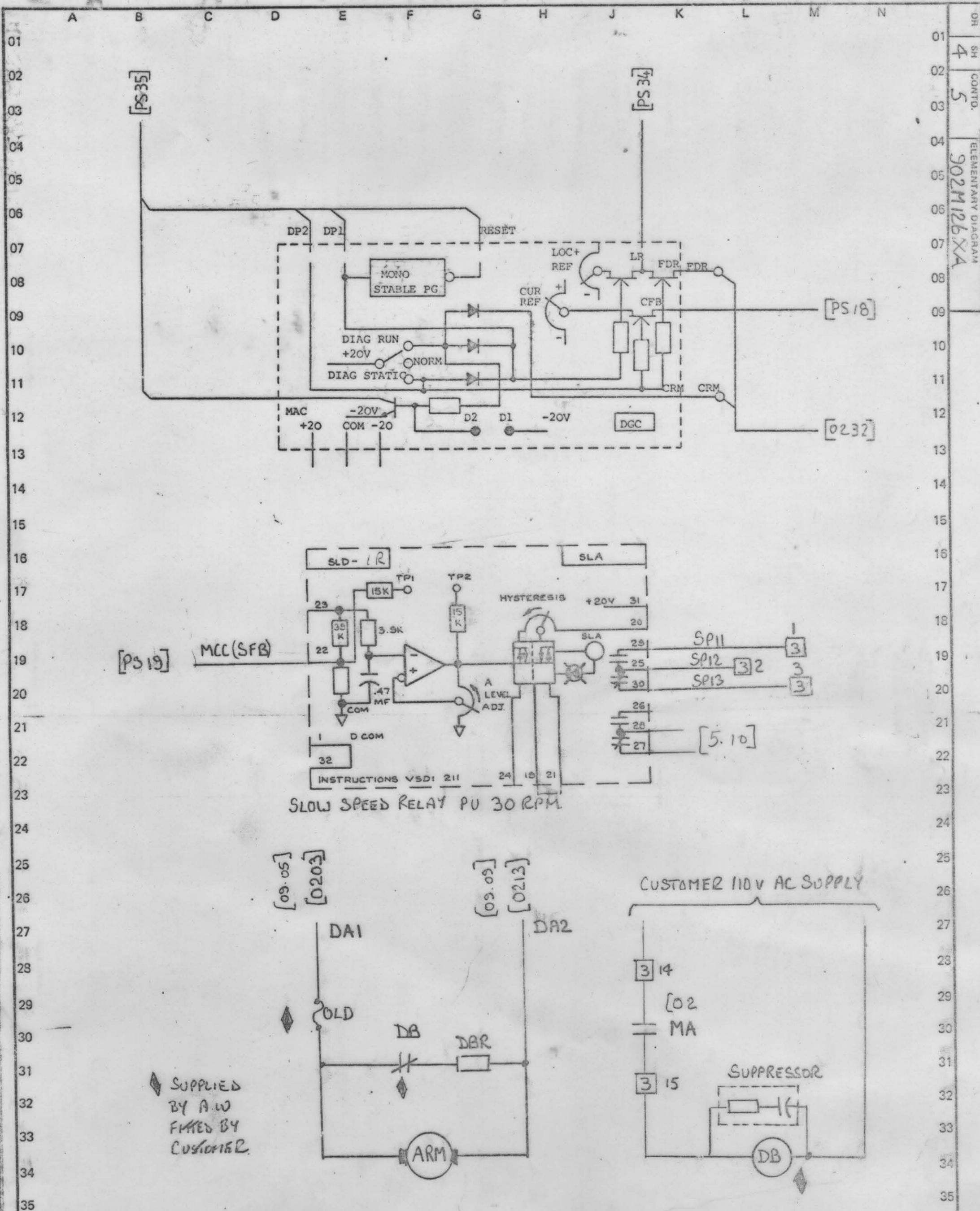
DR

SH

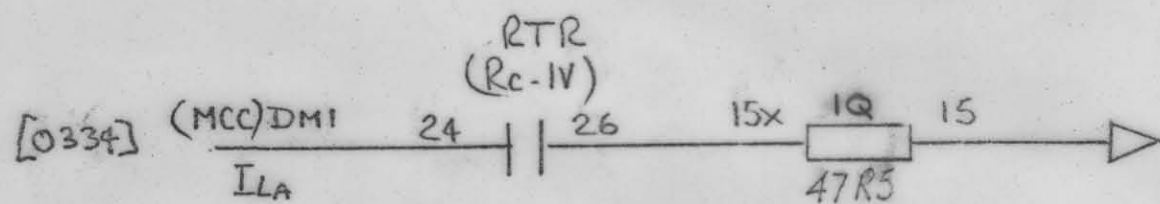
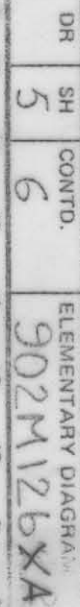
2



Disclaimer Statement: The trade mark is the trade mark of General Electric Company of U.S.A., which is not connected with the English Company of a similar name.

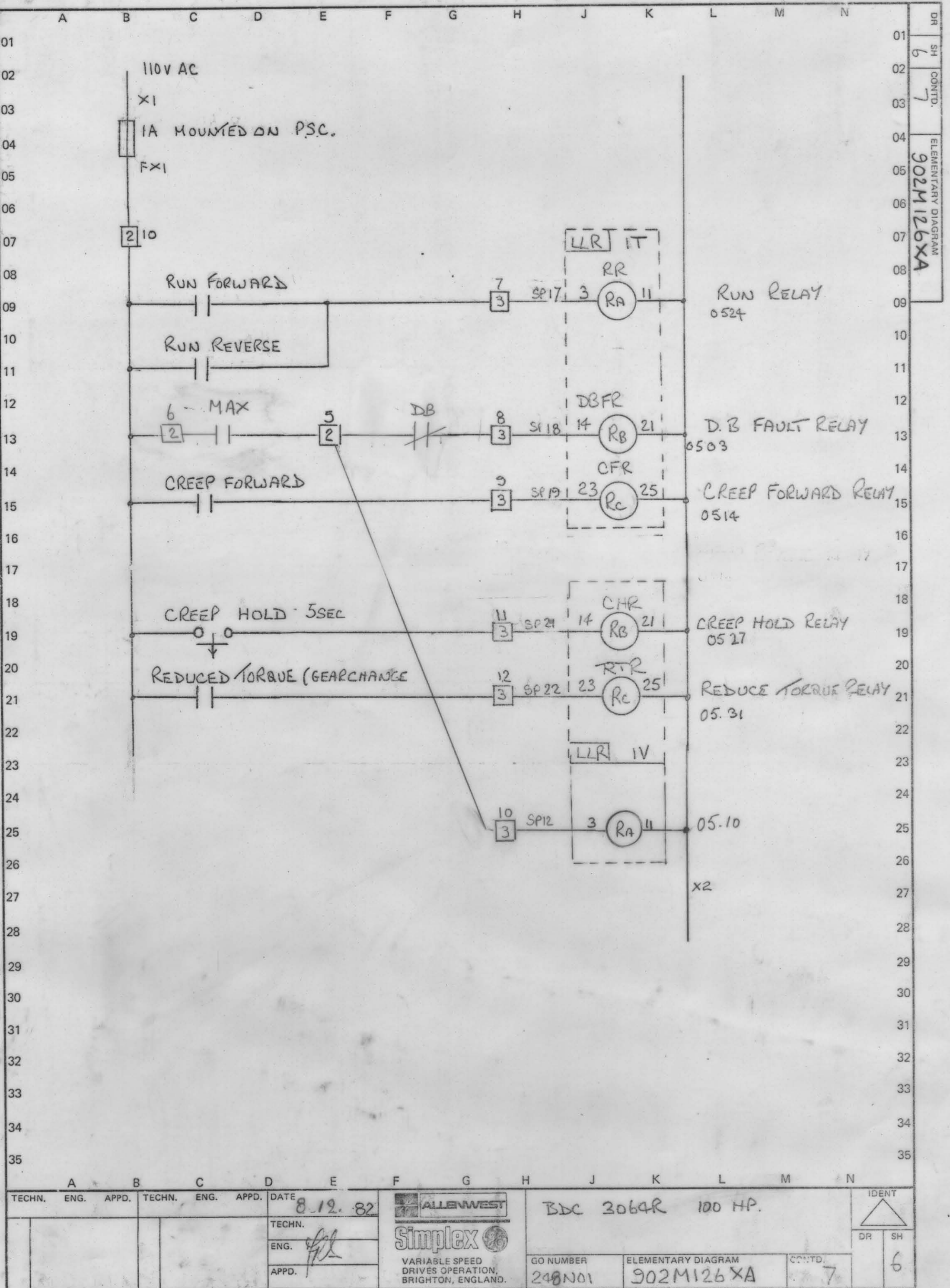


TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	TECHN.	ENG.	APPD.	CO NUMBER	ELEMENTARY DIAGRAM	CONTO.	IDENT
						8.12.82				248N01	902M126XA	5	4
										BDC 3064R 100HP			
							VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.			4			



REDUCED CURRENT LIMIT
APPROX 10% WHEN NORMAL CURRENT
LIMIT SET TO 150%.

A	B	C	D	E	F	G	H	J	K	L	M	N				
TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	86.12.82					BDC 3064 R 100 H?		IDENT 		
						TECHN.								DR SH		
						ENG.	AL	VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.				GO NUMBER 248NDI		ELEMENTARY DIAGRAM 902M126XA	CONTD. G	5
						APPD.										



DR SH CONTD. ELEMENTARY DIAGRAM 902M126XA

TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	8.12.82	ALLENWEST	BDC 3064R 100 HP.	IDENT	DR	SH
						TECHN.		Simplex				
						ENG.		VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.	GO NUMBER 248N01	ELEMENTARY DIAGRAM 902M126XA	CONTD. 7	6
						APPD.						

01	02	03	04	05	06	07	08	09
DR	SH	CONTO.	ELEMENTARY DIAGRAM					
	8	9	902M126XA					

THE PRINTED CIRCUIT CARD SHOULD ALWAYS BE REMOVED WITH THE CARD EXTRACTOR WHICH IS ATTACHED ON TOP OF THE CARD RACK. SOME CARDS CONTAIN PARTS WHICH WILL BE THERMALLY HOT AFTER BEING IN OPERATION. CARE SHOULD BE EXERCISED IN HANDLING ALL CARDS AFTER REMOVAL UNTIL THESE PARTS HAVE COOLED. DO NOT REMOVE OR INSERT CARDS WITH POWER APPLIED.

SYMBOLS:

TEST POST



POT ADJUSTMENT



INDICATING LIGHT

CARD RACK WIRE JUMPER TABLE

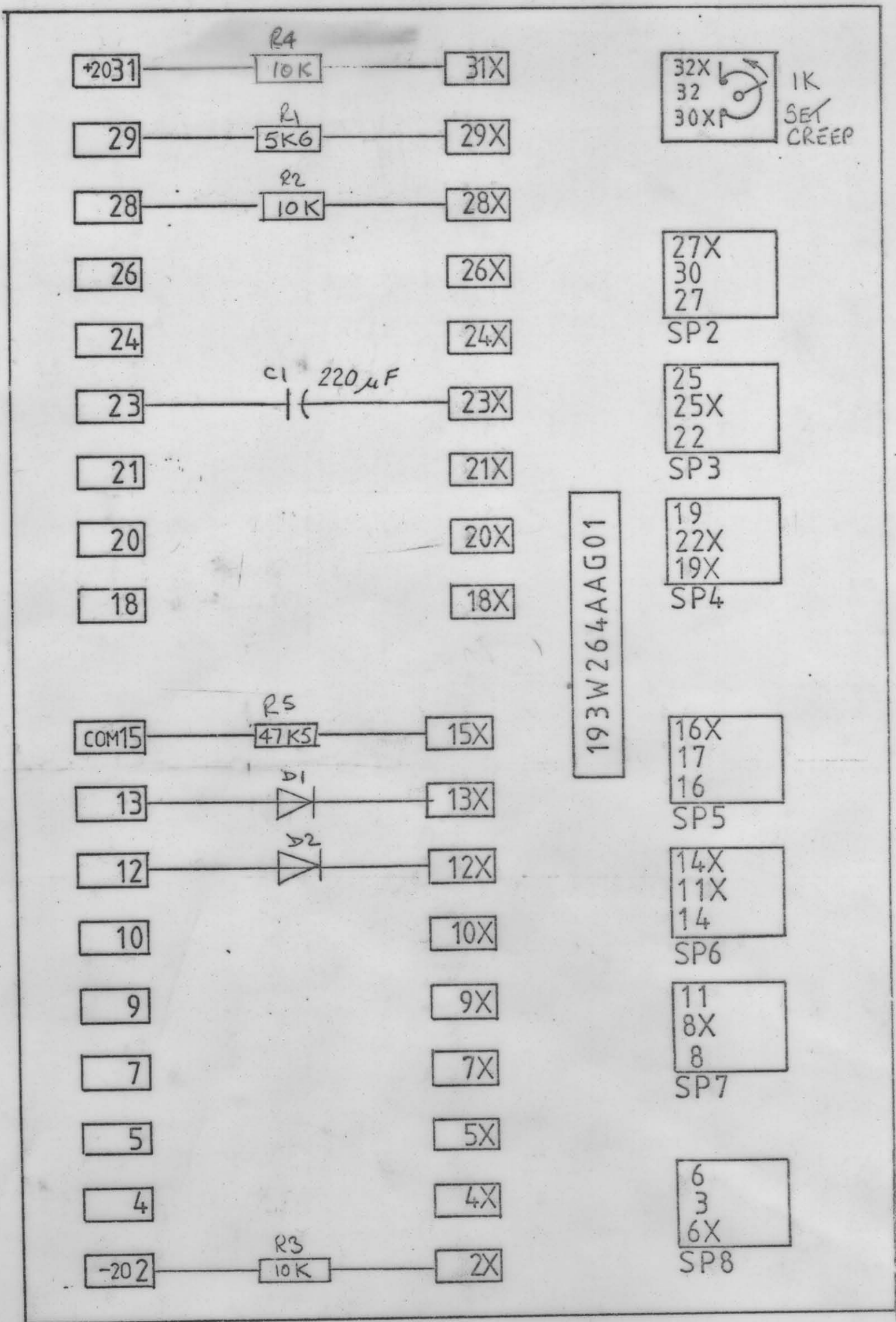
CARD RACK WIRE JUMPER TABLE			
RTB-20 - 1Q02	MCC(SFB) - 1R22	1V26 - 1Q15X	
1Q02 - 1R02	1R19 - 1R21	1V25 - 1V21	SP11 - 1R29
	MCC(DM2) - 1Q12	1V21 - 1V11	SP12 - 1R25
RTB COM - 1Q15	1Q12X - 1Q23X	1V11 - 1T11	SP13 1R30
1Q15 - 1R15	1R31 - 1T13	1T11 - 1T21	SP14
	1T16 - 1Q29	1T21 - 1T25	SP15
RTB+20 - 1Q31	1Q29X - 1Q28		SP16
1Q31 - 1R31	1Q28 - 1Q23		SP17 - 1T 3
	1Q23 - 1Q13		SP18 - 1T 14
RTB-30 - 1T05	1Q28X - 1Q23X		SP19 - 1T 23
1T05 - 1V13	1Q23X - 1Q15		SP20 - 1V 3
RTBx2 - 1R25	1Q13X - MCC SYL		SP21 - 1V 14
	1Q2X - 1T26		SP22 - 1V 23
	1Q1X - 1V05		SP23
	1T24		2TB5 - 3TB10
IR27 - RF(MFC)	1T24 - 1Q32X		2TB10 - 2TB6
IR28 - 1V12	1Q30X - 1Q28X		
IV9 - 1R31	1Q32 - MCC(SOG)		
ILA DM1 MCC	1T06 - MCC(RUN)		
FEA - DM2 MCC	1V16 - MCC(SOG)		MA13 - 3TB14
	MCC DM1 - 1V14		MA14 3TB15

NOTE: RECEPTACLE PINS MAY
BE NUMBERED AS SHOWN
IN EITHER SKETCH. (PIN
33 CORRESPONDS TO PIN
1X, 34 TO 2X, ETC.)

	A	B	C	D	E	F	G	H	J	K	L	M	N	
TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE.	12.1.83	ALLENWEST	BDC 3064R 110 MP.					IDENT △
	2	IR27-RF(MFL) IR28-IV12 IV9-IR31 ADDED. "AS SHIPPED" 26-1-83	TECHN.	F/S		Simplesx	VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.	GO NUMBER	ELEMENTARY DIAGRAM	CONTD.	7			
			ENG.	F/S				248NO1	902M126XA	8				
			APPD.	M										

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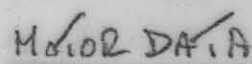
DR SH
7
CONTD.
8
ELEMENTARY DIAGRAM
902M126XA



CARD PART NO. 909W317ABG10 FIT IN RACK POSITION 1Q

A		B		C		D		E		F		G		H		J		K		L		M		N	
TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE																	IDENT		
						8.12.82																	DR SH		
							Allenwest																		
							Simplex																		
							VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.																		
							GO NUMBER 248N01																ELEMENTARY DIAGRAM 902M126XA		
																							CONTD. 9		
																							8		

CONNECT CONTROL V.
PRIMARY FOR
CORRECT VOLTAGE



HP
VA
In
RPM

$$\frac{V_F}{I_F}$$

A			B			C			D			E			F			G			H			J			K			L			M			N															
TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE																																													
						2.12.82													BDC 3064R 100 HP									IDENT																							
						TECHN.																						DR			SH																				
						ENG.																																		9											
						APPD.																																													
						VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.						GO NUMBER 248N01						ELEMENTARY DIAGRAM 902M126XA						SCNTD. 10																											

A B C D E F G H J K L M N

DR
SH
CONTD.
2
ELEMENTARY DIAGRAM
902M128DA

VOLTAGE POLARITIES SHOWN ARE FOR MOTORING DAI(+)

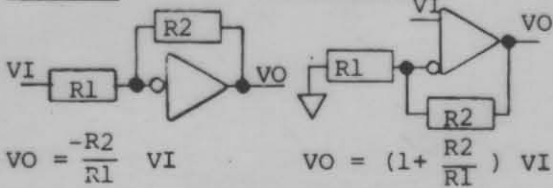
SIGNAL DEFINITIONS AND LOCATIONS

HARDWARE ABBREVIATIONS

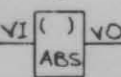
MCC MAIN CONTROL CARD
 IFC INTERFACE CARD
 PSC POWER SUPPLY CARD
 SCR THYRISTOR ASSEMBLY
 DGC DIAGNOSTIC CARD
 MFC MOTOR FIELD CONTROL
 MFE MOTOR FIELD EXCITER
 MDR MODIFICATION RACK
 ACC AUXILIARY CONTROL CARD

SYMBOLS

AMPLIFIERS



CASE GROUND



VO = SIGN () X ABSOLUTE VALUE OF VI

STAB ON TERMINAL

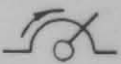


TERMINAL AT 2TB, 3TB, 4TB, RTB.

EX: 9 [2] - 2TB9; X2 [R] - RTBX2



TERMINAL AT T.B.'s



POTENTIOMETER ARROWS ON THE CARD
 ELEMENTARY DIAGRAMS INDICATE THE
 WIPER DIRECTION AS THE POTENTIOMETER
 SHAFT IS ROTATED CLOCKWISE TO INCREASE
 FUNCTION.



THESE RESISTORS ARE CRIMPED IN WIRE
 HARNESS.

FUNCTION	USE	LOC	JUMPERS
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-500%		IFC	I - IHI
-300%		IFC	I-ILO
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9 - 20v		MCC	SRH - COM
JOGR 10v			(NONE)
20v	X	MCC	JH - COM
LT. 3-7sec.			(NONE)
2 - 60sec	X		332Ω FROM LTI TO COM
VREG			NT-CEMF CC-COM
DC TACHO	X		(NONE)
AC TACHO		MCC	AT1 - AT2
TACHO FILT		IFC	TC - TC
TACHO V.		IFC	NT-NT1 PT - PT1
24-64vdc		IFC	NT-NT1 PT - PT1
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G134 G256		IFC	MFC OR MFE
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2.4 5.0		ME	YA - YB
4.0 8.0		ME	YA-YB, YC-YD
7.0 13	X	ME	YA - YC
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L/R < .25S		MFC	QA - QB
INH RUN		DGC	D1-D2 (IF USED)
INH DRV CL		MCC	DC1 - COM
FUSELESS		ACC	CFY - CFX
200% D.C.L	X	MCC	DCX - DCY
REC CL	X	MCC	ILA DM1
FLD EC DLY	X		FEA DM3

* CEMF COUNTER EMF (16)
 * CFB CURRENT FEEDBACK (16)
 CMFA ABSOLUTE VALUE CEMF (08)
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(* - TEST POINT ON DOOR FRONT)

MAPPING SYSTEM

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 TS - THIS SHEET

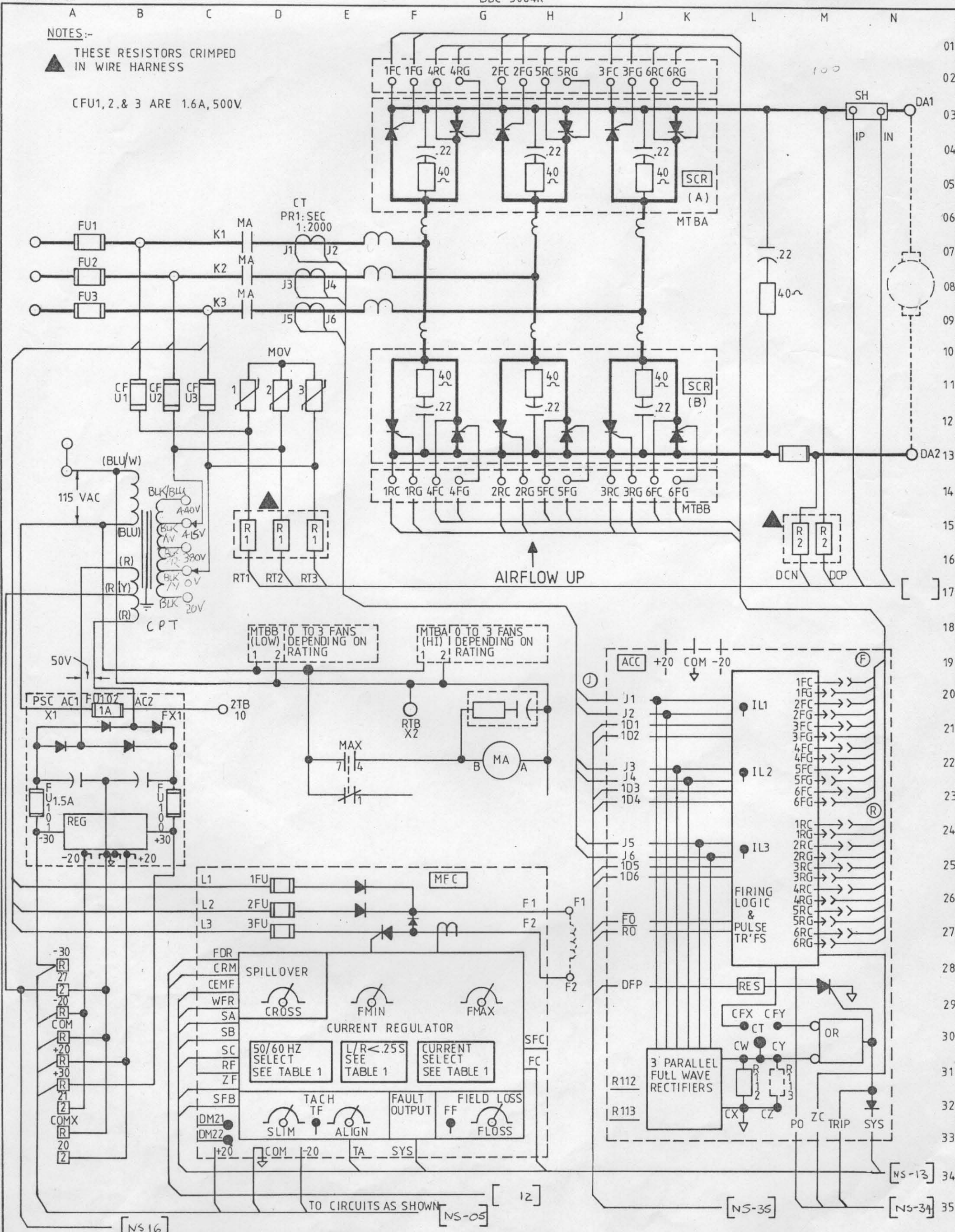
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 SWITCHED FOR "PRECONDITION" - "RUN"
 OR JOG" - "DIAGNOSTIC STATIC" -
 "DIAGNOSTIC RUN" IS SHOWN BY A
 FOUR DIGIT WORD WITH STATE SEQUENCE.

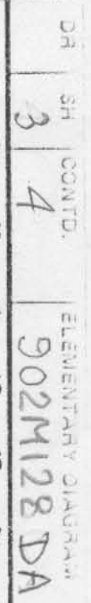
TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	15.5.86		BDC 3064R 75HP		IDENT	
									(KEILON WICKMAN BENNETT)		DR SH	
									GO NUMBER		ELEMENTARY DIAGRAM	
									613N01		902M128DA	
									CONTD.		2	

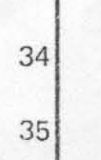
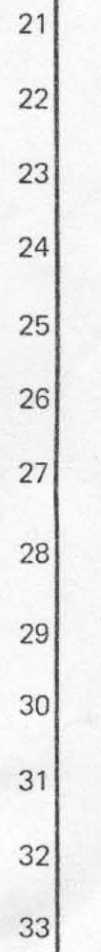
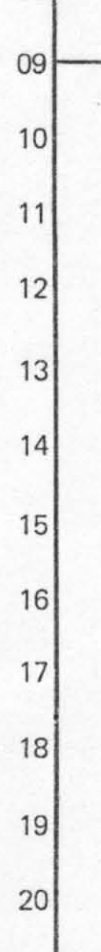
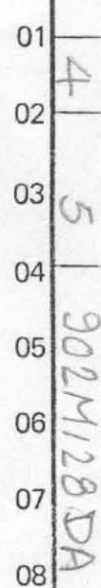
THESE RESISTORS CRIMPED
IN WIRE HARNESS

CFU1, 2. & 3 ARE 1.6A, 500V.

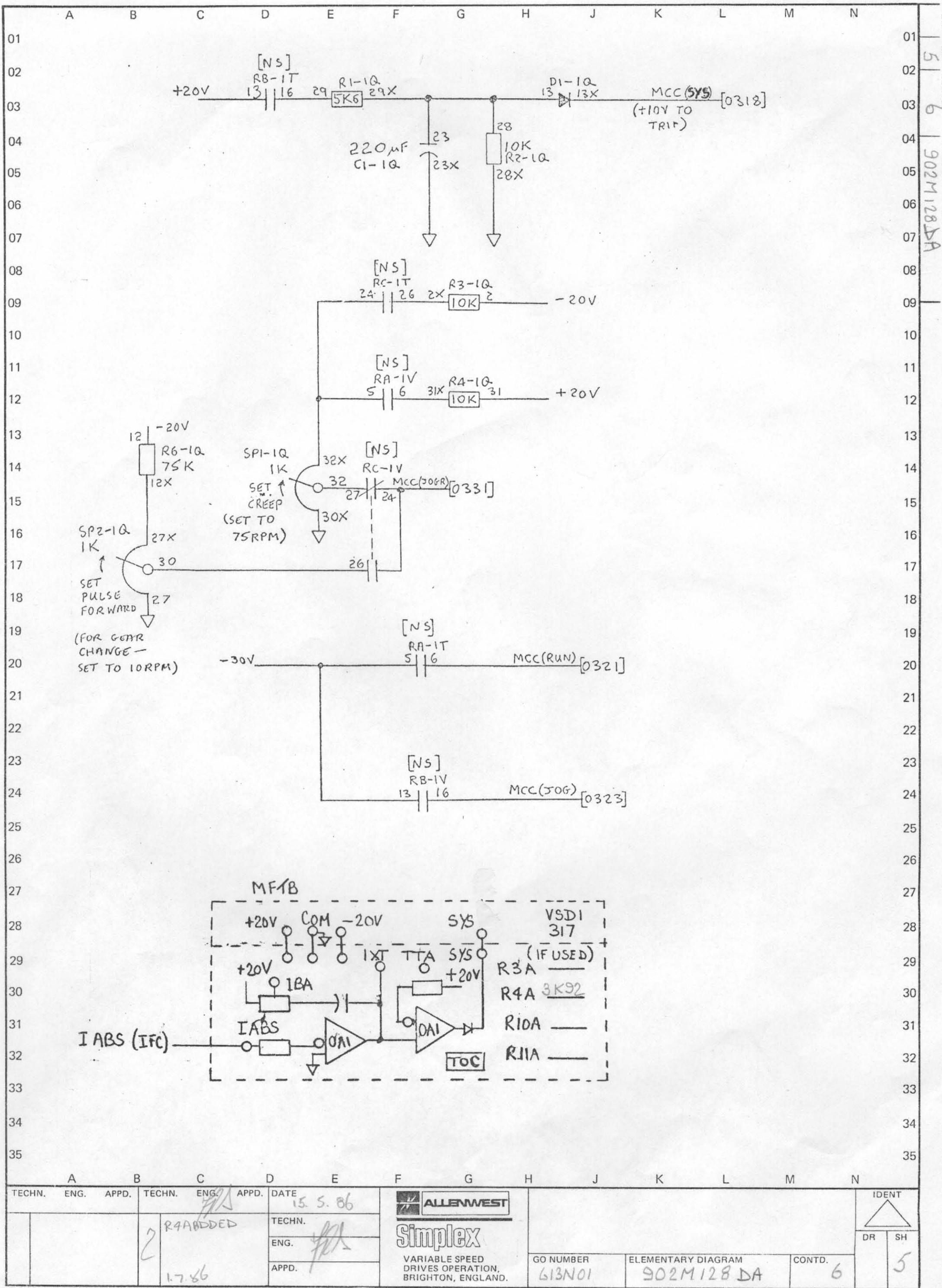


TECHN: ENG: APPD:		TECHN: ENG: APPD:		DATE: 15.5.86	ALLENWEST VARIABLE SPEED DRIVES OPERATION BRIGHTON, ENGLAND.			IDENT. 			
				TECHN:							G.O. NUMBER 613NO1
				ENG: 						DR	SH
				APPD:							2





A	B	C	D	E	F	G	H	J	K	L	M	N
TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE 15.5.86	 <p>Simplex</p> <p>VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.</p>				IDENT 	
					TECHN. PDS						DR	SH
					ENG.							
					APPD.			GO NUMBER 613NO1	ELEMENTARY DIAGRAM 902M128 DA	CONTD. 5	4	



	A	B	C	D	E	F	G	H	J	K	L	M	N	
01														01
02														02
03														03
04														04
05														05
06														06
07														07
08														08
09														09
10														10
11														11

ON PRINTED CIRCUIT CARDS USED IN THIS RACK THE LETTERS 'AA' AFTER BASIC CATALOGUE NUMBER INDICATES ORIGINAL DESIGN. SUBSEQUENT DESIGNS WITH THE SAME BASIC NUMBERS AND GROUP NUMBER WITH THE SECOND LETTER CHANGED, SUCH AS: AB, AC, AD, ETC., ARE DIRECTLY INTERCHANGEABLE AND MAY BE SUPPLIED IN PLACE OF THE 'AA' CARDS.

THE PRINTED CIRCUIT CARD SHOULD ALWAYS BE REMOVED WITH THE CARD EXTRACTOR WHICH IS ATTACHED ON TOP OF THE CARD RACK. SOME CARDS CONTAIN PARTS WHICH WILL BE THERMALLY HOT AFTER BEING IN OPERATION. CARE SHOULD BE EXERCISED IN HANDLING ALL CARDS AFTER REMOVAL UNTIL THESE PARTS HAVE COOLED. DO NOT REMOVE OR INSERT CARDS WITH POWER APPLIED.

FRONT VIEW OF 64 PIN RECEPTACLE AS SEEN IN RACK CLOSED POSITION.

SYMBOLS:



TEST POST



POT ADJUSTMENT



INDICATING LIGHT

32	-	64
31	-	63
30	-	62
29	-	61
28	-	60
27	-	59
26	-	58
25	-	57
24	-	56
23	-	55
22	-	54
21	-	53
20	-	52
19	-	51
18	-	50
17	-	49
16	-	48
15	-	47
14	-	46
13	-	45
12	-	44
11	-	43
10	-	42
9	-	41
8	-	40
7	-	39
6	-	38
5	-	37
4	-	36
3	-	35
2	-	34
1	-	33

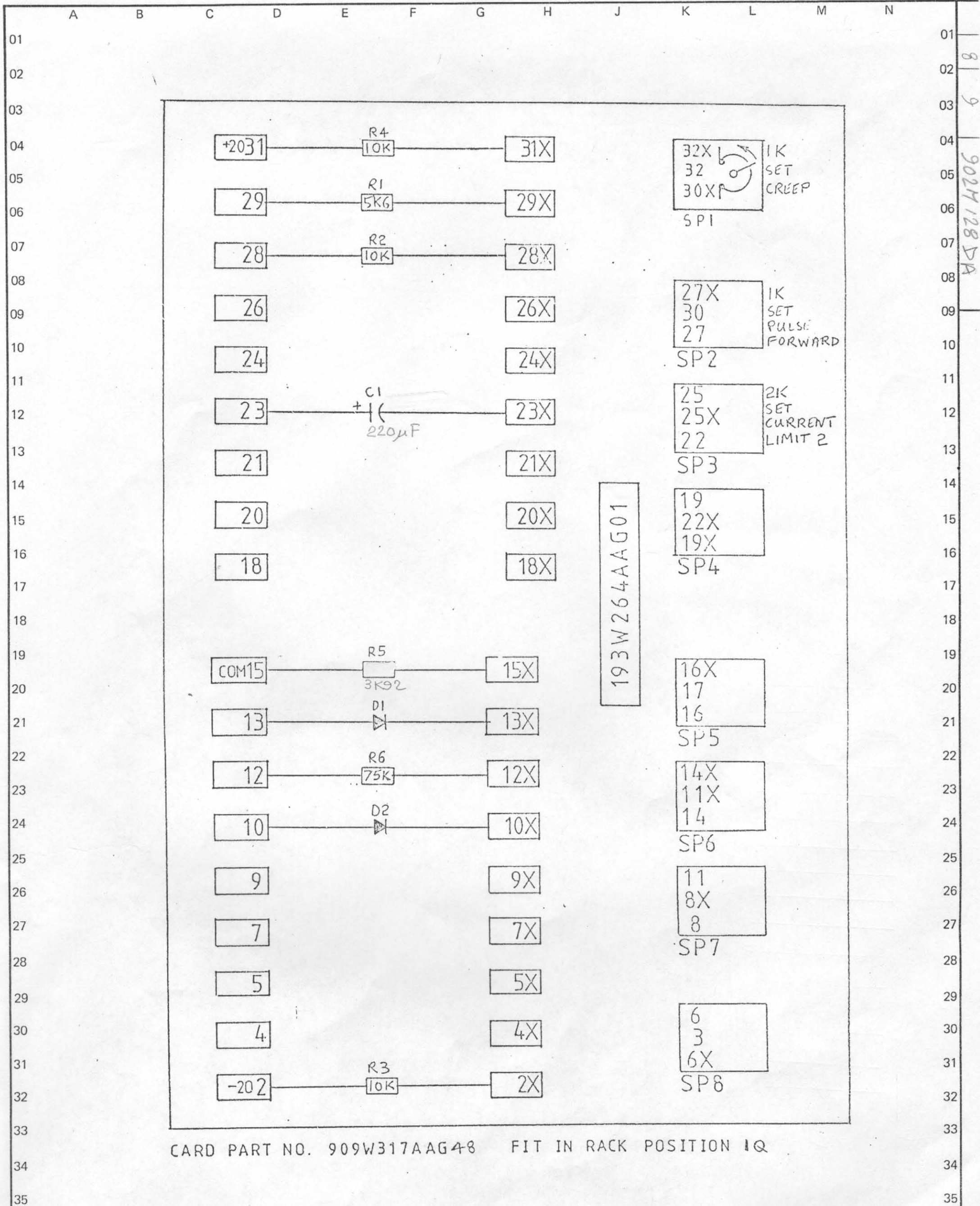
32	-	32X
31	-	31X
30	-	30X
29	-	29X
28	-	28X
27	-	27X
26	-	26X
25	-	25X
24	-	24X
23	-	23X
22	-	22X
21	-	21X
20	-	20X
19	-	19X
18	-	18X
17	-	17X
16	-	16X
15	-	15X
14	-	14X
13	-	13X
12	-	12X
11	-	11X
10	-	10X
9	-	9X
8	-	8X
7	-	7X
6	-	6X
5	-	5X
4	-	4X
3	-	3X
2	-	2X
1	-	1X

CARD RACK WIDE JUMPER TABLE

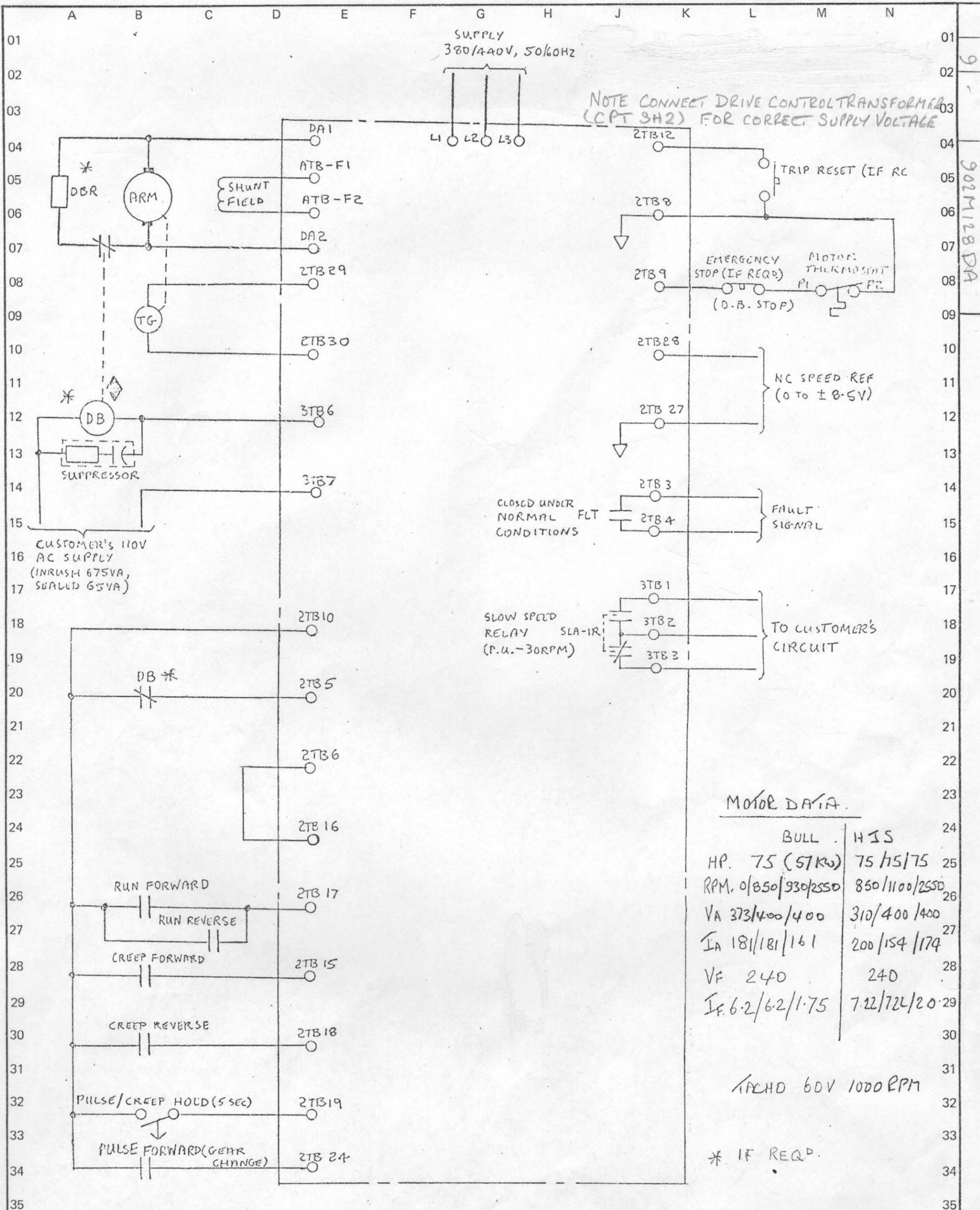
RTB(-20V)-1Q02	MCC(SFB)-1R22	1R22-1R11	
1Q02-1R02	1R19-1R21	1V25-1V21	SP2-1T23
RTB(COM)-1Q15	MCC(UM3)-1R26	1V21-1V11	SP3-1T14
1Q15-1R15	1R28-1Q10	1V11-1T11	SP4-1T03
RTB(+20V)-1Q31	1R31-1T13	1T11-1T21	SP5-1V03
1Q31-1R31	1T16-1Q29	1T21-1T25	SP6-1V14
RTB(-30V)-1T05	1Q29X-1Q28	1Q02-1Q12	SP7-1V23
1T05-1V13	1Q28-1Q23	1Q12X-1Q27X	2TB6-2TB16
RTB(X2)-1T25	1Q23-1Q13	1Q30-1V26	SP11-1R29
	1Q28X-1Q23X	1Q27-1Q23X	SP12-1R25
	1Q23X-1Q15	1V24-MCC(50GR)	SP13-1R30
	1Q13X-MCC(SYS)	1R08-1Q25	
	1Q2X-1T26	1Q25-1Q25X	
	1Q31X-1V06	1Q22-1Q15X	
	1V05-1T24	1R13-1R17	MA13-3TB6
	1T24-1Q32X	1Q25X-MCC(UM2)	MA14-3TB7
	1Q30X-1Q28X	1Q10X-1Q27	
	1Q32-1V27		
	1T06-MCC(RUN)		
	1V16-MCC(50G)		
	MCC(DMI)-1R05		

NOTE: RECEPTACLE PINS MAY BE NUMBERED AS SHOWN IN EITHER SKETCH. (PIN 33 CORRESPONDS TO PIN 1X, 34 TO 2X, ETC.)

TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE						IDENT 	
						15.5.86						DR SH	
												7	
							GO NUMBER		ELEMENTARY DIAGRAM		CONTD.		
							613N01		902M128DA		8		



A			B			C			D			E			F			G			H			J			K			L			M			N		
TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE																																
						15.5.86																																
						TECHN.																																
						ENG.																																
						APPD.																																
						<div style="display: flex; align-items: center;"> <div> <p>Simplex</p> <p>VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.</p> </div> </div>																																
						GO NUMBER						ELEMENTARY DIAGRAM						CONTD.																				
						613N01						902M128DA						9																				
						<div style="display: flex; justify-content: space-between;"> <div> <p>DR</p> <p>SH</p> </div> <div> <p>8</p> </div> </div>																																



TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	15.5.86								IDENT	
						TECHN.		VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.			GO NUMBER 613N01		ELEMENTARY DIAGRAM 902M128DA		CONTD.	
						ENG.									DR SH 9	
						APPD.										

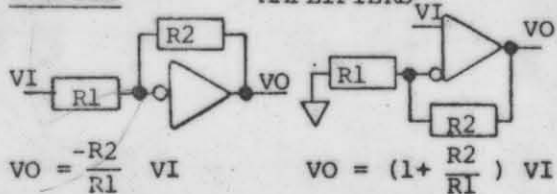
VOLTAGE POLARITIES SHOWN ARE FOR MOTORING DA1 (+)

HARDWARE ABBREVIATIONS

MCC	MAIN CONTROL CARD
IFC	INTERFACE CARD
PSC	POWER SUPPLY CARD
SCR	THYRISTOR ASSEMBLY
DGC	DIAGNOSTIC CARD
MFC	MOTOR FIELD CONTROL
MFE	MOTOR FIELD EXCITER
MDR	MODIFICATION RACK
ACC	AUXILIARY CONTROL CARD

SYMBOLS

AMPLIFIERS




 CASE GROUND

$$\frac{VI}{ABS} \rightarrow VO = \text{SIGN } () \times \text{ABSOLUTE VALUE OF } VI$$

STAB ON TERMINAL

TERMINAL AT 2TB, 3TB, 4TB, RTB.
EX: 9 2 - 2TB9; X2 R - RTBX2

TERMINAL AT T.B.'s



POTENTIOMETER ARROWS ON THE CARD
ELEMENTARY DIAGRAMS INDICATE THE
WIPER DIRECTION AS THE POTENTIOMETER
SHAFT IS ROTATED CLOCKWISE TO INCREASE
FUNCTION.

THESE RESISTORS ARE CRIMPED IN WIRE HARNESS.

FUNCTION	USE	LOC	JUMPERS
60HZ		MFC	ZA-ZB (IF USED)
50HZ	X	MCC	HZA - PHA
IOC-400%	X		(NONE)
-500%		IFC	I - IHI
-300%		IFC	I-ILO
SR5 - 9v	X		(NONE)
9 - 20v		MCC	SRH - COM
JOGR 10v			(NONE)
20V	X	MCC	JH - COM
LT.3-7sec.	X		(NONE)
2 - 60sec			332Ω FROM LTI TO COM
VREG			NT-CEMF CC-COM
DC TACHO	X		(NONE)
AC TACHO		MCC	AT1 - AT2
TACHO FILT		IFC	TC - TC
TACHO V. 24-64vdc		IFC	NT-NT1 PT - PT1
27-71vac		IFC	NT-NT1 PT - PT1
60-160vdc		IFC	NT-NT2 PT - PT2
66-177vac		IFC	NT-NT2 PT - PT2
110-300vdc	X	IFC	NT-NT3 PT - PT3
120-300vac		IFC	NT-NT3 PT - PT3
G134 G256		IFC	MFC OR MFE
1.8T 1.7		MF	NONE
1.3 2.8		MF	YB - YD
2.4 5.0		MF	YA - YB
4.0 8.0		MF	YA-YB, YC-YD
7.0 13	X	MF	YA - YC
13 25		MF	YA-YC, YB-YD
L/R<.25S	X	MFC	QA - QB
INH RUN		DGC	D1-D2 (IF USED)
INH DRV CL		MCC	DC1 - COM
FUSELESS		ACC	CFY - CFX
200% DR.CL	X	MCC	DCX-DCY
REG. C.L.	X	MCC	ILA-DMI
FLD ECON DLY	X	MCC	FEA-DM3

SIGNAL DEFINITIONS AND LOCATIONS

* CEMF	COUNTER EMF (16)
* CFB	CURRENT FEEDBACK (16)
CMFA	ABSOLUTE VALUE CEMF (08)
CRM	CROSSOVER MODIFY (11)
DFP	DELAYED FIRING POWER (25)
* DR	DRIVER REFERENCE (33)
* EAO	ERROR AMP OUTPUT (33)
EST	EXTERNAL FLT STOP INPUT (14)
FAULT	FAULT (14)
* FC	FIELD CURRENT (NS26)
FDR	FIELD DIAGNOSTIC REFERENCE (08)
FEA	FIELD ECONOMY ADJUST (25)
FF	FIELD FAULT (28)
IABS	MOTOR CURRENT ABSOLUTE (09)
ILA	CURRENT LIMIT ADJUST (23)
IMET	CURRENT SIGNAL FOR METER (10)
* IPU	INITIAL PULSE (20)
* LR	LOCAL REF. FROM DGC (33)
* JOG	JOG SWITCH INPUT (23)
* JOGR	JOG REFERENCE INPUT (31)
* MAC	MAX/MA CONTROL SIGNAL (20)
MSW	MODE SWITCH (30)
* OSC	OSCILLATOR (17)
* PCR	PHASE CONTROL REF. (26)
* PRE	DRIVE PRECONDITION (21)
ØSEQ	PHASE SEQUENCE (14)
RERR	REGULATOR ERROR (27)
RIJ	INTEGRATOR SUMMING JUNCTION (27)
RJ	REGULATOR SUMMING JUNCTION (31)
RAA	REGULATOR RESPONSE ADJUST (30)
RSET	RESET (16)
* RTR	READY TO RUN (16)
* RUN	RUN SWITCH INPUT (21)
* SA-C	PHASE SYN OUTPUT (16)
* SFB	SPEED FEEDBACK (20)
SMET	SPEED SIGNAL FOR METER (12)
* SR	SYSTEM REFERENCE INPUT (29)
* SYS	SYSTEM FAULT TRIP (13)
* TA	OUTPUT FOR TACHO TRIP ADJUST (20)
TF	TACHO FAULT (NS28)
* TFB	TACHOMETER FEEDBACK (20)
TFR	AC TACHO FREQUENCY OUTPUT (13)
* TR	TIMED REFERENCE (33)
* VFB	VOLTAGE FEEDBACK (19)
* WFR	WEAK FIELD REFERENCE (20)

(* - TEST POINT ON DOOR FRONT)

MAPPING SYSTEM

(NS/PS/TS) PS - PAST SHEET
NS - NEXT SHEET
TS - THIS SHEET

HENCE (PS - 12) DENOTES LOCATION ON PAST SHEET LINE 12. OTHER LOCATIONS ARE DENOTED BY SHEET NUMBER AND LINE? E.G. (1A16) SIGNIFIES LOCATION ON SHEET 1A, LINE 16 ETC.

NOTE: 1 FIELD EFFECT TRANSISTOR: THE
CLOSED/OPEN (I/O) STATE OF THESE
SWITCHED FOR "PRECONDITION" - "RUN"
OR JOG" - "DIAGNOSTIC STATIC" -
"DIAGNOSTIC RUN" IS SHOWN BY A
FOUR DIGIT WORD WITH STATE SEQUENCE.

TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE
						23-2-82
						TECHN. <i>AM</i>
						ENG.
						APPD. <i>[Signature]</i>

ALLENWEST

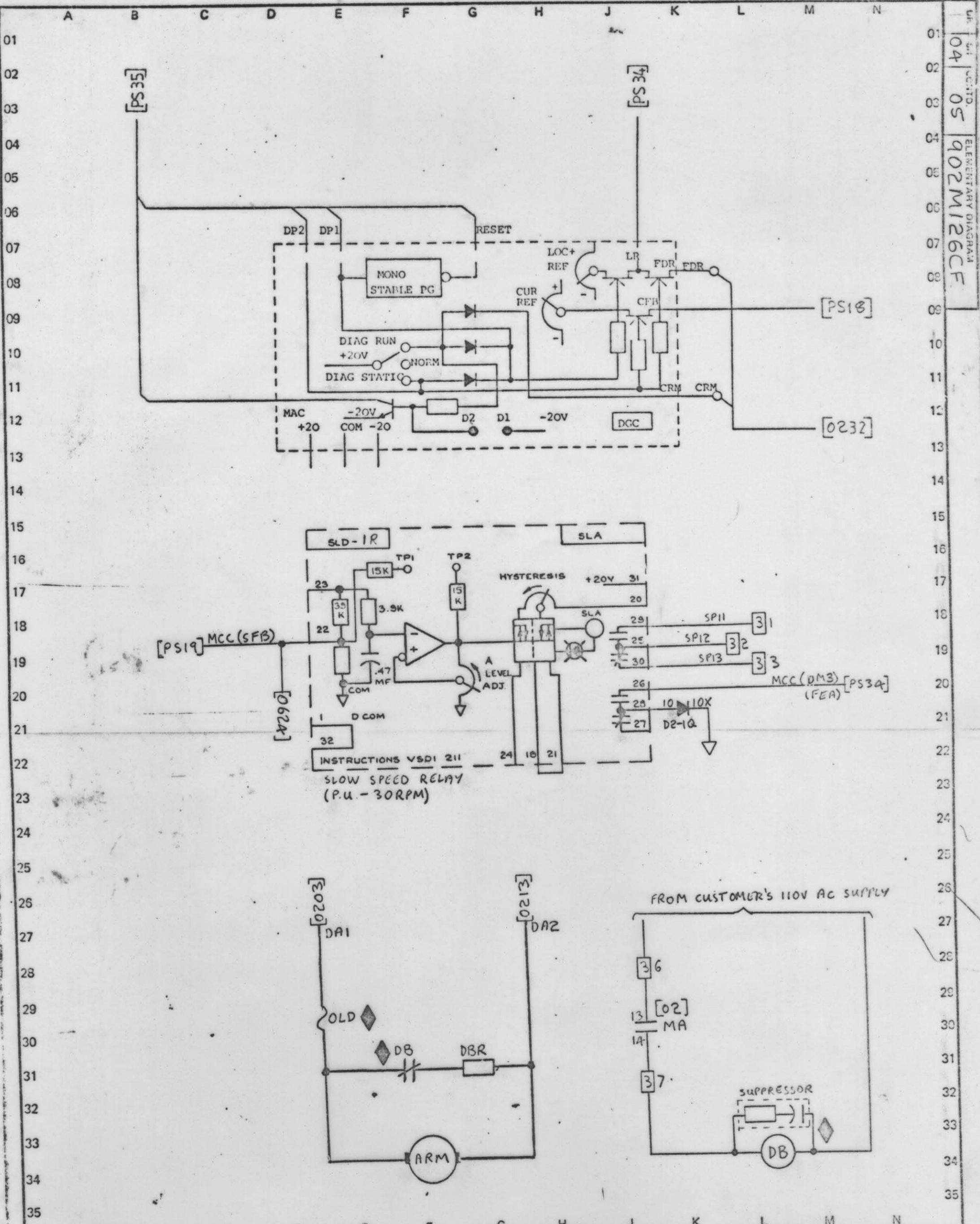
Simplex

VARIABLE SPEED
DRIVES OPERATION,
BRIGHTON, ENGLAND

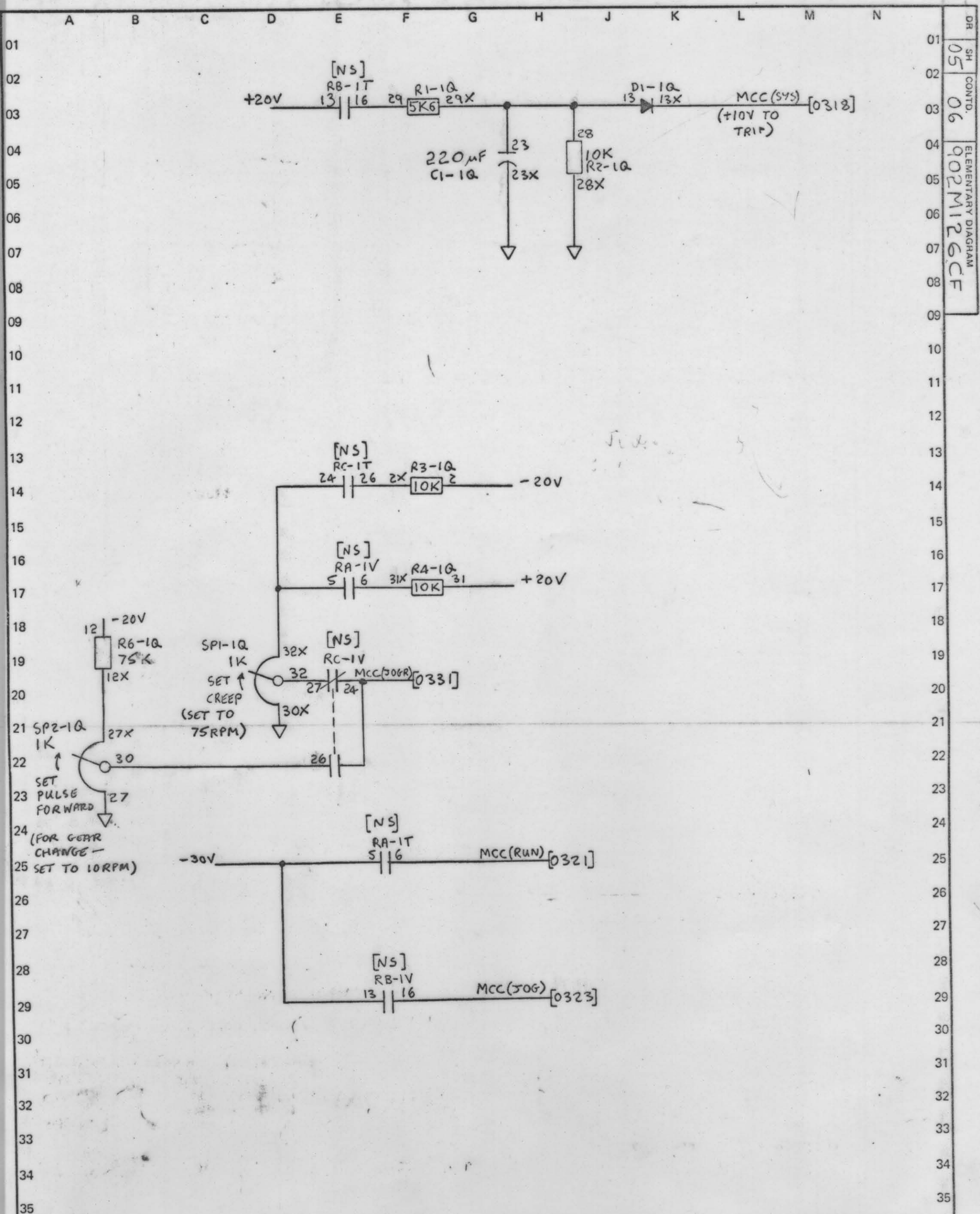
40 HP 3064R
WEBSTER & BENNETT LTD.

GO NUMBER	ELEMENTARY DIAGRAM	CONTD.
145N02	902M126CF	02

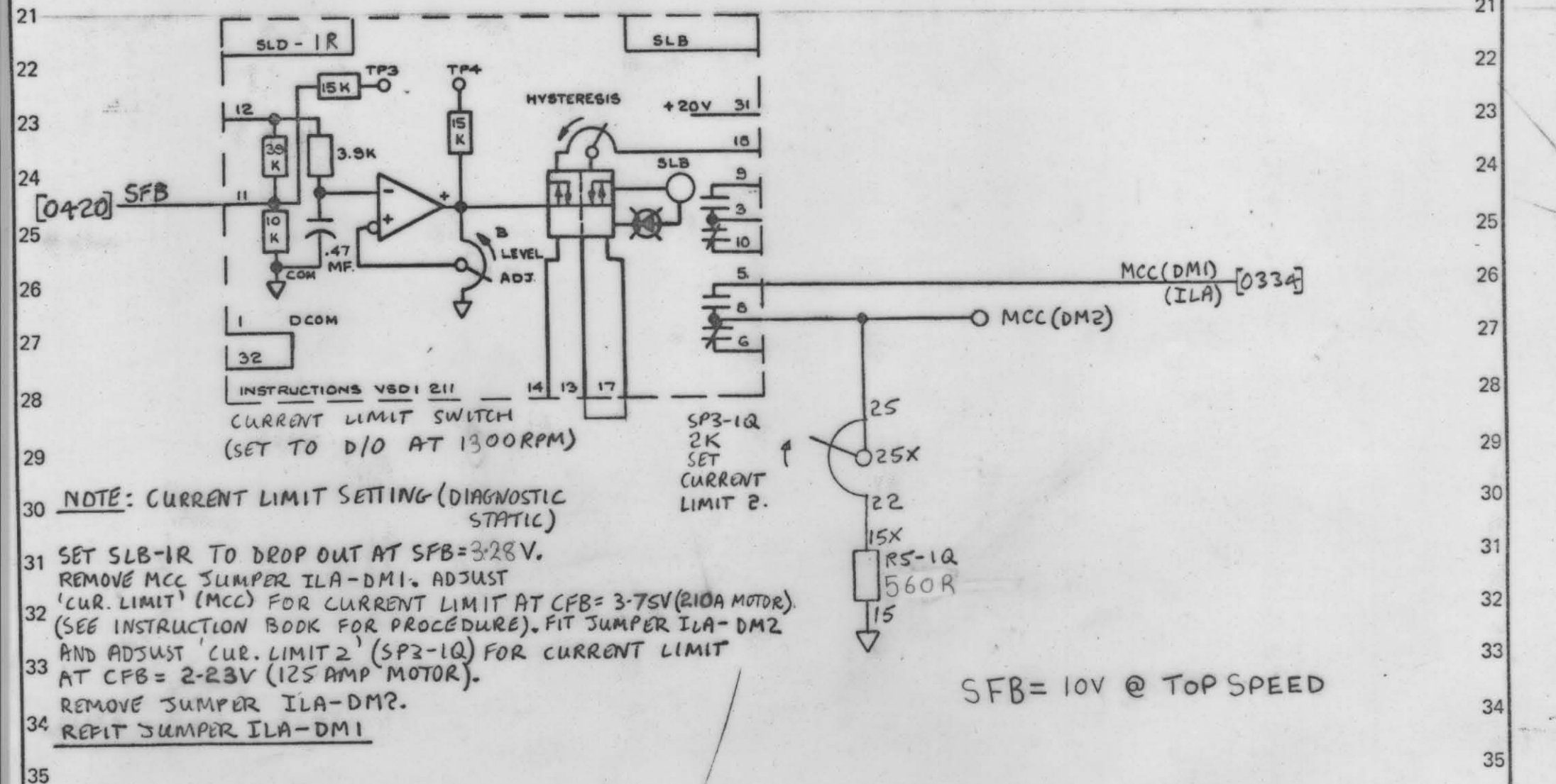
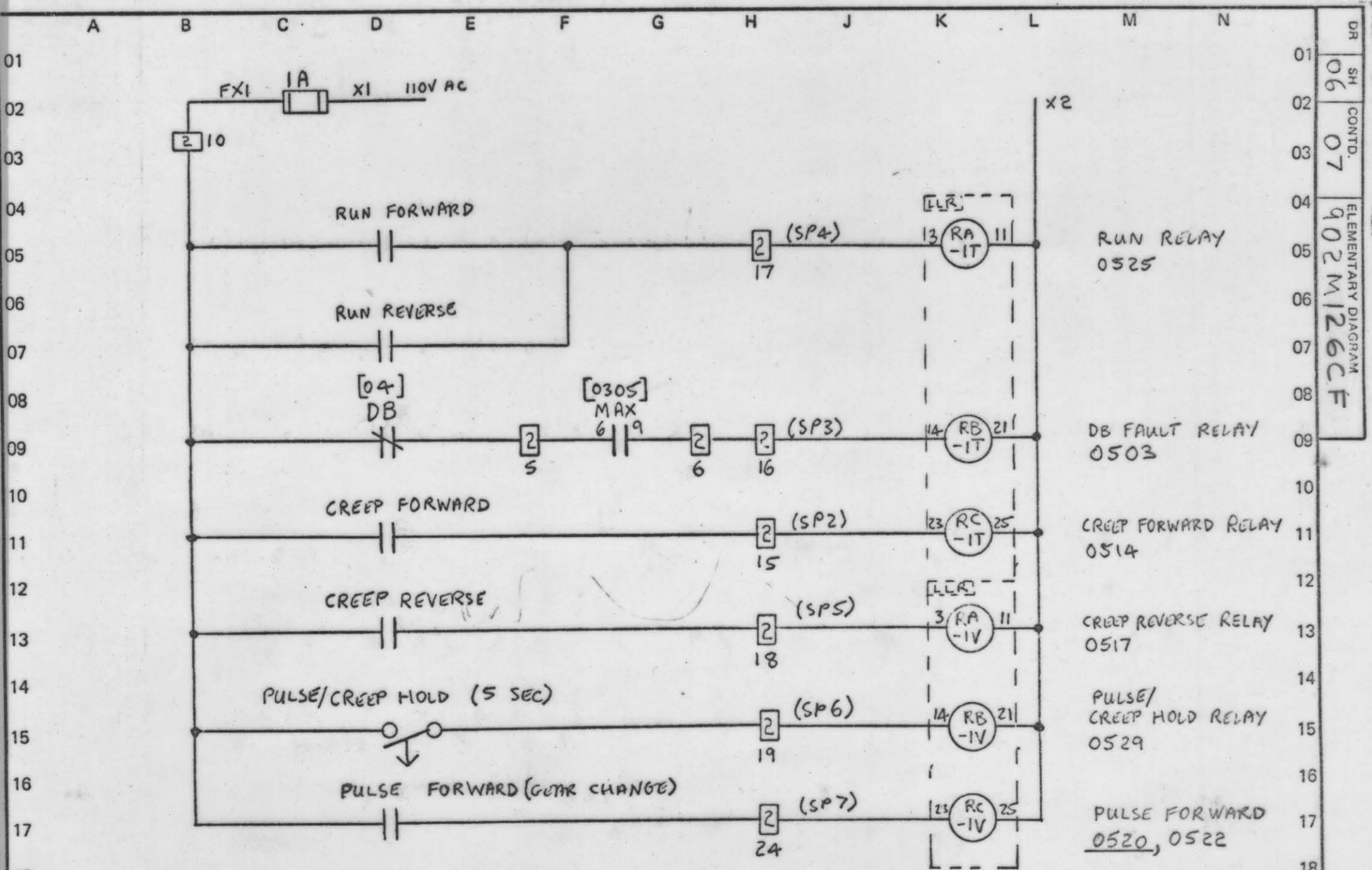
IDENT	
	
DR	SH
	0



TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	23-2-82	BDC 306AR, 40HP		IDENT	
								WEBSTER & BENNETT		04	
								GO NUMBER		ELEMENTARY DIAGRAM	
								145N02		902M126CF	
										CONTD.	
										05	



TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE 24-2-82	 VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.		BDC 3064R, 40HP WEBSTER & BENNETT		IDENT DR SH 05	
						GO NUMBER 145N02	ELEMENTARY DIAGRAM 902M126CF		CONTD. 06			



A	B	C	D	E	F	G	H	J	K	L	M	N
TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	24-2-82					
						TECHN.	AS SHIPPED.					
						ENG.	DATE: 1-4-82					
						APPD.						
						VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.						
						BDC 3064R, 40HP						
						WEBSTER + BENNETT						
						GO NUMBER	ELEMENTARY DIAGRAM	CONTD.				
						145N02	902M126CF	07				
						DR	SH					
						06						

	A	B	C	D	E	F	G	H	J	K	L	M	N
01	A	B	C	D	E	F	G	H	J	K	L	M	N
02													
03													
04													
05													
06													
07													
08													
09													
10													
11													

ON PRINTED CIRCUIT CARDS USED IN THIS RACK THE LETTERS 'AA' AFTER BASIC CATALOGUE NUMBER INDICATES ORIGINAL DESIGN. SUBSEQUENT DESIGNS WITH THE SAME BASIC NUMBERS AND GROUP NUMBER WITH THE SECOND LETTER CHANGED, SUCH AS: AB, AC, AD, ETC., ARE DIRECTLY INTERCHANGEABLE AND MAY BE SUPPLIED IN PLACE OF THE 'AA' CARDS.

THE PRINTED CIRCUIT CARD SHOULD ALWAYS BE REMOVED WITH THE CARD EXTRACTOR WHICH IS ATTACHED ON TOP OF THE CARD RACK. SOME CARDS CONTAIN PARTS WHICH WILL BE THERMALLY HOT AFTER BEING IN OPERATION. CARE SHOULD BE EXERCISED IN HANDLING ALL CARDS AFTER REMOVAL UNTIL THESE PARTS HAVE COOLED. DO NOT REMOVE OR INSERT CARDS WITH POWER APPLIED.

FRONT VIEW OF 64 PIN
RECEPTACLE AS SEEN
IN RACK CLOSED
POSITION.

SYMBOLS:



TEST POST



POT ADJUSTMENT



INDICATING LIGHT

CARD RACK WIRE JUMPER TABLE

RTB(-20V)-1Q02	MCC(SFB)-1R22	1R22-1R11	
1Q02-1R02	1R19-1R21	1V25-1V21	SP2-1T23
RTB(COM)-1Q15	MCC(DM3)-1R26	1V21-1V11	SP3-1T14
1Q15-1R15	1R28-1Q10	1V11-1T11	SP4-1T03
RTB(+20V)-1Q31	1R31-1T13	1T11-1T21	SP5-1V03
1Q31-1R31	1T16-1Q29	1T21-1T25	SP6-1V14
RTB(-30V)-1T05	1Q29X-1Q28	1Q02-1Q12	SP7-1V23
1T05-1V13	1Q28-1Q23	1Q12X-1Q27X	2TB6-2TB16
RTB(X2)-1T25	1Q23-1Q13	1Q30-1V26	SP11-1R29
	1Q28X-1Q23X	1Q27-1Q23X	SP12-1R25
	1Q23X-1Q15	1V24-MCC(50GR)	SP13-1R30
	1Q13X-MCC(SYS)	1R08-1Q25	
	1Q2X-1T26	1Q25-1Q25X	
	1Q31X-1V06	1Q22-1Q15X	
	1V05-1T24	1R13-1R17	MA13-3TB6
	1T24-1Q32X	1Q25X-MCC(DM2)	MA14-3TB7
	1Q30X-1Q28X	1Q10X-1Q27	
	1Q32-1V27		
	1T06-MCC(RUN)		
	1V16-MCC(50G)		
	MCC(DM1)-1R05		

32 - 64	32 - 32X
31 - 63	31 - 31X
30 - 62	30 - 30X
29 - 61	29 - 29X
28 - 60	28 - 28X
27 - 59	27 - 27X
26 - 58	26 - 26X
25 - 57	25 - 25X
24 - 56	24 - 24X
23 - 55	23 - 23X
22 - 54	22 - 22X
21 - 53	21 - 21X
20 - 52	20 - 20X
19 - 51	19 - 19X
18 - 50	18 - 18X
17 - 49	17 - 17X
16 - 48	16 - 16X
15 - 47	15 - 15X
14 - 46	14 - 14X
13 - 45	13 - 13X
12 - 44	12 - 12X
11 - 43	11 - 11X
10 - 42	10 - 10X
9 - 41	9 - 9X
8 - 40	8 - 8X
7 - 39	7 - 7X
6 - 38	6 - 6X
5 - 37	5 - 5X
4 - 36	4 - 4X
3 - 35	3 - 3X
2 - 34	2 - 2X
1 - 33	1 - 1X

NOTE: RECEPTACLE PINS MAY
BE NUMBERED AS SHOWN
IN EITHER SKETCH. (PIN
33 CORRESPONDS TO PIN
1X, 34 TO 2X, ETC.)

TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	BDC 3064R, 40HP		ELEM.	
						24-2-82	WEBSTER & BENNETT		DR SH	
2 AS SHIPPED			DATE: 1-4-82			VARIABLE SPEED DRIVES OPERATION. BRIGHTON, ENGLAND.		145N02 902M126CF		08
								07		

A B C D E F G H J K L M N

VOLTAGE POLARITIES SHOWN ARE FOR MOTORING DA1(+)

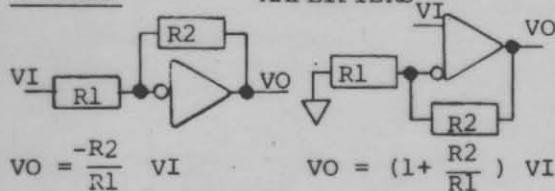
SIGNAL DEFINITIONS AND LOCATIONS

HARDWARE ABBREVIATIONS

MCC MAIN CONTROL CARD
 IFC INTERFACE CARD
 PSC POWER SUPPLY CARD
 SCR THYRISTOR ASSEMBLY
 DGC DIAGNOSTIC CARD
 MFC MOTOR FIELD CONTROL
 MFE MOTOR FIELD EXCITER
 MDR MODIFICATION RACK
 ACC AUXILIARY CONTROL CARD

SYMBOLS

AMPLIFIERS



CASE GROUND

$VO = \text{SIGN} () \times \text{ABSOLUTE VALUE OF } VI$

STAB ON TERMINAL

TERMINAL AT 2TB, 3TB, 4TB, RTB.
 EX: 9 - 2TB9; X2 - RTB2

TERMINAL AT T.B.'s

POTENTIOMETER ARROWS ON THE CARD
 ELEMENTARY DIAGRAMS INDICATE THE
 WIPER DIRECTION AS THE POTENTIOMETER
 SHAFT IS ROTATED CLOCKWISE TO INCREASE
 FUNCTION.

THESE RESISTORS ARE CRIMPED IN WIRE
 HARNESS.

FUNCTION	USE	LOC	JUMPERS
60HZ		MFC	ZA-ZB (IF USED)
50HZ	X	MCC	HZA - PHA
IOC-400%	X		(NONE)
-500%		IFC	I - IHI
-300%		IFC	I - ILO
SR5 - 9v	X		(NONE)
9 - 20v		MCC	SRH - COM
JOG 10v			(NONE)
20v	X	MCC	JH - COM
LT. 3-7sec.			(NONE)
2 - 60sec	X		332Ω FROM LTI TO COM
VREG			NT-CEMF CC-COM
DC TACHO	X		(NONE)
AC TACHO		MCC	AT1 - AT2
TACHO FILT		IFC	TC - TC
TACHO V.		IFC	NT-NT1 PT - PT1
24-64vdc		IFC	NT-NT1 PT - PT1
27-71vac		IFC	NT-NT2 PT - PT2
60-160vdc		IFC	NT-NT2 PT - PT2
66-177vac		IFC	NT-NT2 PT - PT2
110-300vdc	X	IFC	NT-NT3 PT - PT3
120-300vac		IFC	NT-NT3 PT - PT3
G134 G256		IFC	MFC OR MFE
1.8T 1.7		ME	NONE
1.3 2.8		ME	YB - YD
2.4 5.0		ME	YA - YB
4.0 8.0		ME	YA-YB, YC-YD
7.0 13		ME	YA - YC
13 25		ME	YA-YC, YB-YD
L/R < .25S		MFC	QA - QB
INH RUN		DGC	D1-D2 (IF USED)
INH DRV CL		MCC	DC1 - COM
FUSELESS		ACC	CFY - CFX
175%/100 DCL		MCC	DEX - DLY

* CEMF COUNTER EMF (3 16)
 * CFB CURRENT FEEDBACK (3 16)
 CMFA ABSOLUTE VALUE CEMF (3 08)
 CRM CROSSOVER MODIFY (4 11)
 DFP DELAYED FIRING POWER (3 25)
 * DR DRIVER REFERENCE (3 33)
 * EAO ERROR AMP OUTPUT (3 33)
 EST EXTERNAL FLT STOP INPUT (3 14)
 FALT FAULT (3 14)
 * FC FIELD CURRENT (NS26)
 FDR FIELD DIAGNOSTIC REFERENCE (3 08)
 FEA FIELD ECONOMY ADJUST (3 25)
 FF FIELD FAULT (3 28)
 IABS MOTOR CURRENT ABSOLUTE (3 09)
 ILA CURRENT LIMIT ADJUST (3 23)
 IMET CURRENT SIGNAL FOR METER (3 10)
 * IPU INITIAL PULSE (3 20)
 * LR LOCAL REF. FROM DGC (3 33)
 * JOG JOG SWITCH INPUT (3 23)
 * JOGR JOG REFERENCE INPUT (3 31)
 * MAC MAX/MA CONTROL SIGNAL (3 20)
 MSW MODE SWITCH (3 30)
 * OSC OSCILLATOR (3 17)
 * PCR PHASE CONTROL REF. (3 26)
 * PRE DRIVE PRECONDITION (3 21)
 ØSEQ PHASE SEQUENCE (3 14)
 RERR REGULATOR ERROR (3 27)
 RIJ INTEGRATOR SUMMING JUNCTION (3 27)
 RJ REGULATOR SUMMING JUNCTION (3 31)
 RRA REGULATOR RESPONSE ADJUST (3 30)
 RSET RESET (3 16)
 * RTR READY TO RUN (3 16)
 * RUN RUN SWITCH INPUT (3 21)
 * SA-C PHASE SYN OUTPUT (3 16)
 * SFB SPEED FEEDBACK (3 20)
 SMET SPEED SIGNAL FOR METER (3 12)
 * SR SYSTEM REFERENCE INPUT (3 29)
 * SYS SYSTEM FAULT TRIP (3 13)
 * TA OUTPUT FOR TACHO TRIP ADJUST (3 20)
 TF TACHO FAULT (NS28)
 * TFB TACHOMETER FEEDBACK (3 20)
 TFR AC TACHO FREQUENCY OUTPUT (3 13)
 * TR TIMED REFERENCE (3 33)
 * VFB VOLTAGE FEEDBACK (3 19)
 * WFR WEAK FIELD REFERENCE (3 20)

(* - TEST POINT ON DOOR FRONT)

MAPPING SYSTEM

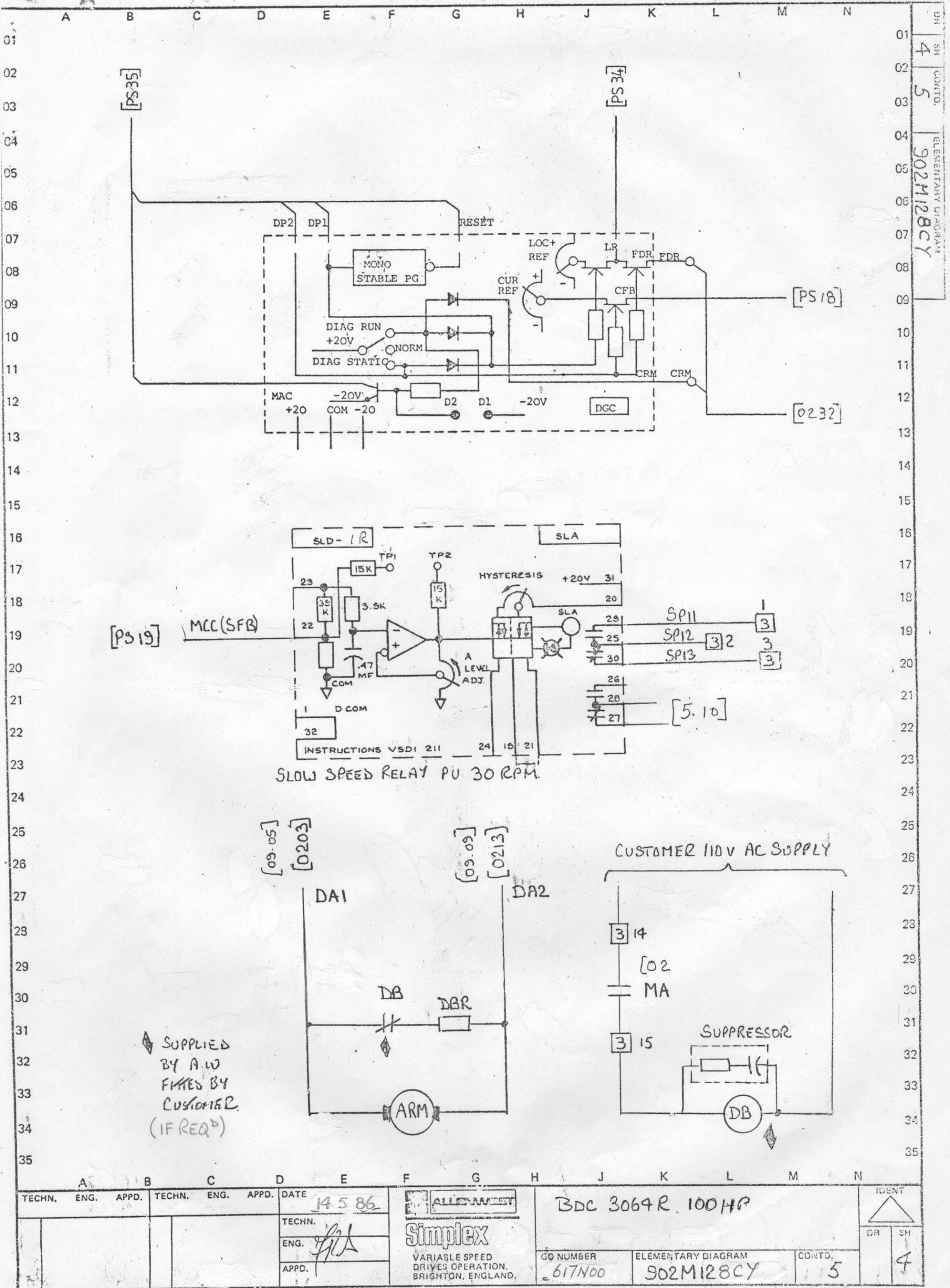
(NS/PS/TS) PS - PAST SHEET
 NS - NEXT SHEET
 TS - THIS SHEET

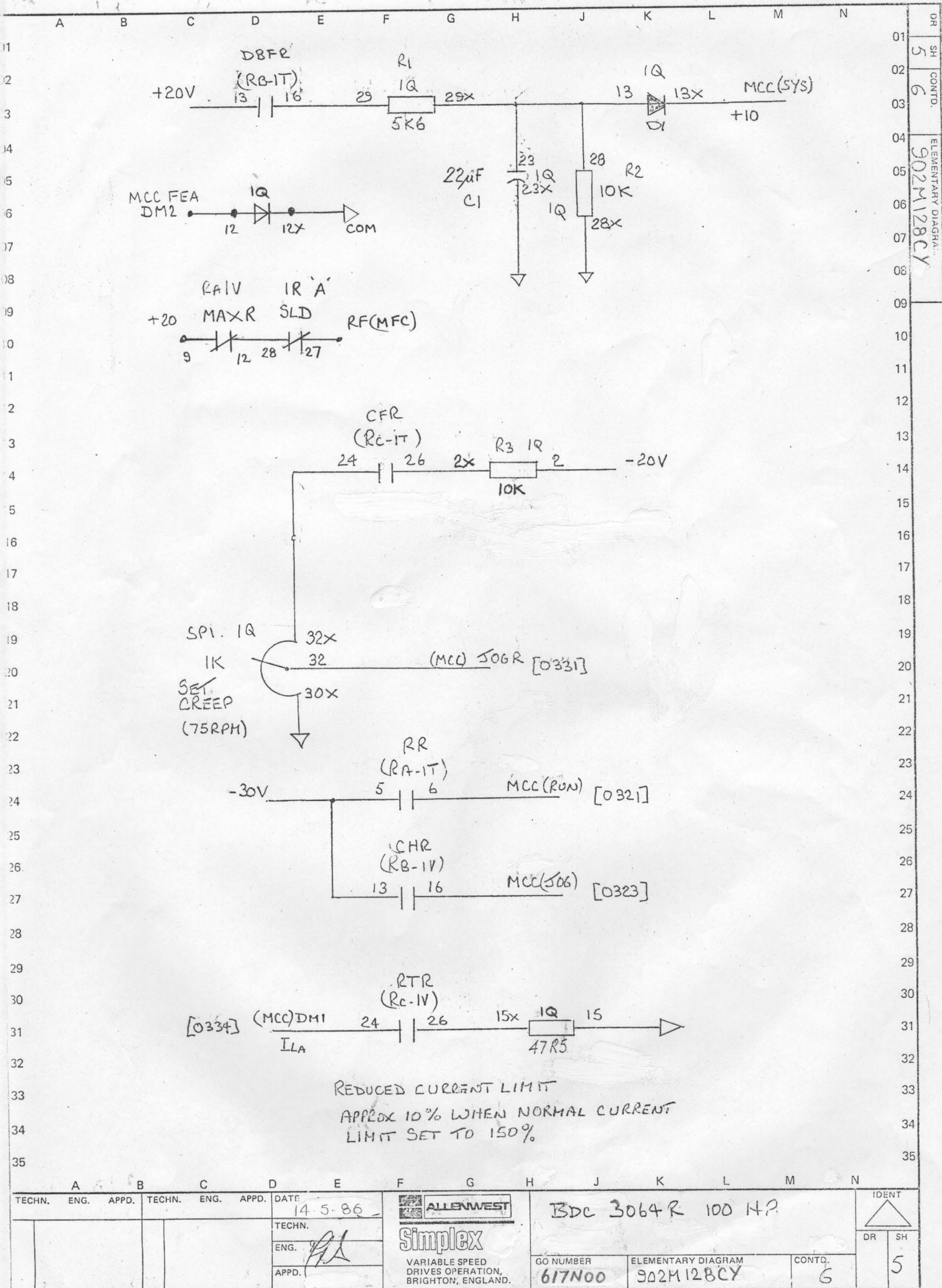
HENCE (PS - 12) DENOTES LOCATION ON PAST SHEET LINE 12. OTHER LOCATIONS ARE
 DENOTED BY SHEET NUMBER AND LINE? E.G. (1A16) SIGNIFIES LOCATION ON SHEET
 1A, LINE 16 ETC.

NOTE: FIELD EFFECT TRANSISTOR: THE
 CLOSED/OPEN (I/O) STATE OF THESE
 SWITCHED FOR "PRECONDITION" - "RUN"
 OR JOG" - "DIAGNOSTIC STATIC" -
 "DIAGNOSTIC RUN" IS SHOWN BY A
 FOUR DIGIT WORD WITH STATE SEQUENCE.

A B C D E F G H J K L M N

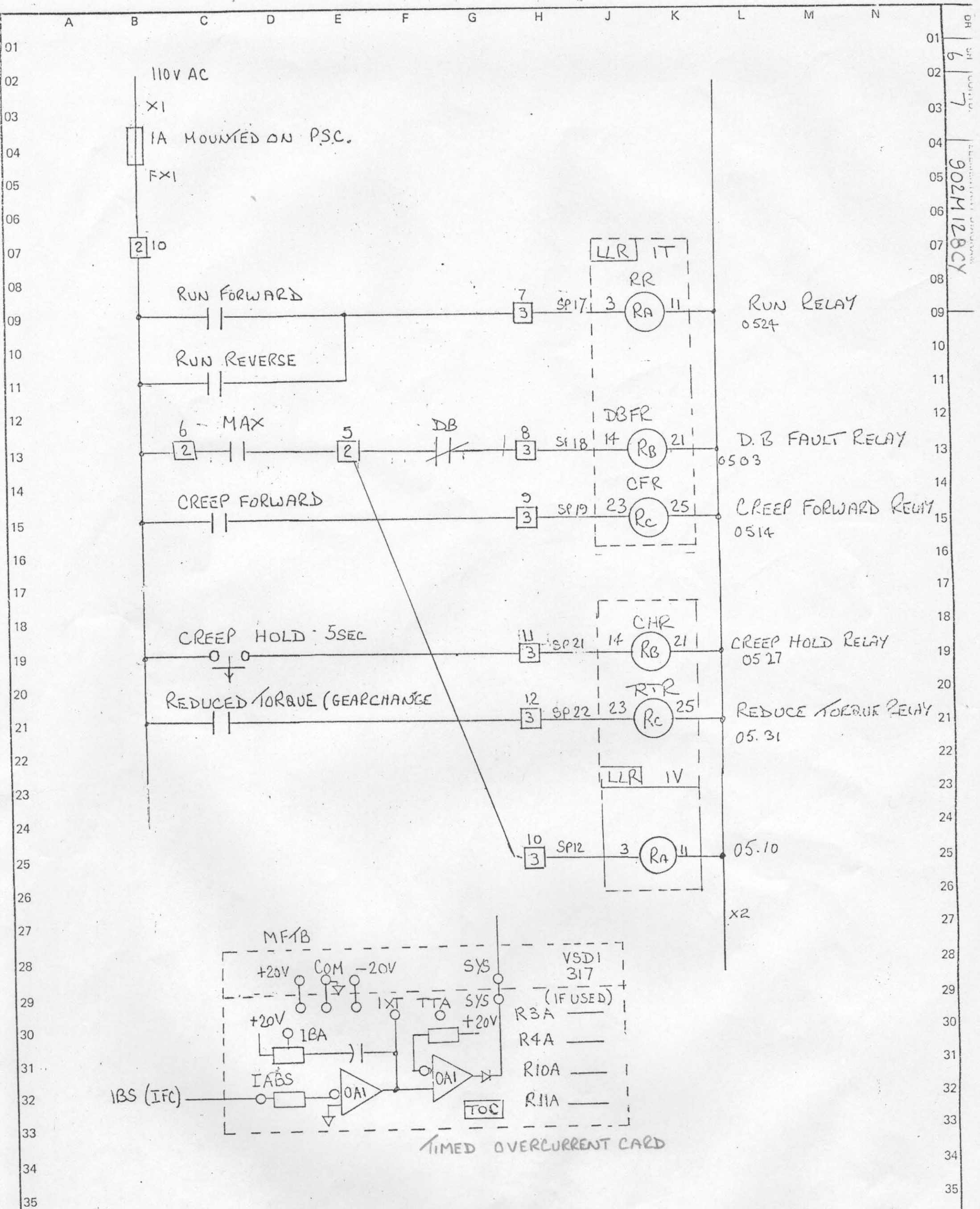
TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	14.5.86		ALLENWEST		BDC 3064R 100 HP		IDENT		
						TECHN.			Simplex		KETLON (WICKMAN BENNETT)		DR SH		
						ENG.			VARIABLE SPEED DRIVES OPERATION. BRIGHTON, ENGLAND.		GO NUMBER		617N00		
						APPD.					ELEMENTARY DIAGRAM		902M128CY		
												CONTD.		2	





DR	SH	CONTO.	ELEMENTARY DIAGRAM
5	6	902M128CY	

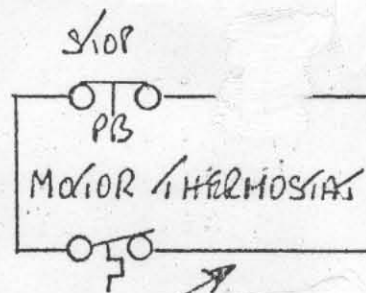
TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	14.5.86			ALLENWEST			BDC 3064 R 100 H?			IDENT	
						TECHN.	AL			Simplex			GO NUMBER			617N00	
						ENG.				VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.			ELEMENTARY DIAGRAM			902M128CY	
						APPD.							CONTO.			6	
															5		



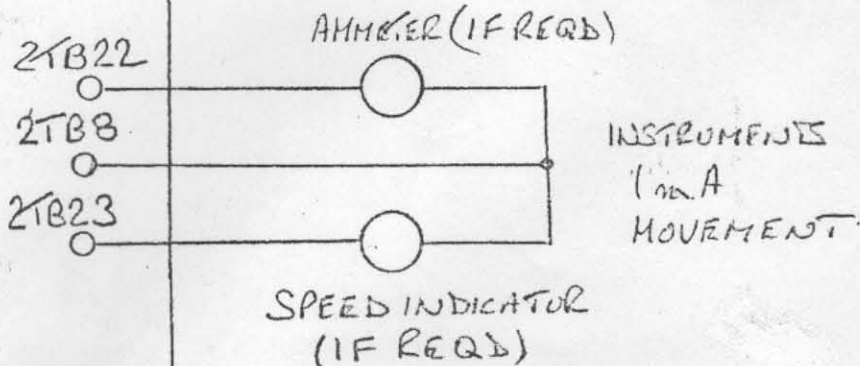
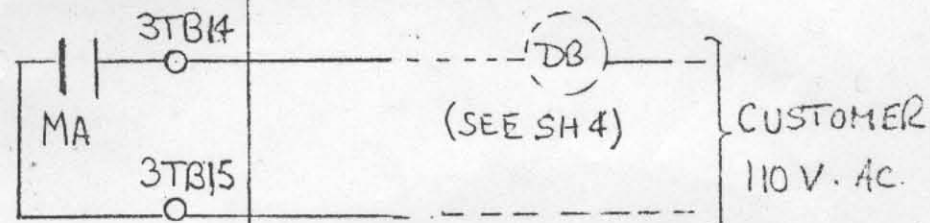
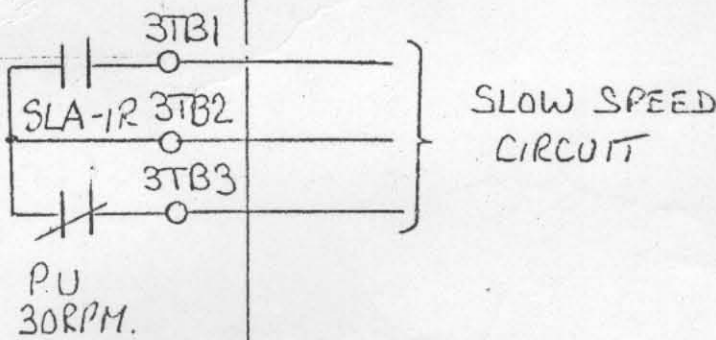
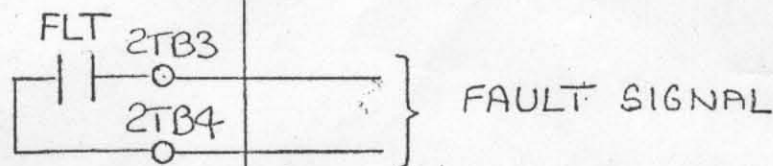
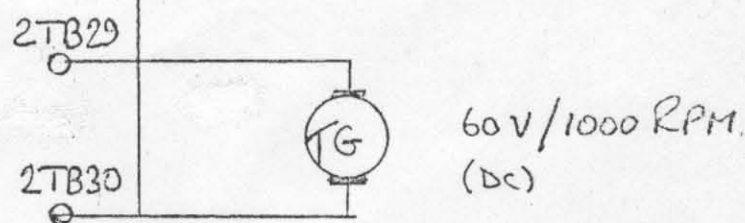
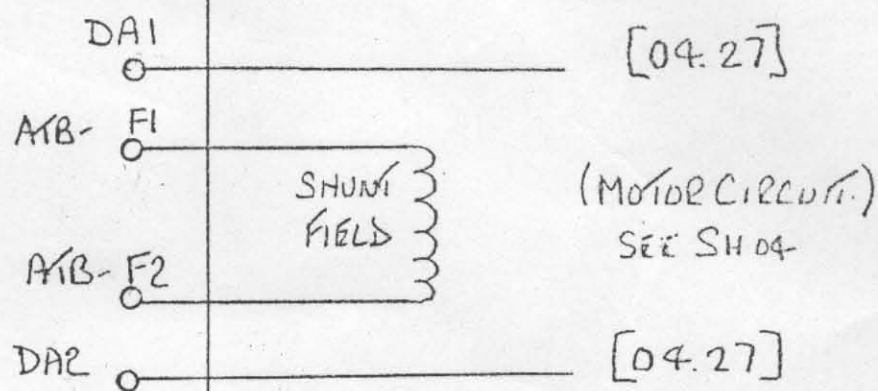
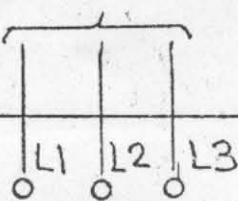
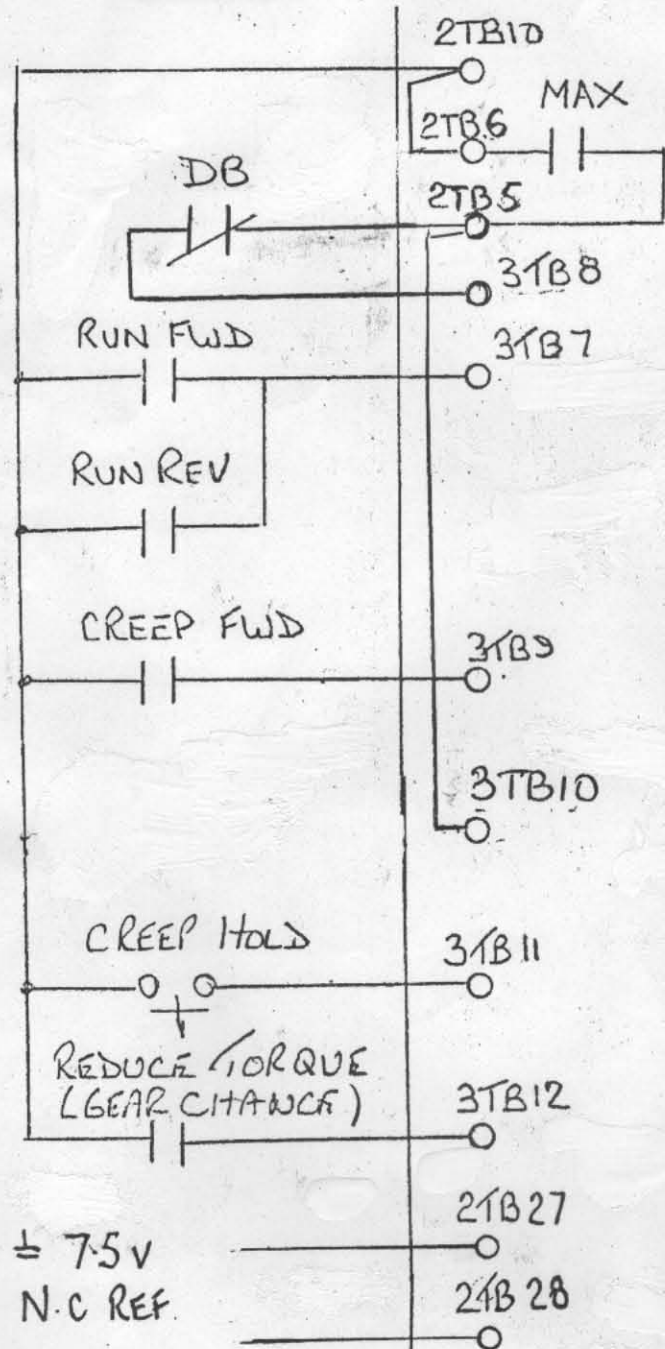
A		B		C		D		E		F		G		H		J		K		L		M		N											
TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	14.5.86																												
						TECHN.	[Signature]																												
						ENG.	[Signature]																												
						APPD.	[Signature]																												
						ALLENWEST						BDC 3064R 100 HP.						IDENT																	
						Simplex						VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.						GO NUMBER						ELEMENTARY DIAGRAM						CONTD.					
																		617N00						902M128CY						7					

NOTE SEE SH1 FOR 50/60HZ SUPPLY
LINKS 420/460V 50/60HZ

CONNECT CONTROL VT
PRIMARY FOR
CORRECT VOLTAGE



KEEP WIRING
SEPARATE
FROM AC/DC
MAINS AND
CONTROLS



MOTOR DATA

HP.
VA
IA
RPM

VF
IF

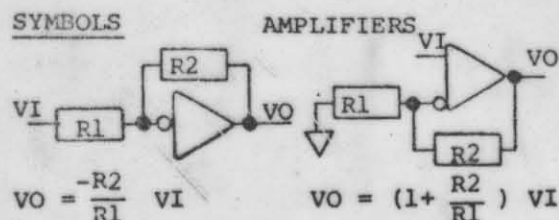
TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	TECHN.	ENG.	APPD.	GO NUMBER	ELEMENTARY DIAGRAM	CONTD.	IDENT
						14.5.86				617N00	902M128CY		9
							ALLENWEST			BDC 3064R 100 HP			DR
							Simplex						SH
							VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.						

VOLTAGE POLARITIES SHOWN ARE FOR MOTORING DA1(+)

HARDWARE ABBREVIATIONS

MCC	MAIN CONTROL CARD
IFC	INTERFACE CARD
PSC	POWER SUPPLY CARD
SCR	THYRISTOR ASSEMBLY
DGC	DIAGNOSTIC CARD
MFC	MOTOR FIELD CONTROL
MFE	MOTOR FIELD EXCITER
MDR	MODIFICATION RACK
ACC	AUXILIARY CONTROL CARD

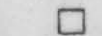
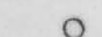
SYMBOLS



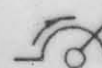
CASE GROUND

VO = SIGN () X ABSOLUTE VALUE OF VI

STAB ON TERMINAL

TERMINAL AT 2TB, 3TB, 4TB, RTB.
EX: 9 [2] - 2TB9; X2 [2] - RTB9

TERMINAL AT T.B.'s

POTENTIOMETER ARROWS ON THE CARD
ELEMENTARY DIAGRAMS INDICATE THE
WIPER DIRECTION AS THE POTENTIOMETER
SHAFT IS ROTATED CLOCKWISE TO INCREASE
FUNCTION.THESE RESISTORS ARE CRIMPED IN WIRE
HARNESS.

FUNCTION	USE	LOC	JUMPERS
60HZ		MFC	ZA-ZB (IF USED)
50HZ		MCC	HZA - PHA
IOC-400%	X		(NONE)
-500%		IFC	I - IHI
-300%		IFC	I - ILO
SR5 - 9v	X		(NONE)
9 - 20v		MCC	SRH - COM
JOGR 10v			(NONE)
20v	X	MCC	JH - COM
LT.3-7sec			(NONE)
2 - 60sec	X		332Ω FROM LTI TO COM
VREG			NT-CEMF CC-COM
DC TACHO	X		(NONE)
AC TACHO		MCC	AT1 - AT2
TACHO FILT		IFC	TC - TC
TACHO V.		IFC	NT-NT1 PT - PT1
24-64vdc		IFC	NT-NT1 PT - PT1
27-71vac	X	IFC	NT-NT2 PT - PT2
60-160vdc		IFC	NT-NT2 PT - PT2
66-177vac		IFC	NT-NT3 PT - PT3
110-300vdc		IFC	NT-NT3 PT - PT3
120-300vac		IFC	NT-NT3 PT - PT3
G134 G256		IFC	MFC OR MFE
1.8T 1.7		MF	NONE
1.3 2.8		MF	YB - YD
2.4 5.0		MF	YA - YB
4.0 8.0	X	MF	YA-YB, YC-YD
7.0 13		MF	YA - YC
13 25		MF	YA-YC, YB-YD
L/R < .25S		MFC	QA - QB
INH RUN		DGC	D1-D2 (IF USED)
INH DRV CL		MCC	DC1 - COM
FUSELESS		ACC	CFY - CFX
200% DC CL	X		DCX - DCY
CL ADS	X		ILA - DM1
FLD ECE 247	X	MCC	FEA - DM2

SEE NOTE

SIGNAL DEFINITIONS AND LOCATIONS

* CEMF	COUNTER EMF (316)
* CFB	CURRENT FEEDBACK (316)
CMFA	ABSOLUTE VALUE CEMF (308)
CRM	CROSSOVER MODIFY (311)
DFP	DELAYED FIRING POWER (325)
* DR	DRIVER REFERENCE (333)
* EAO	ERROR AMP OUTPUT (333)
EST	EXTERNAL FLT STOP INPUT (314)
FALT	FAULT (314)
* FC	FIELD CURRENT (NS26)
FDR	FIELD DIAGNOSTIC REFERENCE (308)
FEA	FIELD ECONOMY ADJUST (325)
FF	FIELD FAULT (328)
IABS	MOTOR CURRENT ABSOLUTE (309)
ILA	CURRENT LIMIT ADJUST (323)
IMET	CURRENT SIGNAL FOR METER (310)
* IPU	INITIAL PULSE (320)
* LR	LOCAL REF. FROM DGC (333)
* JOG	JOG SWITCH INPUT (323)
* JOGR	JOG REFERENCE INPUT (331)
* MAC	MAX/MA CONTROL SIGNAL (320)
MSW	MODE SWITCH (330)
* OSC	OSCILLATOR (317)
* PCR	PHASE CONTROL REF. (326)
* PRE	DRIVE PRECONDITION (321)
ØSEQ	PHASE SEQUENCE (314)
RERR	REGULATOR ERROR (327)
RIJ	INTEGRATOR SUMMING JUNCTION (327)
RJ	REGULATOR SUMMING JUNCTION (331)
RRA	REGULATOR RESPONSE ADJUST (330)
RSET	RESET (316)
* RTR	READY TO RUN (316)
* RUN	RUN SWITCH INPUT (321)
* SA-C	PHASE SYN OUTPUT (316)
* SFB	SPEED FEEDBACK (320)
SMET	SPEED SIGNAL FOR METER (312)
* SR	SYSTEM REFERENCE INPUT (329)
* SYS	SYSTEM FAULT TRIP (313)
* TA	OUTPUT FOR TACHO TRIP ADJUST (320)
TF	TACHO FAULT (NS28)
* TFB	TACHOMETER FEEDBACK (320)
TFR	AC TACHO FREQUENCY OUTPUT (313)
* TR	TIMED REFERENCE (333)
* VFB	VOLTAGE FEEDBACK (319)
* WFR	WEAK FIELD REFERENCE (320)

(* - TEST POINT ON DOOR FRONT)

MAPPING SYSTEM

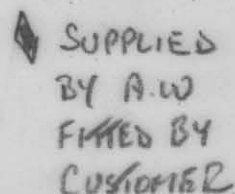
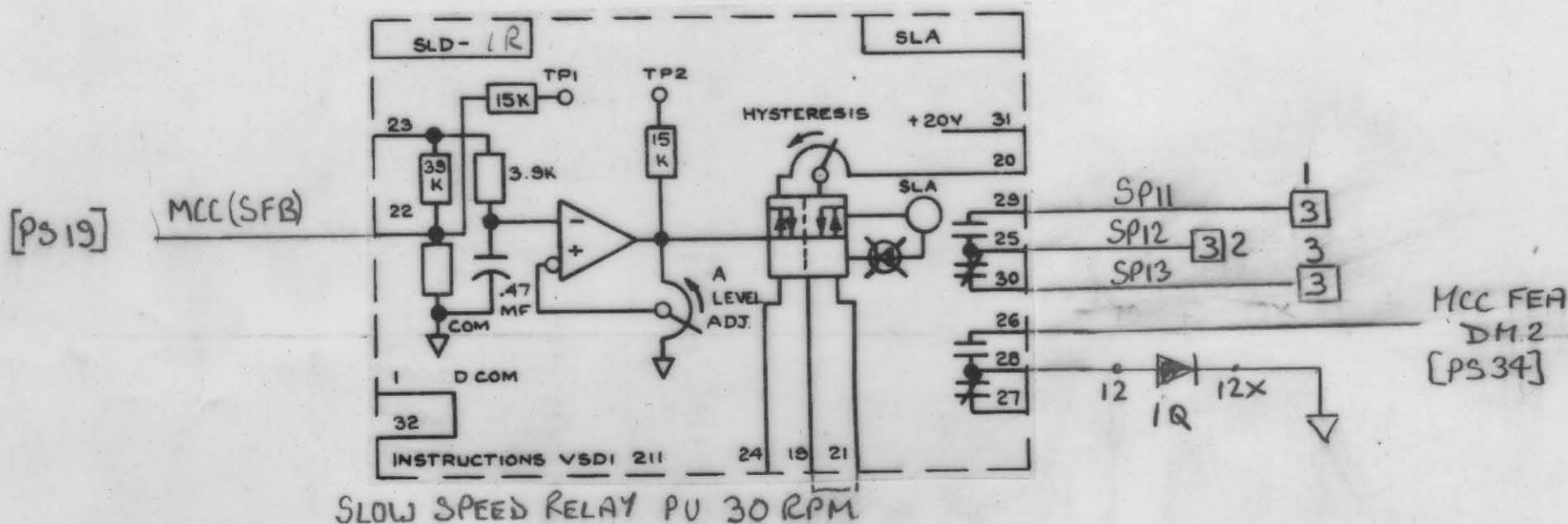
(NS/PS/TS) PS - PAST SHEET
NS - NEXT SHEET
TS - THIS SHEET

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1A, LINE 16 ETC.

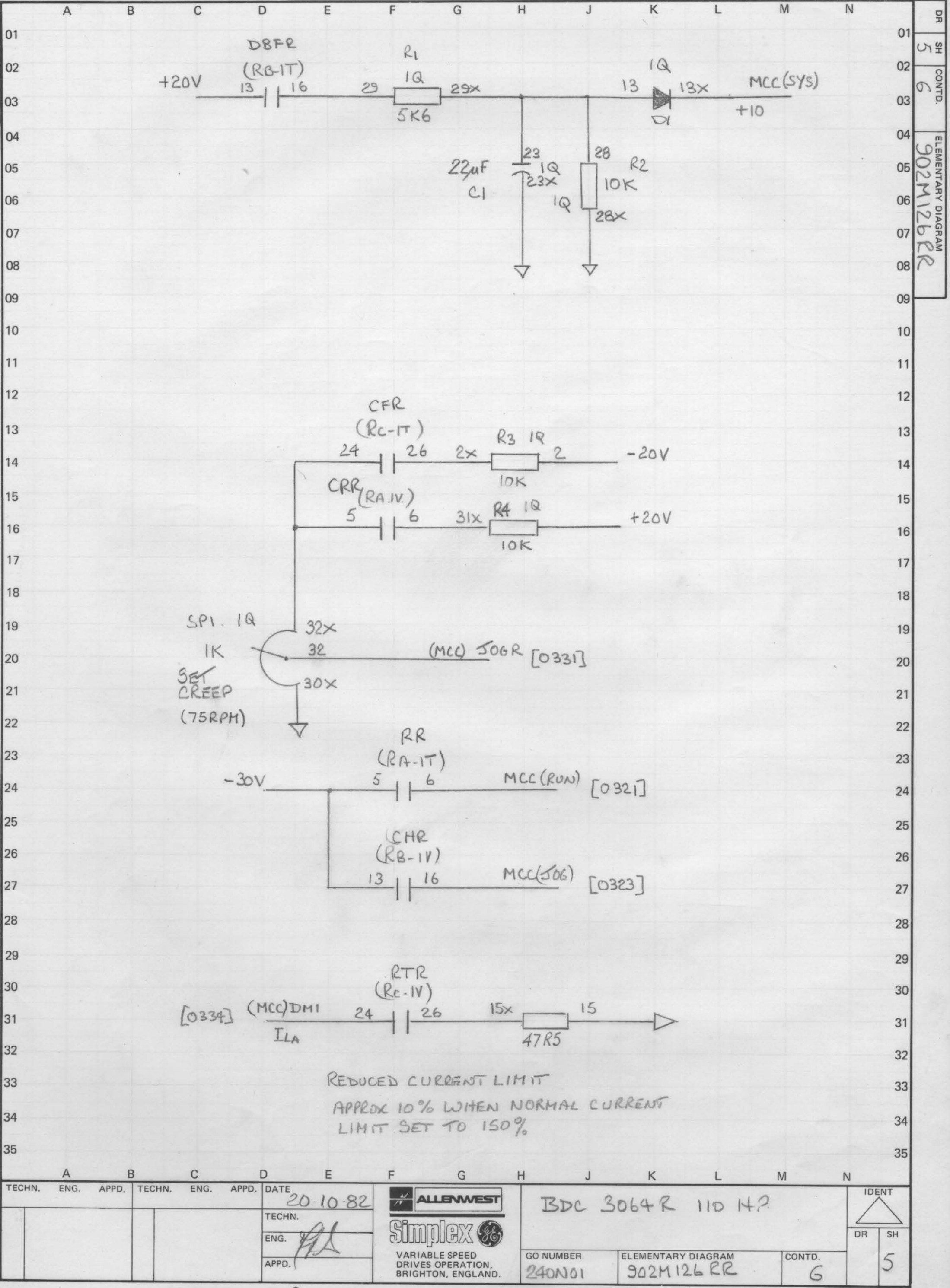
NOTE: FIELD EFFECT TRANSISTOR: THE
CLOSED/OPEN (I/O) STATE OF THESE
SWITCHED FOR "PRECONDITION" - "RUN"
OR JOG" - "DIAGNOSTIC STATIC" -
"DIAGNOSTIC RUN" IS SHOWN BY A
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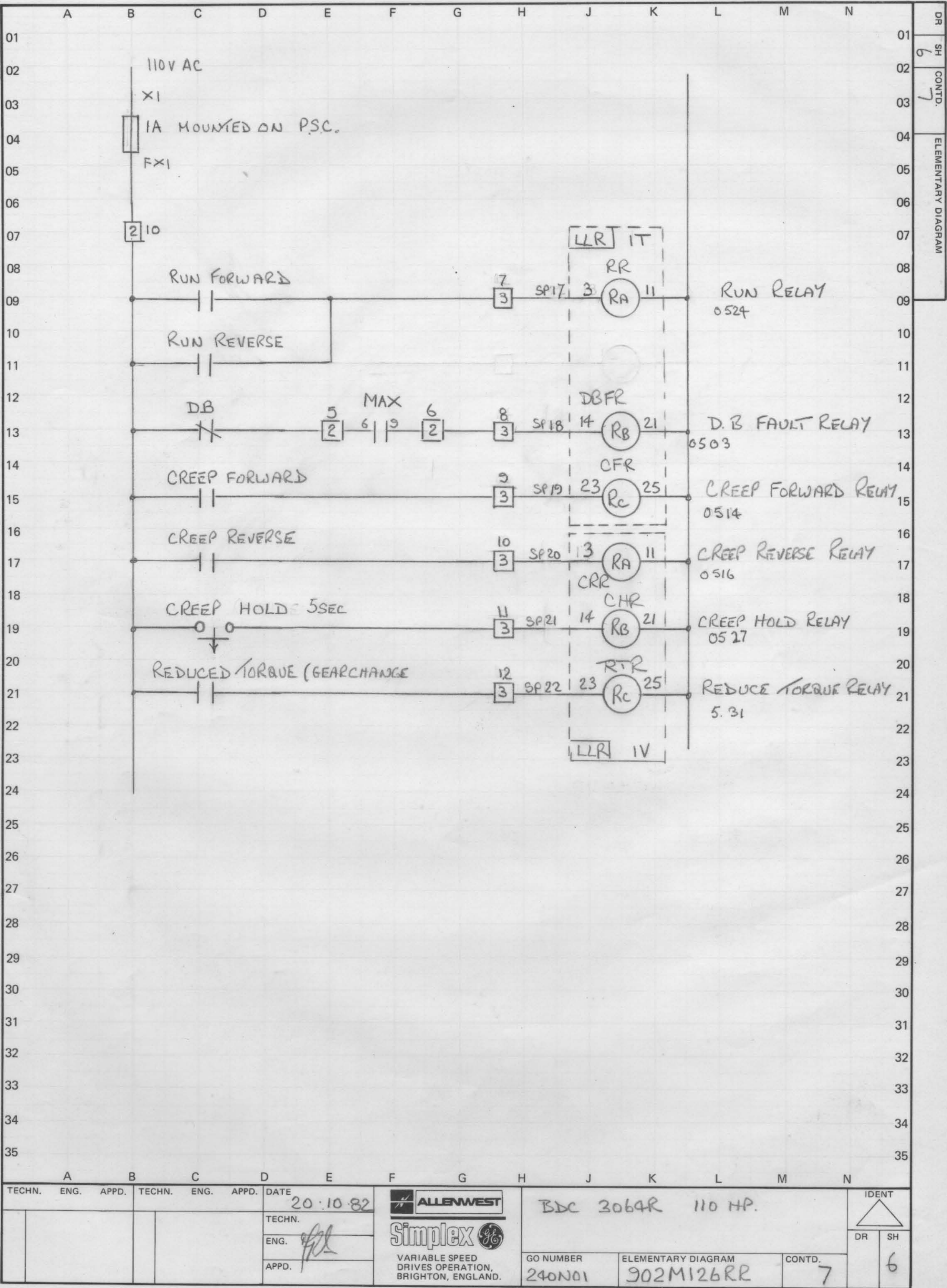
* FIT LINKS FOR 50 HZ FOR U.K. OPERATION
FIT LINKS FOR 60 HZ ON FINAL INSTALLATION.

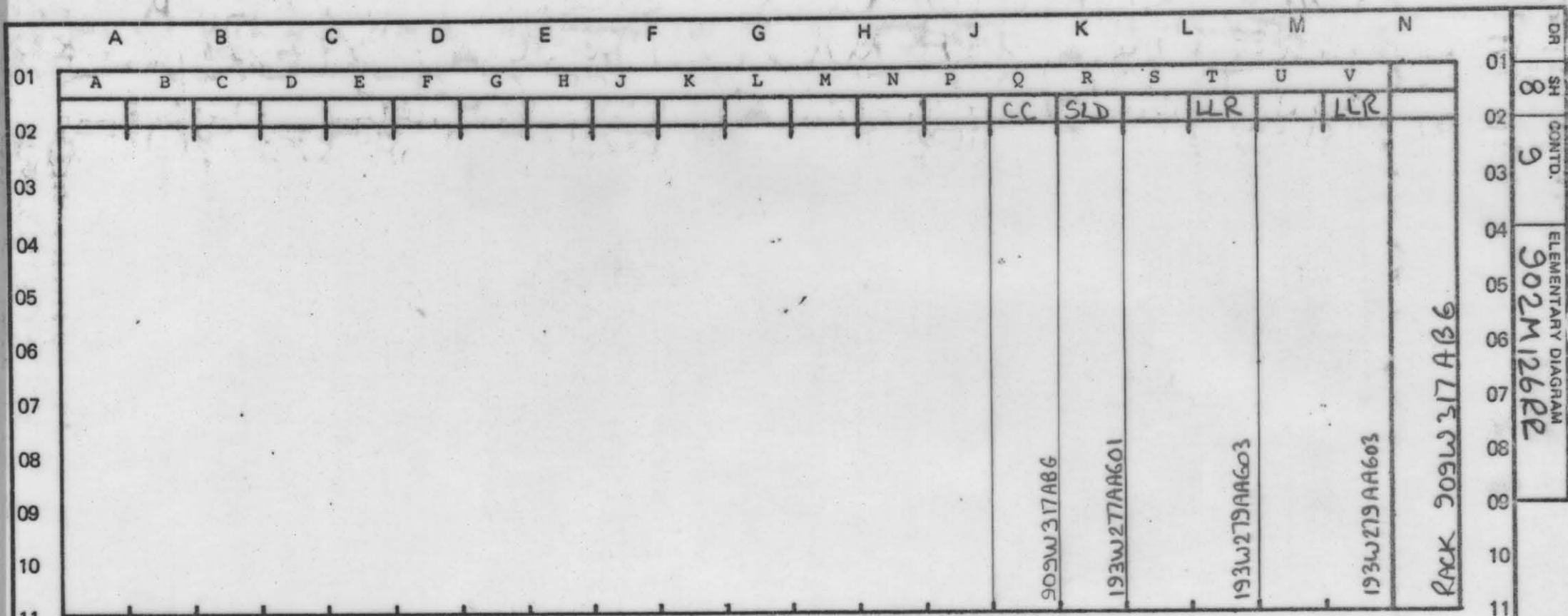
TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	20 10 82	ALLENWEST	BDC 3064R	110 H.P.	IDENT	
								Simplex	WEBSTER & BENNETT		DR	SH
								VARIABLE SPEED DRIVES OPERATION. BRIGHTON, ENGLAND.	GO NUMBER	ELEMENTARY DIAGRAM	CONTD.	
									240N01	902M126RR	2	1



Disclaimer Statement The trade mark is the trade mark of General Electric Company of U.S.A., which is not connected with the English Company of a similar name.







ON PRINTED CIRCUIT CARDS USED IN THIS RACK THE LETTERS 'AA' AFTER BASIC CATALOGUE NUMBER INDICATES ORIGINAL DESIGN. SUBSEQUENT DESIGNS WITH THE SAME BASIC NUMBERS AND GROUP NUMBER WITH THE SECOND LETTER CHANGED, SUCH AS: AB, AC, AD, ETC., ARE DIRECTLY INTERCHANGEABLE AND MAY BE SUPPLIED IN PLACE OF THE 'AA' CARDS.

THE PRINTED CIRCUIT CARD SHOULD ALWAYS BE REMOVED WITH THE CARD EXTRACTOR WHICH IS ATTACHED ON TOP OF THE CARD RACK. SOME CARDS CONTAIN PARTS WHICH WILL BE THERMALLY HOT AFTER BEING IN OPERATION. CARE SHOULD BE EXERCISED IN HANDLING ALL CARDS AFTER REMOVAL UNTIL THESE PARTS HAVE COOLED. DO NOT REMOVE OR INSERT CARDS WITH POWER APPLIED.

FRONT VIEW OF 54 PIN RECEPTACLE AS SEEN IN RACK CLOSED POSITION.

SYMBOLS:



TEST POST



POT ADJUSTMENT



INDICATING LIGHT

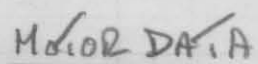
CARD RACK WIRE JUMPER TABLE

RIB-20 - 1Q02	MCC(SFB) - 1R22	1V26 - 1Q15X	
1Q02 - 1R02	1R13 - 1R21	1V25 - 1V21	SP11 - 1R29
	MCC(DM2) - 1R26	1V21 - 1V11	SP12 - 1R25
RIB COM - 1Q15	1R28 - 1Q12	1V11 - 1T11	SP13 1R30
1Q15 - 1R15	1R31 - 1T13	1T11 - 1T21	SP14
	1T16 - 1Q29	1T21 - 1T25	SP15
RIB+20 - 1Q31	1Q29X - 1Q28	1Q12X - 1Q23X	SP16
1Q31 - 1R31	1Q28 - 1Q23		SP17 - 1T 3
	1Q23 - 1Q13		SP18 - 1T 14
RIB-30 - 1T05	1Q28X - 1Q23X		SP19 - 1T 23
1T05 - 1V13	1Q23X - 1Q15		SP20 - 1V 3
RIBX2 - 1R25	1Q13X - MCC(SYS)		SP21 - 1V 14
	1Q2X - 1T26		SP22 - 1V 23
	1Q31X - 1V06		SP23
	1V05 - 1T24		
	1T24 - 1Q32X		
	1Q30X - 1Q28X		
	1Q32 - MCC(S06)		2TB6 - 3TB8 LINK
	1T06 - MCC(RUN)		
	1V16 - MCC(S06)		MA13 - 3TB14 RED 24/02
	MCC(DM1) - 1V14		MA14 3TB15

NOTE: RECEPTACLE PINS MAY BE NUMBERED AS SHOWN IN EITHER SKETCH. (PIN 33 CORRESPONDS TO PIN 1X, 34 TO 2X, ETC.)

TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	ALLENWEST			BDC 3064R 110 HP.			IDENT	
						20-10-82	Simplex						DR SH	
							VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.			GO NUMBER			7	
										ELEMENTARY DIAGRAM			8	
										CONTO.			7	

CONNECT CONTROL VT
PRIMARY FOR
CORRECT VOLTAGE



HP 110
VA 460
IA 203
RPM 0/750/1500

$$\begin{matrix} V_F \\ I_F \end{matrix}$$

A			B			C			D			E			F			G			H			J			K			L			M			N					
TECHN.			ENG.			APPD.			TECHN.			ENG.			APPD.			DATE																							
																		BDC 3064R 110 HP						IDENT 																	
						NC REF ADDED			TECHN.			ENG.			APPD.			VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.						DR SH																	
						15-12-82												GO NUMBER 240N01						ELEMENTARY DIAGRAM 902M126RR									CONTD. 10								

A B C D E F G H J K L M N

VOLTAGE POLARITIES SHOWN ARE FOR MOTORING DA1(+)

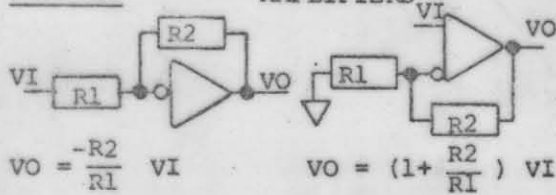
SIGNAL DEFINITIONS AND LOCATIONS

HARDWARE ABBREVIATIONS

MCC MAIN CONTROL CARD
 IFC INTERFACE CARD
 PSC POWER SUPPLY CARD
 SCR THYRISTOR ASSEMBLY
 DGC DIAGNOSTIC CARD
 MFC MOTOR FIELD CONTROL
 MFE MOTOR FIELD EXCITER
 MDR MODIFICATION RACK
 ACC AUXILIARY CONTROL CARD

SYMBOLS

AMPLIFIERS



CASE GROUND

 $VO = \text{SIGN} () \times \text{ABSOLUTE VALUE OF } VI$
 STAB ON TERMINAL

 TERMINAL AT 2TB, 3TB, 4TB, RTB.
 EX: 9 [2] - 2TB9; X2 [R] - RTBX2

TERMINAL AT T.B.'s

 POTENTIOMETER ARROWS ON THE CARD
 ELEMENTARY DIAGRAMS INDICATE THE
 WIPER DIRECTION AS THE POTENTIOMETER
 SHAFT IS ROTATED CLOCKWISE TO INCREASE
 FUNCTION.

 THESE RESISTORS ARE CRIMPED IN WIRE
 HARNESS.

FUNCTION	USE	LOC	JUMPERS
60HZ		MFC	ZA-ZB (IF USED)
50HZ		MCC	HZA - PHA
IOC-400%	X		(NONE)
-500%		IFC	I - IHI
-300%		IFC	I - ILO
SR5 - 9v	X		(NONE)
9 - 20v		MCC	SRH - COM
JOGR 10v			(NONE)
20v	X	MCC	JH - COM
LT.3-7sec.			(NONE)
2 - 60sec	X		332Ω FROM LTI TO COM
VREG			NT-CEMF CC-COM
DC TACHO	X		(NONE)
AC TACHO		MCC	AT1 - AT2
TACHO FILT		IFC	TC - TC
TACHO V.		IFC	NT-NT1 PT - PT1
24-64vdc		IFC	NT-NT1 PT - PT1
27-71vac		IFC	NT-NT2 PT - PT2
60-160vdc		IFC	NT-NT2 PT - PT2
66-177vac		IFC	NT-NT3 PT - PT3
110-300vdc	X	IFC	NT-NT3 PT - PT3
120-300vac		IFC	NT-NT3 PT - PT3
G134 G256		IFC	MFC OR MFE
1.3 1.7		ME	NONE
1.3 2.8		ME	YB - YD
2.4 5.0		ME	YA - YB
4.0 8.0		ME	YA-YB, YC-YD
7.0 13		ME	YA - YC
11 25		ME	YA-YC, YB-YD
L/R < .25S	X	MFC	QA - QB
INH RUN		DGC	D1-D2 (IF USED)
INH DRV CL		MCC	DC1 - COM
FUSELESS		ACC	CFY - CFX
175/200% DCL	X	MCC	DCX - DCY
	X	MFC	LPD - LPI

SEE NOTE
SH 10

* CEMF COUNTER EMF (16)
 * CFB CURRENT FEEDBACK (16)
 CMFA ABSOLUTE VALUE CEMF (08)
 CRM CROSSOVER MODIFY (11)
 DFP DELAYED FIRING POWER (25)
 * DR DRIVER REFERENCE (33)
 * EAO ERROR AMP OUTPUT (33)
 EST EXTERNAL FLT STOP INPUT (14)
 FALT FAULT (14)
 * FC FIELD CURRENT (NS26)
 FDR FIELD DIAGNOSTIC REFERENCE (08)
 FEA FIELD ECONOMY ADJUST (25)
 FF FIELD FAULT (28)
 IABS MOTOR CURRENT ABSOLUTE (09)
 ILA CURRENT LIMIT ADJUST (23)
 IMET CURRENT SIGNAL FOR METER (10)
 * IPU INITIAL PULSE (20)
 * LR LOCAL REF. FROM DGC (33)
 * JOG JOG SWITCH INPUT (23)
 * JOGR JOG REFERENCE INPUT (31)
 * MAC MAX/MA CONTROL SIGNAL (20)
 MSW MODE SWITCH (30)
 * OSC OSCILLATOR (17)
 * PCR PHASE CONTROL REF. (26)
 * PRE DRIVE PRECONDITION (21)
 ØSEQ PHASE SEQUENCE (14)
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 RIJ INTEGRATOR SUMMING JUNCTION (27)
 RJ REGULATOR SUMMING JUNCTION (31)
 RRA REGULATOR RESPONSE ADJUST (30)
 RSET RESET (16)
 * RTR READY TO RUN (16)
 * RUN RUN SWITCH INPUT (21)
 * SA-C PHASE SYN OUTPUT (16)
 * SFB SPEED FEEDBACK (20)
 SMET SPEED SIGNAL FOR METER (12)
 * SR SYSTEM REFERENCE INPUT (29)
 * SYS SYSTEM FAULT TRIP (13)
 * TA OUTPUT FOR TACHO TRIP ADJUST (20)
 TF TACHO FAULT (NS28)
 * TFB TACHOMETER FEEDBACK (20)
 TFR AC TACHO FREQUENCY OUTPUT (13)
 * TR TIMED REFERENCE (33)
 * VFB VOLTAGE FEEDBACK (19)
 * WFR WEAK FIELD REFERENCE (20)

(* - TEST POINT ON DOOR FRONT)

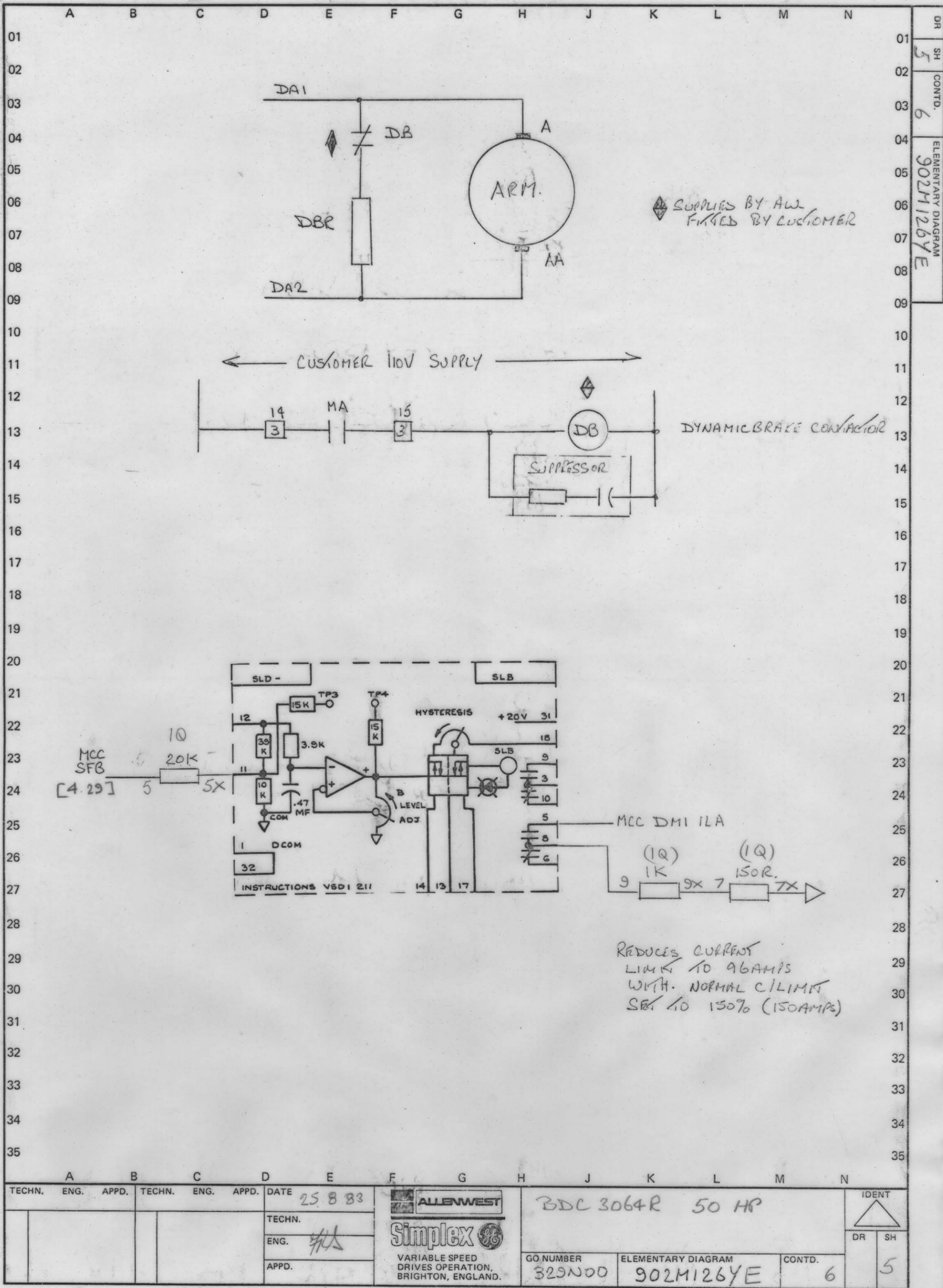
MAPPING SYSTEM

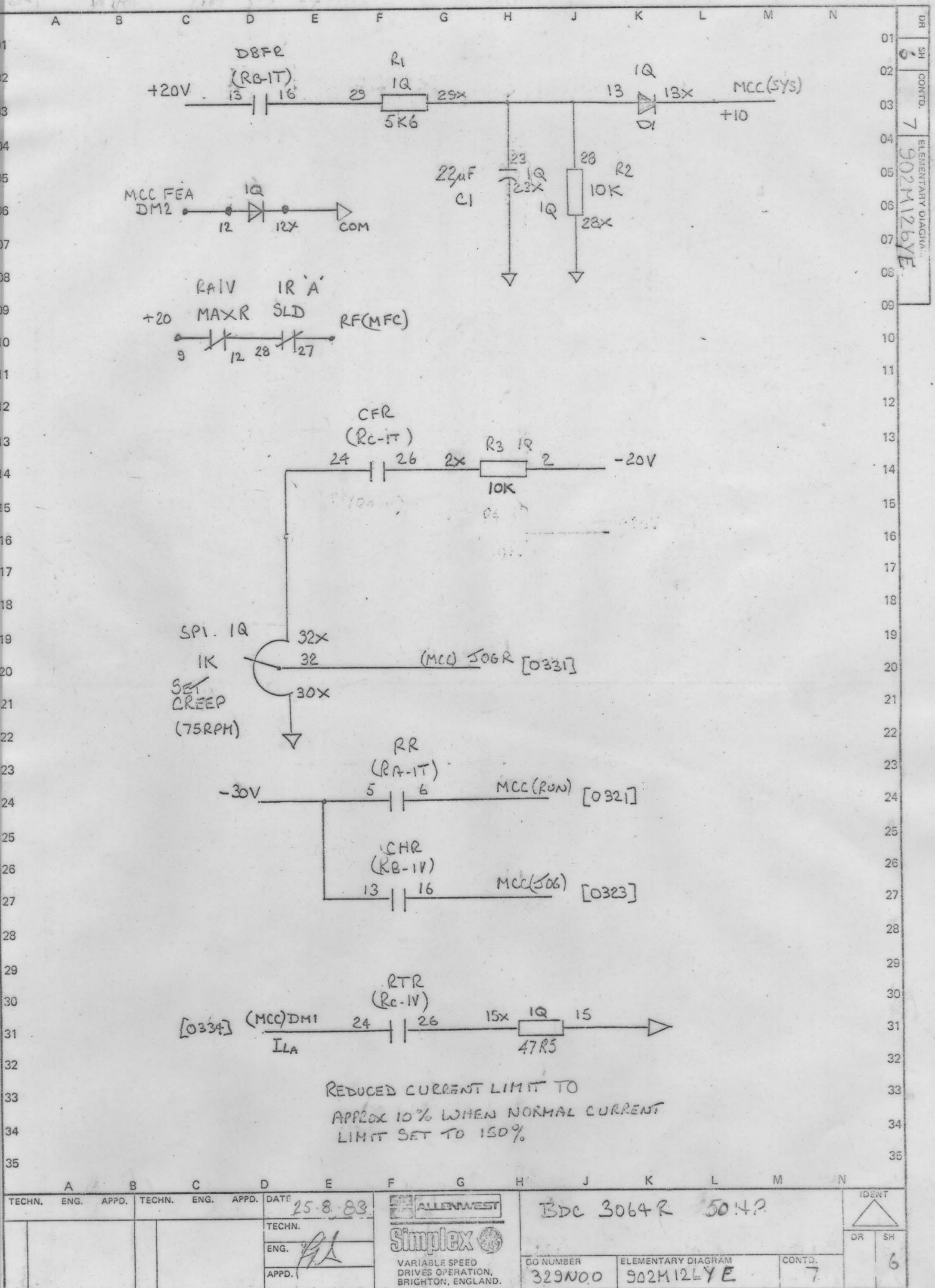
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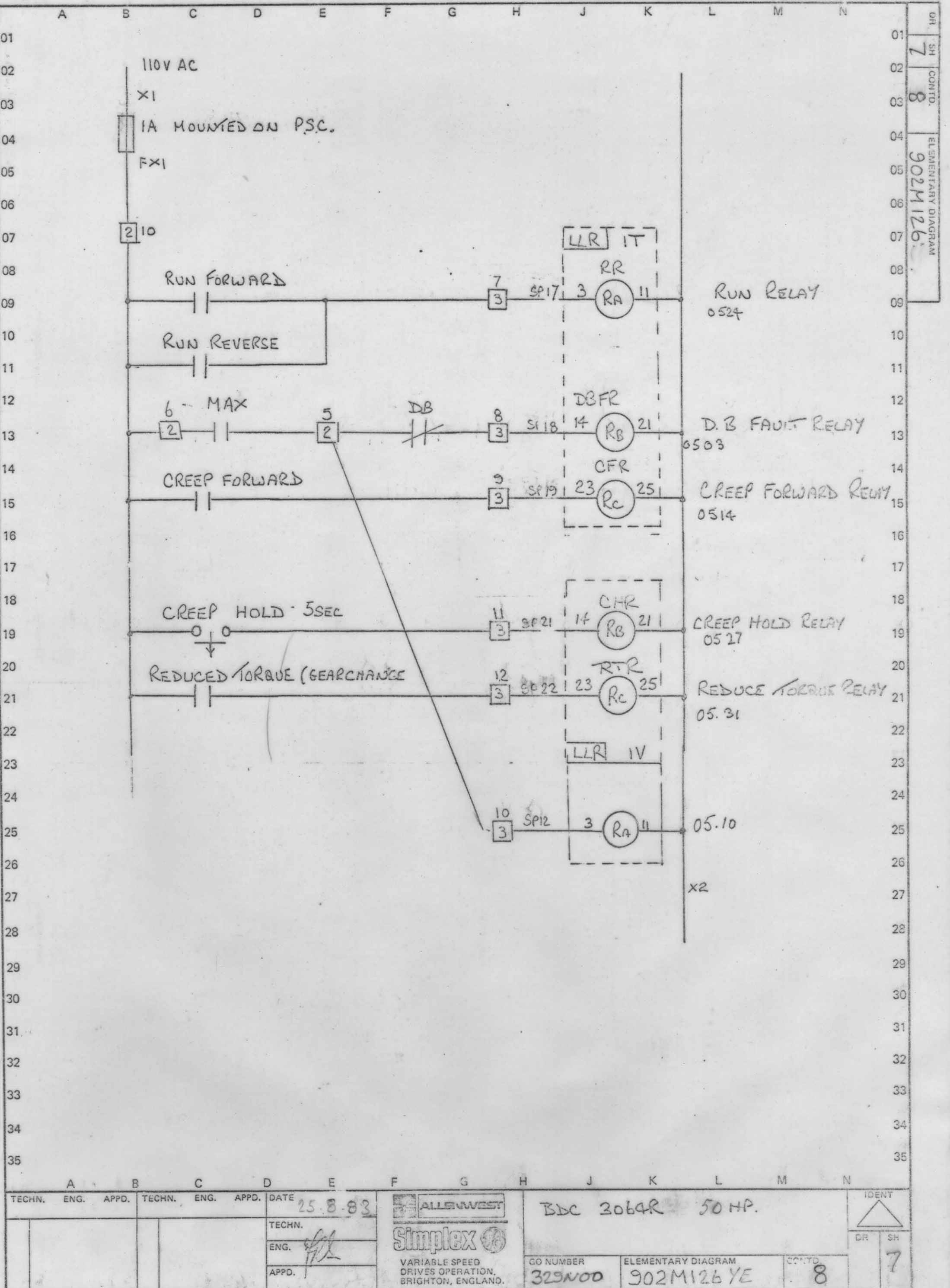
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 OR JOG" - "DIAGNOSTIC STATIC" -
 "DIAGNOSTIC RUN" IS SHOWN BY A
 FOUR DIGIT WORD WITH STATE SEQUENCE.

TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	25.8.83	 VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.	BDC 3064R 50HP (37KW)		IDENT DR SH 1		
2 AS SHIPPED						TECHN.			(WICKMAN LTD)				
25.10.83						ENG.			GO NUMBER	329N00		ELEMENTARY DIAGRAM	902M126YE







OR
7
SH
CONTO.
8
ELEMENTARY DIAGRAM
902M126

TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	25.8.83	ALLENWEST	BDC 3064R 50 HP.	IDENT	DR	SH
						TECHN.		Simplex				
						ENG.		VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.	GO NUMBER 329N00	ELEMENTARY DIAGRAM 902M126 YE	CONTO. 8	7
						APPD.						

	A	B	C	D	E	F	G	H	J	K	L	M	N
01										CC	SLD	ILR	ILR
02													
03													
04													
05													
06													
07													
08													
09													
10													
11													

902W317A6625

193W277A6602

193W279A6603

193W279A6603

RACK 902W317A6621

CR SH CONTO. ELEMENTARY DIAGRAM
9 10 902M126YE

ON PRINTED CIRCUIT CARDS USED IN THIS RACK THE LETTERS 'AA' AFTER BASIC CATALOGUE NUMBER INDICATES ORIGINAL DESIGN. SUBSEQUENT DESIGNS WITH THE SAME BASIC NUMBERS AND GROUP NUMBER WITH THE SECOND LETTER CHANGED, SUCH AS: AB, AC, AD, ETC., ARE DIRECTLY INTERCHANGEABLE AND MAY BE SUPPLIED IN PLACE OF THE 'AA' CARDS.

THE PRINTED CIRCUIT CARD SHOULD ALWAYS BE REMOVED WITH THE CARD EXTRACTOR WHICH IS ATTACHED ON TOP OF THE CARD RACK. SOME CARDS CONTAIN PARTS WHICH WILL BE THERMALLY HOT AFTER BEING IN OPERATION. CARE SHOULD BE EXERCISED IN HANDLING ALL CARDS AFTER REMOVAL UNTIL THESE PARTS HAVE COOLED. DO NOT REMOVE OR INSERT CARDS WITH POWER APPLIED.

FRONT VIEW OF 64 PIN RECEPTACLE AS SEEN IN RACK CLOSED POSITION.

SYMBOLS:

● TEST POST ⊗ POT ADJUSTMENT ✕ INDICATING LIGHT

32	- 64	32	- 32X
31	- 63	31	- 31X
30	- 62	30	- 30X
29	- 61	29	- 29X
28	- 60	28	- 28X
27	- 59	27	- 27X
26	- 58	26	- 26X
25	- 57	25	- 25X
24	- 56	24	- 24X
23	- 55	23	- 23X
22	- 54	22	- 22X
21	- 53	21	- 21X
20	- 52	20	- 20X
19	- 51	19	- 19X
18	- 50	18	- 18X
17	- 49	17	- 17X
16	- 48	16	- 16X
15	- 47	15	- 15X
14	- 46	14	- 14X
13	- 45	13	- 13X
12	- 44	12	- 12X
11	- 43	11	- 11X
10	- 42	10	- 10X
9	- 41	9	- 9X
8	- 40	8	- 8X
7	- 39	7	- 7X
6	- 38	6	- 6X
5	- 37	5	- 5X
4	- 36	4	- 4X
3	- 35	3	- 3X
2	- 34	2	- 2X
1	- 33	1	- 1X

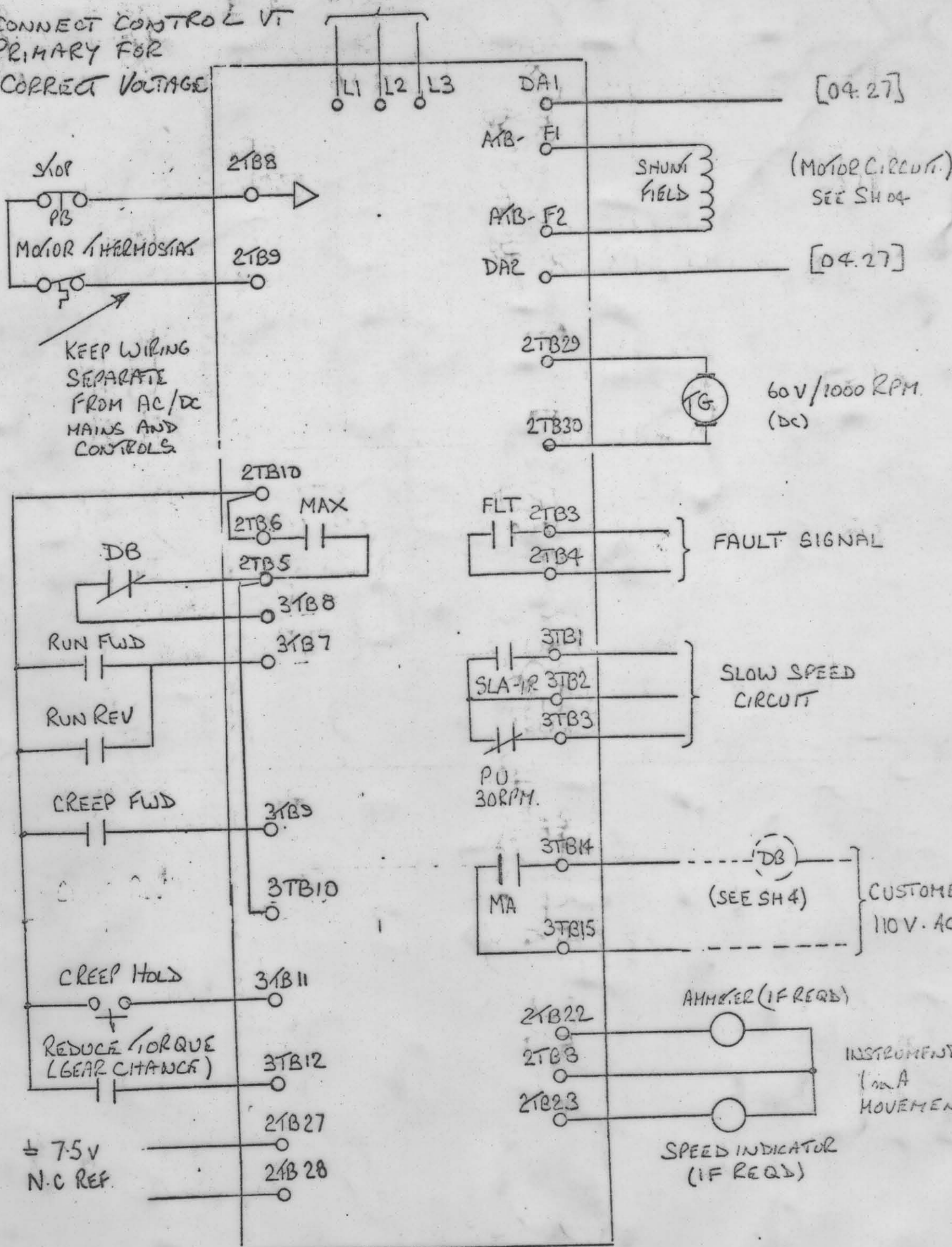
CARD RACK WIRE JUMPER TABLE			
R/B-20 - 1Q02	MCC(SYG) - 1R22	1V26 - 1Q15X	
1Q02 - 1R02	1R19 - 1R21	1V25 - 1V21	SP11 - 1R29
	MCC(BM2) - 1Q12	1V21 - 1V11	SP12 - 1R25
R/B COM - 1Q15	1Q12X - 1Q23X	1V11 - 1T11	SP13 - 1R30
1Q15 - 1R15	1R31 - 1T13	1T11 - 1T21	SP14
	1T16 - 1Q29	1T21 - 1T25	SP15
R/B+20 - 1Q31	1Q29X - 1Q28		SP16
1Q31 - 1R31	1Q28 - 1Q23		SP17 - 1T3
	1Q23 - 1Q13		SP18 - 1T14
R/B-30 - 1T05	1Q28X - 1Q23X		SP19 - 1T23
1T05 - 1V13	1Q23X - 1Q15		SP20 - 1V3
R/Bx2 - 1R25	1Q13X - MCC(SYG)		SP21 - 1V14
	1Q2X - 1T26		SP22 - 1V23
		1R22 - 1Q5	
		1Q5X - 1R11	2TB5 - 3TB10
		1R5 - 1V24	2TB10 - 2TB6
1R27 - RF(MFC)	1T24 - 1Q32X	1R8 - 1Q9	
1R28 - 1V12	1Q30X - 1Q28X	1Q9X - 1Q7	
1V9 - 1R31	1Q32 - MCC(SYG)	1Q7X - 1Q15	
1LA DM1 MCC	1T06 - MCC(BM2)	1R13 - 1R17	MA13 - 3TB14
FEA - DM2 MCC	1V16 - MCC(SYG)		MA14 - 3TB15
	MCC DM1 - 1V14		

NOTE: RECEPTACLE PINS MAY BE NUMBERED AS SHOWN IN EITHER SKETCH. (PIN 33 CORRESPONDS TO PIN 1X, 34 TO 2X, ETC.)

TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	TECHN.	ENG.	APPD.	DATE	TECHN.	ENG.	APPD.	DATE	TECHN.	ENG.	APPD.	DATE
						25.8.83												
						VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.						GO NUMBER 329W00						
												ELEMENTARY DIAGRAM 902M126YE						
												CONTO. 10						
												IDENT 9						

NOTE SEE SH1 FOR 50/60 HZ SUPPLY
LINKS 420/460V 50/60 HZ

CONNECT CONTROL VT
PRIMARY FOR
CORRECT VOLTAGE



MOTOR DATA

HP 50/50/50
VA 400/500/500
IA 107/82/82
RPM 850/1100/2550

VF 300V
IF 375/375/1.2A.

TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	25.8.83	<p>VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.</p>	BDC 3064R 50 HP		<p>DR SH</p> <p>10</p>
						TECHN.			GO NUMBER	ELEMENTARY DIAGRAM	
						ENG.			329N00	902M126YE	
						APPD.					

Disclaimer Statement The trade mark is the trade mark of General Electric Company of U.S.A., which is not connected with the English Company of a similar name.

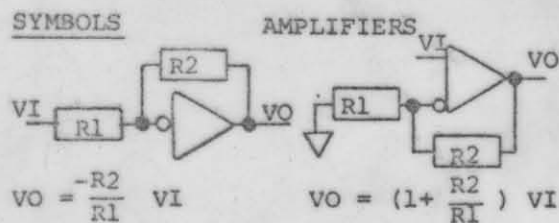
A B C D E F G H J K L M N

VOLTAGE POLARITIES SHOWN ARE FOR MOTORING DA1(+)

HARDWARE ABBREVIATIONS

MCC MAIN CONTROL CARD
 IFC INTERFACE CARD
 PSC POWER SUPPLY CARD
 SCR THYRISTOR ASSEMBLY
 DGC DIAGNOSTIC CARD
 MFC MOTOR FIELD CONTROL
 MFE MOTOR FIELD EXCITER
 MDR MODIFICATION RACK
 ACC AUXILIARY CONTROL CARD

SYMBOLS



CASE GROUND

 $VO = \text{SIGN} () \times \text{ABSOLUTE VALUE OF VI}$

STAB ON TERMINAL

 TERMINAL AT 2TB, 3TB, 4TB, RTB.
 EX: 9 [2] - 2TB9; X2 [R] - RTBX2

TERMINAL AT T.B.'s

 POTENTIOMETER ARROWS ON THE CARD
 ELEMENTARY DIAGRAMS INDICATE THE
 WIPER DIRECTION AS THE POTENTIOMETER
 SHAFT IS ROTATED CLOCKWISE TO INCREASE
 FUNCTION.

 THESE RESISTORS ARE CRIMPED IN WIRE
 HARNESS.

FUNCTION	USE	LOC	JUMPERS
60HZ		MFC	ZA-ZB (IF USED)
50HZ	X	MCC	HZA - PHA
IOC-400%	X		(NONE)
-500%		IFC	I - IHI
-300%		IFC	I - ILO
SR5 - 9v	X		(NONE)
9 - 20v		MCC	SRH - COM
JOGR 10v			(NONE)
20v	X	MCC	JH - COM
LT.3-7sec.	X		(NONE)
2 - 60sec			332Ω FROM LTI TO COM
VREG			NT-CEMF CC-COM
DC TACHO	X		(NONE)
AC TACHO		MCC	AT1 - AT2
TACHO FILT		IFC	TC - TC
TACHO V.		IFC	NT-NT1 PT - PT1
24-64vdc		IFC	NT-NT1 PT - PT1
27-71vac		IFC	NT-NT2 PT - PT2
60-160vdc		IFC	NT-NT2 PT - PT2
66-177vac		IFC	NT-NT3 PT - PT3
110-300vdc	X	IFC	NT-NT3 PT - PT3
120-300vac		IFC	NT-NT3 PT - PT3
G134 G256		IFC	MFC OR MFE
1.7		ME	NONE
1.3		ME	YB - YD
2.4		ME	YA - YB
4.0		ME	YA-YB, YC-YD
7.0		ME	YA - YC
13		ME	YA-YC, YB-YD
13		ME	YA-YC, YB-YD
L/R < .25S		MFC	QA - QB
INH RUN		DGC	D1-D2 (IF USED)
INH DRV CL		MCC	DC1 - COM
FUSELESS		ACC	CFY - CFX
200% DCL	X	MCC	DCX-DCY
REC CL	X	MCC	ILA - DMI
FLD RLDY	X	MCC	FEA DM3

SIGNAL DEFINITIONS AND LOCATIONS

* CEMF COUNTER EMF (16)
 * CFB CURRENT FEEDBACK (16)
 CMFA ABSOLUTE VALUE CEMF (08)
 CRM CROSSOVER MODIFY (11)
 DFP DELAYED FIRING POWER (25)
 * DR DRIVER REFERENCE (33)
 * EAO ERROR AMP OUTPUT (33)
 EST EXTERNAL FLT STOP INPUT (14)
 FALT FAULT (14)
 * FC FIELD CURRENT (NS26)
 FDR FIELD DIAGNOSTIC REFERENCE (08)
 FEA FIELD ECONOMY ADJUST (25)
 FF FIELD FAULT (28)
 IABS MOTOR CURRENT ABSOLUTE (09)
 ILA CURRENT LIMIT ADJUST (23)
 IMET CURRENT SIGNAL FOR METER (10)
 * IPU INITIAL PULSE (20)
 * LR LOCAL REF. FROM DGC (33)
 * JOG JOG SWITCH INPUT (23)
 * JOGR JOG REFERENCE INPUT (31)
 * MAC MAX/MA CONTROL SIGNAL (20)
 MSW MODE SWITCH (30)
 * OSC OSCILLATOR (17)
 * PCR PHASE CONTROL REF. (26)
 * PRE DRIVE PRECONDITION (21)
 ØSEQ PHASE SEQUENCE (14)
 RERR REGULATOR ERROR (27)
 RIJ INTEGRATOR SUMMING JUNCTION (27)
 RJ REGULATOR SUMMING JUNCTION (31)
 RRA REGULATOR RESPONSE ADJUST (30)
 RSET RESET (16)
 * RTR READY TO RUN (16)
 * RUN RUN SWITCH INPUT (21)
 * SA-C PHASE SYN OUTPUT (16)
 * SFB SPEED FEEDBACK (20)
 SMET SPEED SIGNAL FOR METER (12)
 * SR SYSTEM REFERENCE INPUT (29)
 * SYS SYSTEM FAULT TRIP (13)
 * TA OUTPUT FOR TACHO TRIP ADJUST (20)
 TF TACHO FAULT (NS28)
 * TFB TACHOMETER FEEDBACK (20)
 TFR AC TACHO FREQUENCY OUTPUT (13)
 * TR TIMED REFERENCE (33)
 * VFB VOLTAGE FEEDBACK (19)
 * WFR WEAK FIELD REFERENCE (20)

(* - TEST POINT ON DOOR FRONT)

MAPPING SYSTEM

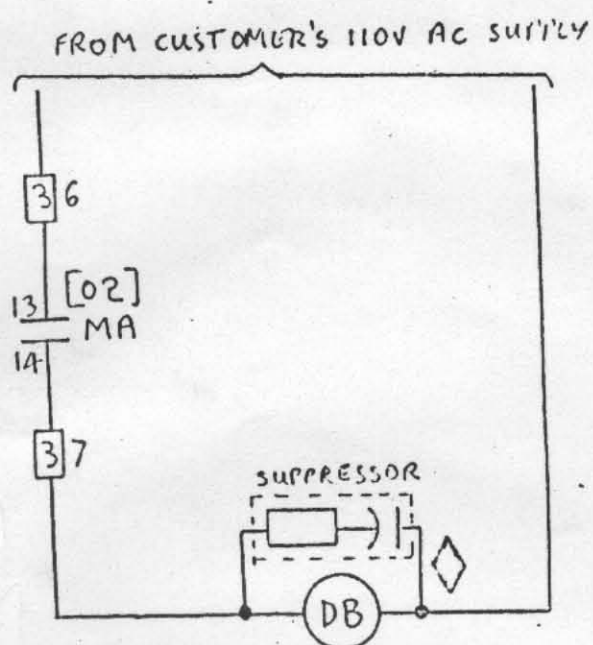
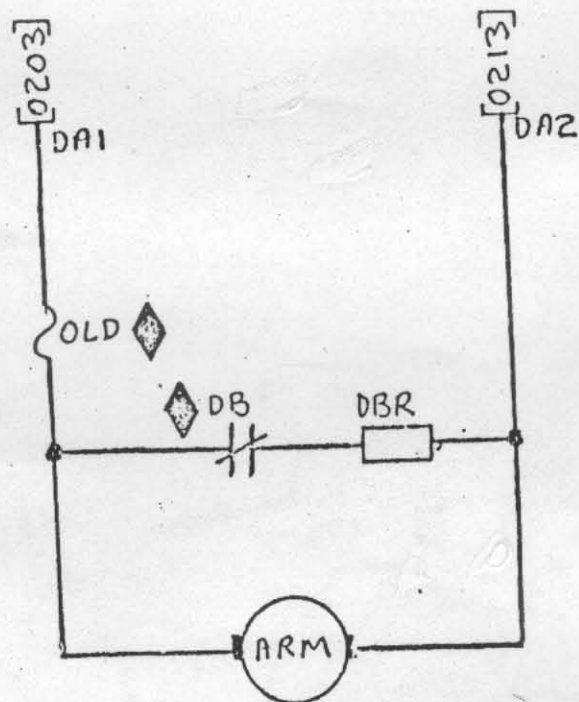
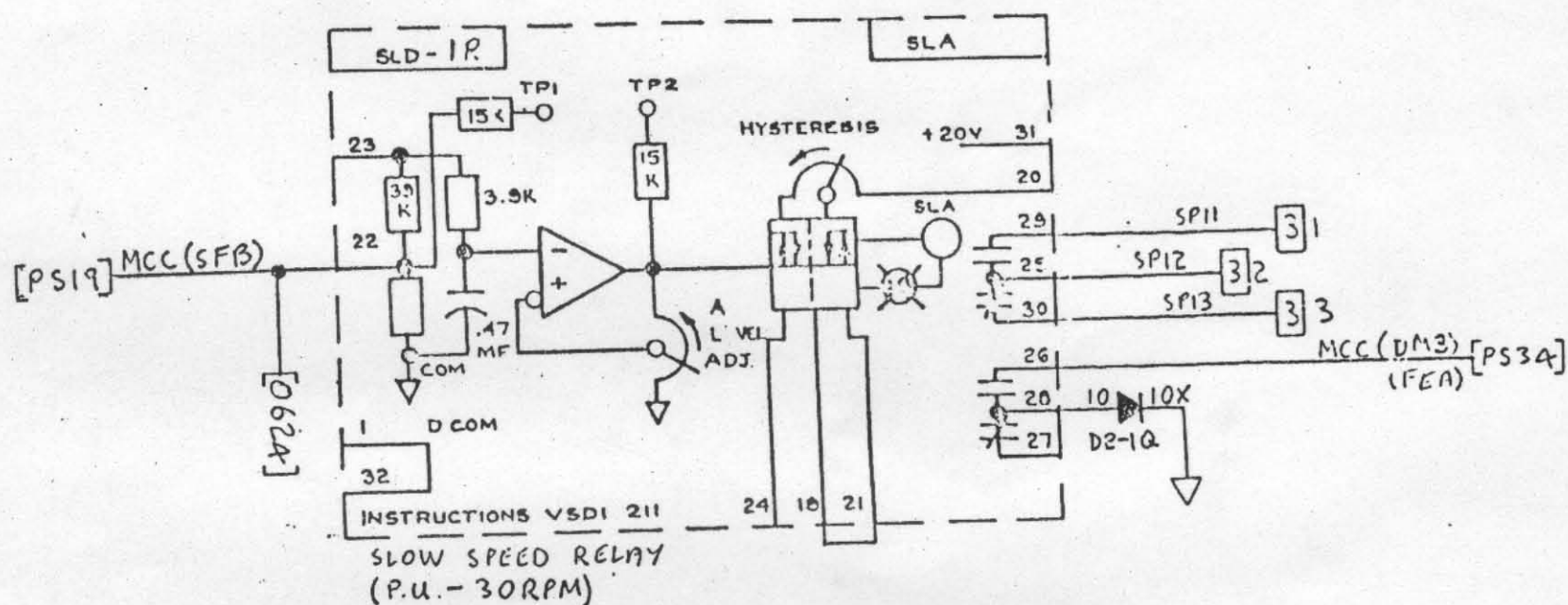
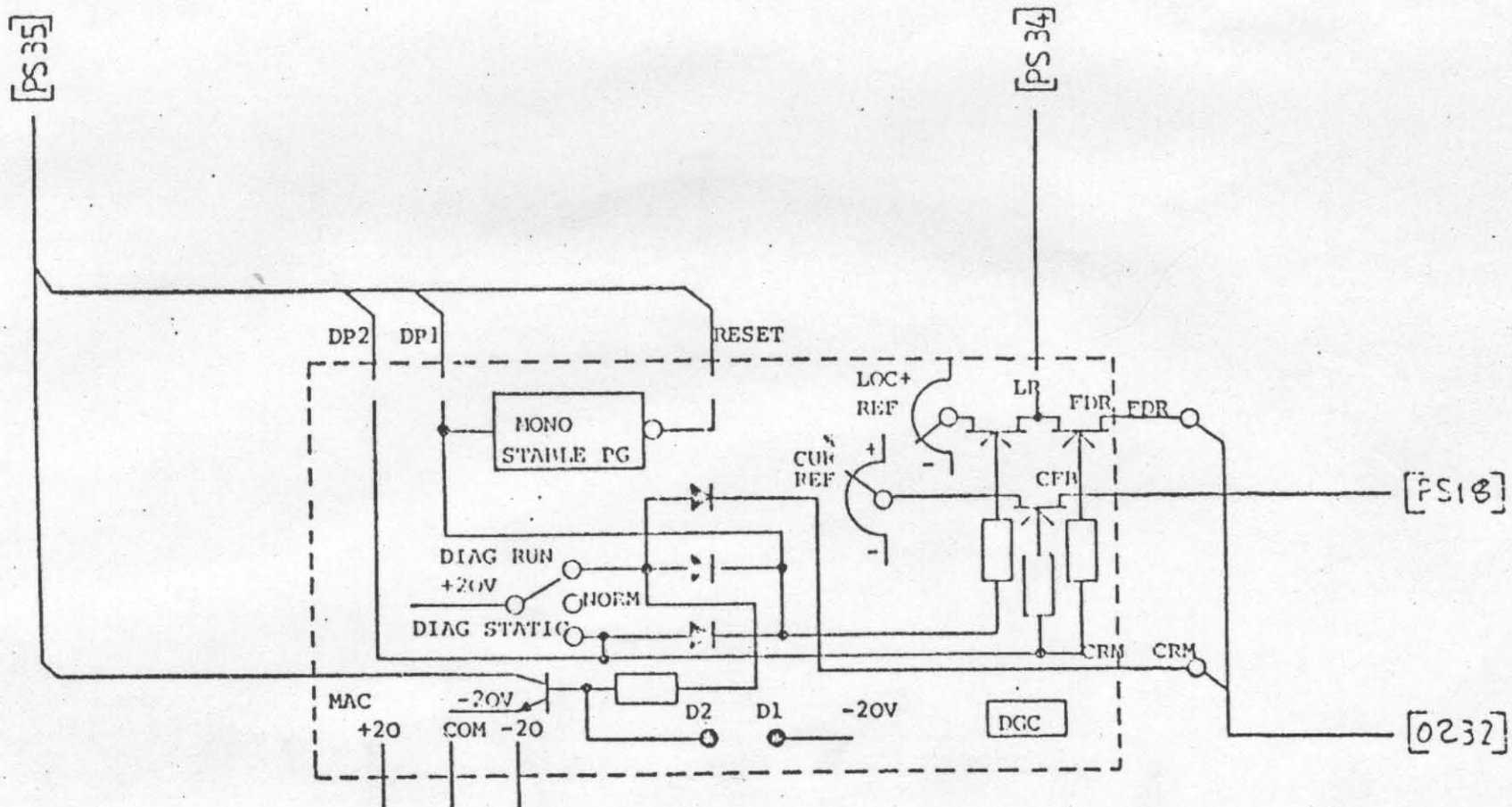
(NS/PS/TS) PS - PAST SHEET
 NS - NEXT SHEET
 TS - THIS SHEET

HENCE (PS - 12) DENOTES LOCATION ON PAST SHEET LINE 12. OTHER LOCATIONS ARE
 DENOTED BY SHEET NUMBER AND LINE? E.G. (1A16) SIGNIFIES LOCATION ON SHEET
 1A, LINE 16 ETC.

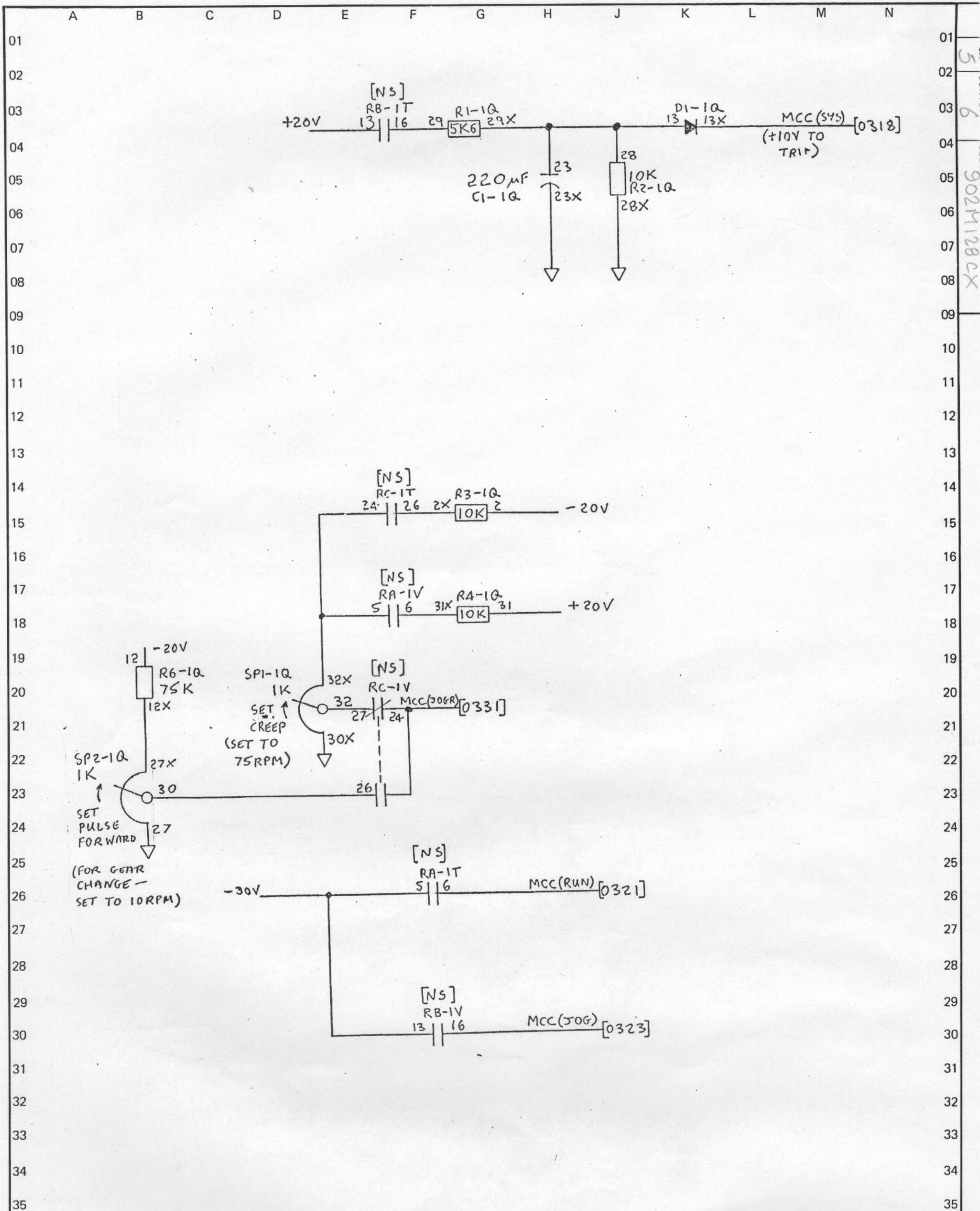
NOTE: FIELD EFFECT TRANSISTOR: THE
 CLOSED/OPEN (I/O) STATE OF THESE
 SWITCHED FOR "PRECONDITION" - "RUN"
 OR JOG" - "DIAGNOSTIC STATIC" -
 "DIAGNOSTIC RUN" IS SHOWN BY A
 FOUR DIGIT WORD WITH STATE SEQUENCE.

SEE ALSO 906P118CD

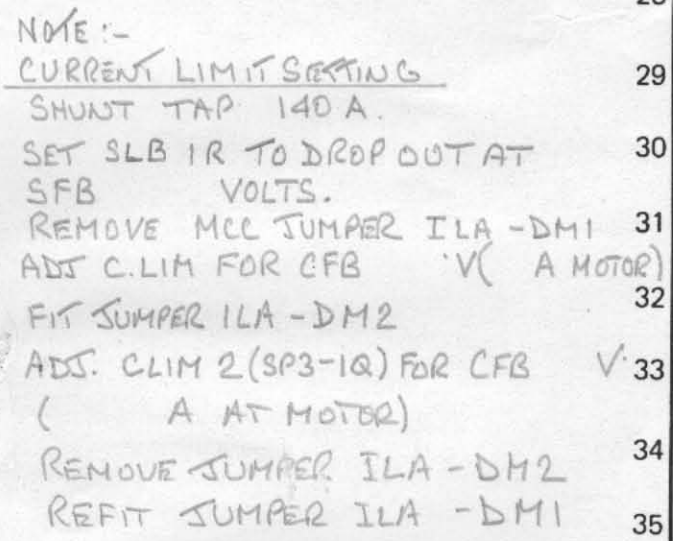
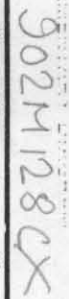
TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	23.4.86			BDC 3064R 50HP FOR KETLOW UK LTD (WICKMAN BENNETT)			IDENT		
							TECHN.							DR SH	
							ENG.							1	
							APPD.								
							VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.			GO NUMBER 6042075			ELEMENTARY DIAGRAM 902M128CX		
										CONTD.					

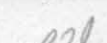


TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	ALLENWEST		50 HP. 3064R		IDENT	
						24.4.86	Simplex		KETZON (W. BANNETT)		DR SH	
							VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.		GO NUMBER 6042075		ELEMENTARY DIAGRAM 902M128CX	
									CONTD. 5		4	



TECHN.		ENG.		APPD.		DATE		24.4.86							
TECHN.		ENG.		APPD.		DATE				GO NUMBER 6042075		ELEMENTARY DIAGRAM 902M128CX		CONTD. 6	
TECHN.		ENG.		APPD.		DATE				IDENT DR		SH 5			



A			B			C			D			E			F			G			H			J			K			L			M			N																	
TECHN.			ENG.			APPD.			TECHN.			ENG.			APPD.			DATE 24. A 86			 Simplex VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.												IDENT 																				
									TECHN.																																												
									ENG.																																												
									APPD.																																												
																								GO NUMBER 6042075						ELEMENTARY DIAGRAM 902 M 128 CX						CONTD. 7									DR			SH			6		

	A	B	C	D	E	F	G	H	J	K	L	M	N	
01														01
02														02
03														03
04														04
05														05
06														06
07														07
08														08
09														09
10														10
11														11

ON PRINTED CIRCUIT CARDS USED IN THIS RACK THE LETTERS 'AA' AFTER BASIC CATALOGUE NUMBER INDICATES ORIGINAL DESIGN. SUBSEQUENT DESIGNS WITH THE SAME BASIC NUMBERS AND GROUP NUMBER WITH THE SECOND LETTER CHANGED, SUCH AS: AB, AC, AD, ETC., ARE DIRECTLY INTERCHANGEABLE AND MAY BE SUPPLIED IN PLACE OF THE 'AA' CARDS.

THE PRINTED CIRCUIT CARD SHOULD ALWAYS BE REMOVED WITH THE CARD EXTRACTOR WHICH IS ATTACHED ON TOP OF THE CARD RACK. SOME CARDS CONTAIN PARTS WHICH WILL BE THERMALLY HOT AFTER BEING IN OPERATION. CARE SHOULD BE EXERCISED IN HANDLING ALL CARDS AFTER REMOVAL UNTIL THESE PARTS HAVE COOLED. DO NOT REMOVE OR INSERT CARDS WITH POWER APPLIED.

FRONT VIEW OF 64 PIN
RECEPTACLE AS SEEN
IN RACK CLOSED
POSITION.

SYMBOLS:

● TEST POST



POT ADJUSTMENT



INDICATING LIGHT

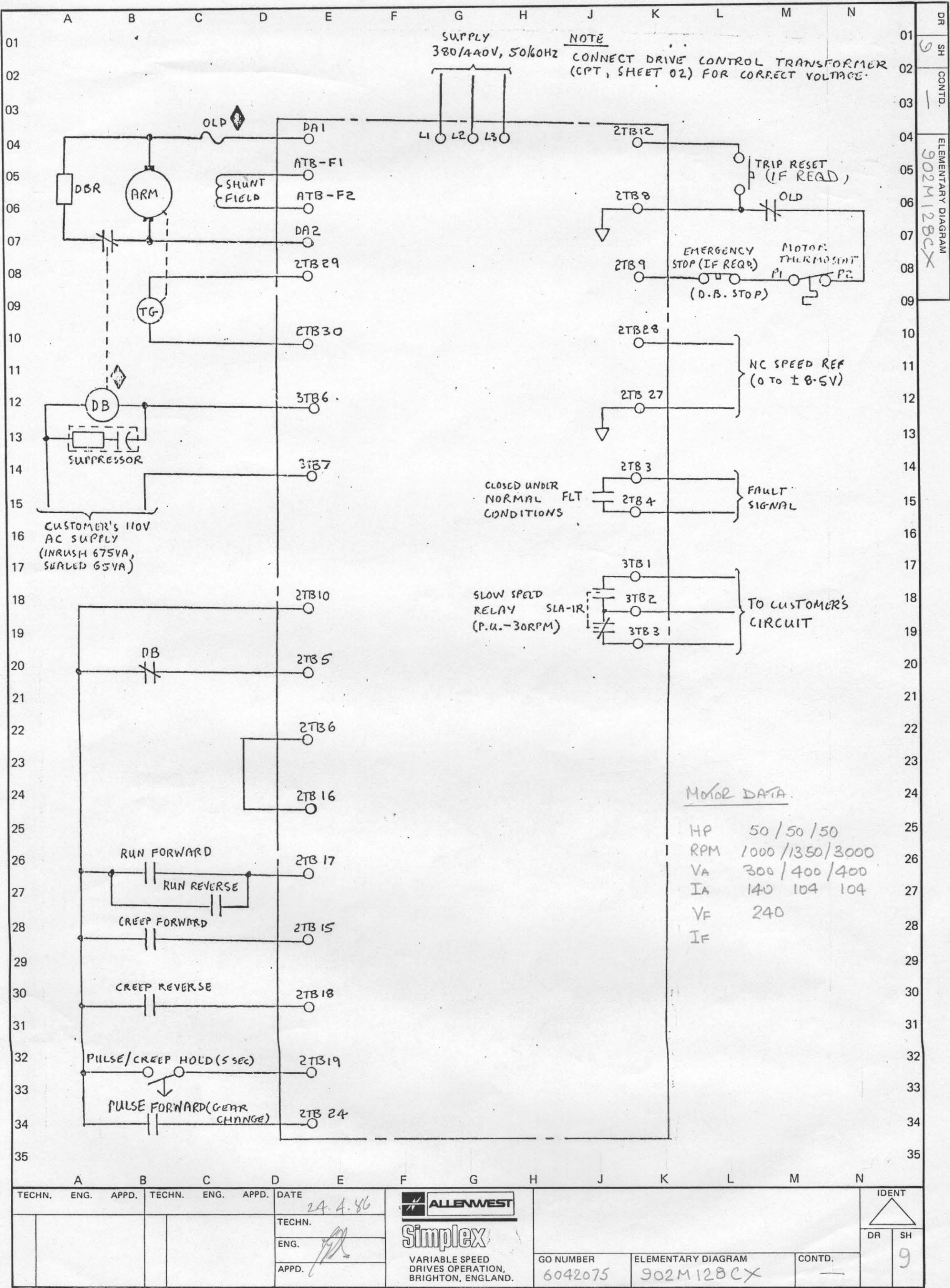
32 - 64	32 - 32X
31 - 63	31 - 31X
30 - 62	30 - 30X
29 - 61	29 - 29X
28 - 60	28 - 28X
27 - 59	27 - 27X
26 - 58	26 - 26X
25 - 57	25 - 25X
24 - 56	24 - 24X
23 - 55	23 - 23X
22 - 54	22 - 22X
21 - 53	21 - 21X
20 - 52	20 - 20X
19 - 51	19 - 19X
18 - 50	18 - 18X
17 - 49	17 - 17X
16 - 48	16 - 16X
15 - 47	15 - 15X
14 - 46	14 - 14X
13 - 45	13 - 13X
12 - 44	12 - 12X
11 - 43	11 - 11X
10 - 42	10 - 10X
9 - 41	9 - 9X
8 - 40	8 - 8X
7 - 39	7 - 7X
6 - 38	6 - 6X
5 - 37	5 - 5X
4 - 36	4 - 4X
3 - 35	3 - 3X
2 - 34	2 - 2X
1 - 33	1 - 1X

NOTE: RECEPTACLE PINS MAY
BE NUMBERED AS SHOWN
IN EITHER SKETCH. (PIN
33 CORRESPONDS TO PIN
1X, 34 TO 2X, ETC.)

CARD RACK WIRE JUNCTION TABLE

RTB(-20V)-1Q02	MCC(SFB)-1R22	1R22-1R11	
1Q02-1R02	1R19-1R21	1V25-1V21	SP2-1T23
RTB(COM)-1Q15	MCC(DM3)-1R26	1V21-1V11	SP3-1T14
1Q15-1R15	1R28-1Q10	1V11-1T11	SP4-1T03
RTB(+20V)-1Q31	1R31-1T13	1T11-1T21	SP5-1V03
1Q31-1R31	1T16-1Q29	1T21-1T25	SP6-1V14
RTB(-30V)-1T05	1Q29X-1Q28	1Q02-1Q12	SP7-1V23
1T05-1V13	1Q28-1Q23	1Q12X-1Q27X	2TB6-2TB16
RTB(X2)-1T25	1Q23-1Q13	1Q30-1V26	SP11-1R29
	1Q28X-1Q23X	1Q27-1Q23X	SP12-1R25
	1Q23X-1Q15	1V24-MCC(506R)	SP13-1R30
	1Q13X-MCC(SYS)	1R08-1Q25	
	1Q2X-1T26	1Q25-1Q25X	
	1Q31X-1V06	1Q22-1Q15X	
	1V05-1T24	1R13-1R17	MA13-3TB6
	1T24-1Q32X	1Q25X-MCC(DM2)	MA14-3TB7
	1Q30X-1Q28X	1Q10X-1Q27	30/025 RED
	1Q32-1V27		
	1T06-MCC(RUN)		
	1V16-MCC(506)		
	MCC(DM1)-1R05		

TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	24.4.86			ALLENWEST			IDENT	
						TECHN.								
						ENG.								
						APPD.								
						Simplex			VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.					
						GO NUMBER			ELEMENTARY DIAGRAM			CONTD.		
						6042075			902M128CX			8		
												7		



A B C D E F G H J K L M N

VOLTAGE POLARITIES SHOWN ARE FOR MOTORING DA1(+)

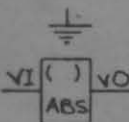
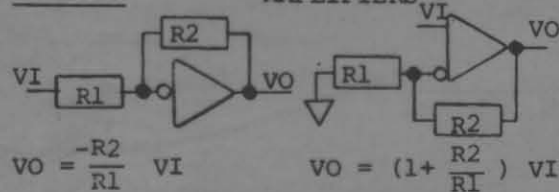
SIGNAL DEFINITIONS AND LOCATIONS

HARDWARE ABBREVIATIONS

MCC MAIN CONTROL CARD
 IFC INTERFACE CARD
 PSC POWER SUPPLY CARD
 SCR THYRISTOR ASSEMBLY
 DGC DIAGNOSTIC CARD
 MFC MOTOR FIELD CONTROL
 MFE MOTOR FIELD EXCITER
 MDR MODIFICATION RACK
 ACC AUXILIARY CONTROL CARD

SYMBOLS

AMPLIFIERS



CASE GROUND

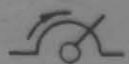
VO = SIGN () X ABSOLUTE VALUE OF VI

STAB ON TERMINAL

TERMINAL AT 2TB, 3TB, 4TB, RTB.

EX: 9 [2] - 2TB9; X2 [R] - RTBx2

TERMINAL AT T.B.'s



POTENTIOMETER ARROWS ON THE CARD

ELEMENTARY DIAGRAMS INDICATE THE
 WIPER DIRECTION AS THE POTENTIOMETER
 SHAFT IS ROTATED CLOCKWISE TO INCREASE
 FUNCTION.

▲ THESE RESISTORS ARE CRIMPED IN WIRE
 HARNESS.

FUNCTION	USE	LOC	JUMPERS
60HZ		MFC	ZA-ZB (IF USED)
50HZ	X	MCC	HZA - PHA
IOC-400%	X		(NONE)
-500%		IFC	I - IHI
-300%		IFC	I-ILO
SR5 - 9v	X		(NONE)
9 - 20v		MCC	SRH - COM
JOGR 10v			(NONE)
20v	X	MCC	JH - COM
LT.3-7sec	X		(NONE)
2 - 60sec			332Ω FROM LTI TO COM
VREG			NT-CEMF CC-COM
DC TACHO			(NONE)
AC TACHO	X	MCC	AT1 - AT2
TACHO FILT		IFC	TC - TC
TACHO V.		IFC	NT-NT1 PT - PT1
24-64vdc	X	IFC	NT-NT1 PT - PT1
27-71vac		IFC	NT-NT2 PT - PT2
60-160vdc		IFC	NT-NT2 PT - PT2
66-177vac		IFC	NT-NT3 PT - PT3
110-300vdc		IFC	NT-NT3 PT - PT3
120-300vac		IFC	NT-NT3 PT - PT3
G134 G256		IFC	MFC OR MFE
1.3 1.7		ME	NONE
2.4 2.8		ME	VB - YD
4.0 5.0		ME	VA - YB
7.0 8.0	X	ME	VA-YB, YC-YD
13 15		ME	VA - YC
25 25		ME	VA-YC, YB-YD
L/R < .25S		MFC	QA - QB
INH RUN		DGC	D1-D2 (IF USED)
INH DRV CL		MCC	DC1 - COM
FUSELESS		ACC	CFY - CFY
200% DR CL	X	MCC	DCX - DCY
REG. CL	X	MCC	ILA - DMI
FIELD FROM DMM	X	MCC	FEA - DM2

* CEMF COUNTER EMF (16)
 * CFB CURRENT FEEDBACK (16)
 CMFA ABSOLUTE VALUE CEMF (08)
 CRM CROSSOVER MODIFY (11)
 DFP DELAYED FIRING POWER (25)
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 * EAO ERROR AMP OUTPUT (33)
 EST EXTERNAL FLT STOP INPUT (14)
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 * FC FIELD CURRENT (NS26)
 FDR FIELD DIAGNOSTIC REFERENCE (08)
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 * OSC OSCILLATOR (17)
 * PCR PHASE CONTROL REF. (26)
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 ØSEQ PHASE SEQUENCE (14)
 RERR REGULATOR ERROR (27)
 RIJ INTEGRATOR SUMMING JUNCTION (27)
 RJ REGULATOR SUMMING JUNCTION (31)
 RRA REGULATOR RESPONSE ADJUST (30)
 RSET RESET (16)
 * RTR READY TO RUN (16)
 * RUN RUN SWITCH INPUT (21)
 * SA-C PHASE SYN OUTPUT (16)
 * SFB SPEED FEEDBACK (20)
 SMET SPEED SIGNAL FOR METER (12)
 * SR SYSTEM REFERENCE INPUT (29)
 * SYS SYSTEM FAULT TRIP (13)
 * TA OUTPUT FOR TACHO TRIP ADJUST (20)
 TF TACHO FAULT (NS28)
 * TFB TACHOMETER FEEDBACK (20)
 TFR AC TACHO FREQUENCY OUTPUT (13)
 * TR TIMED REFERENCE (33)
 * VFB VOLTAGE FEEDBACK (19)
 * WFR WEAK FIELD REFERENCE (20)

(* - TEST POINT ON DOOR FRONT)

MAPPING SYSTEM

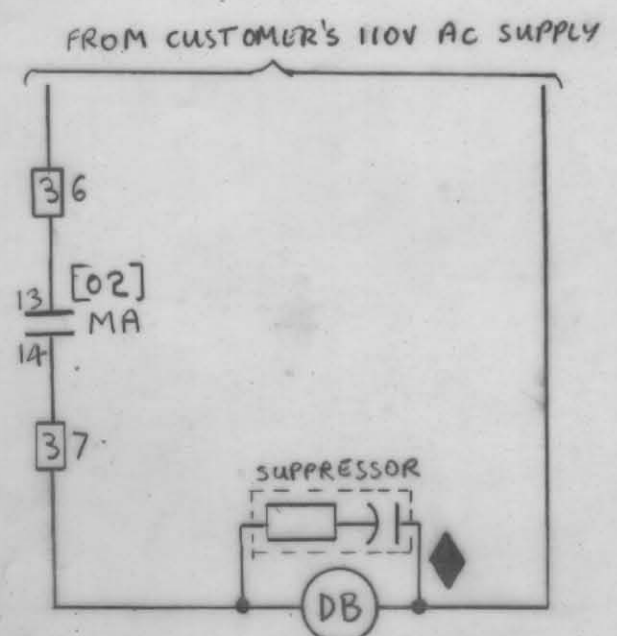
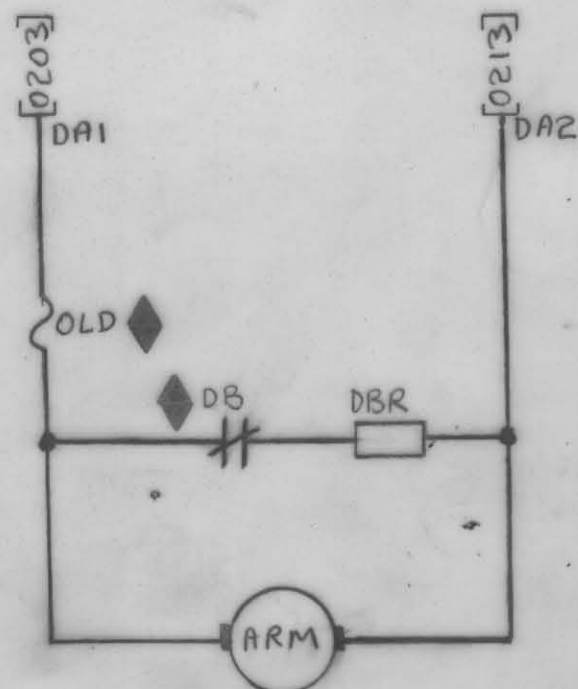
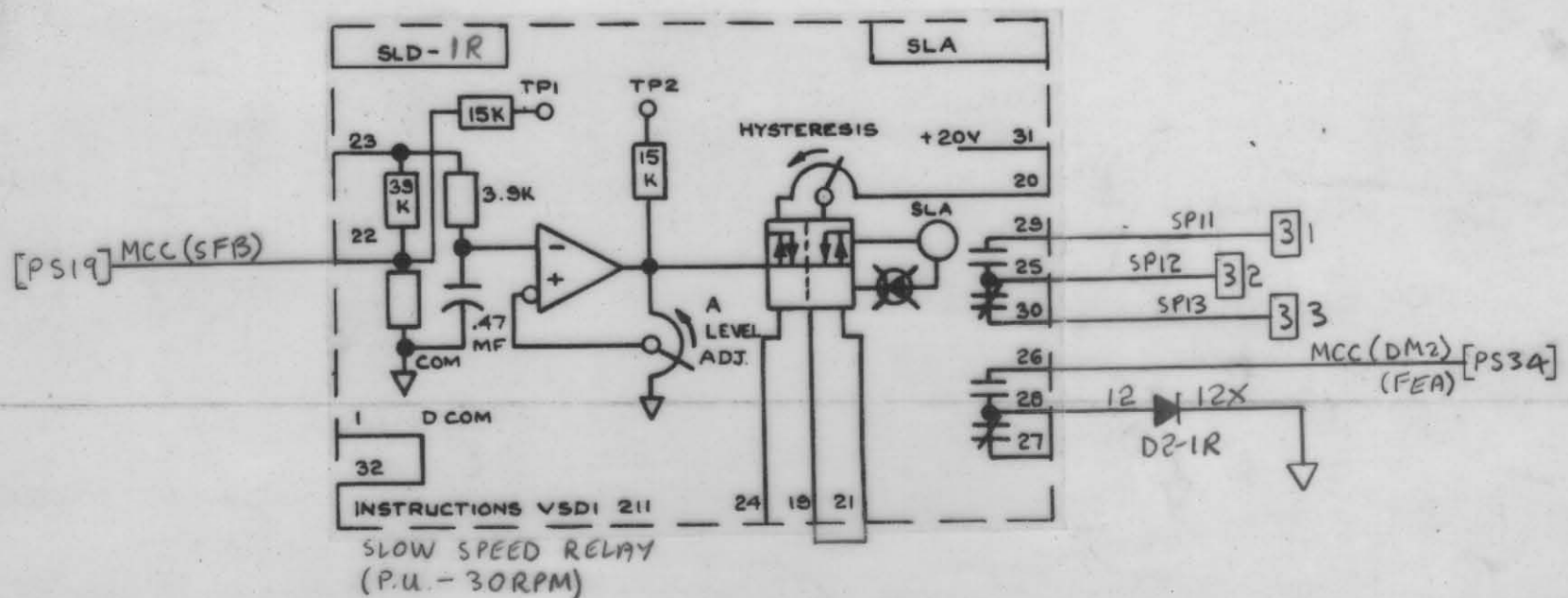
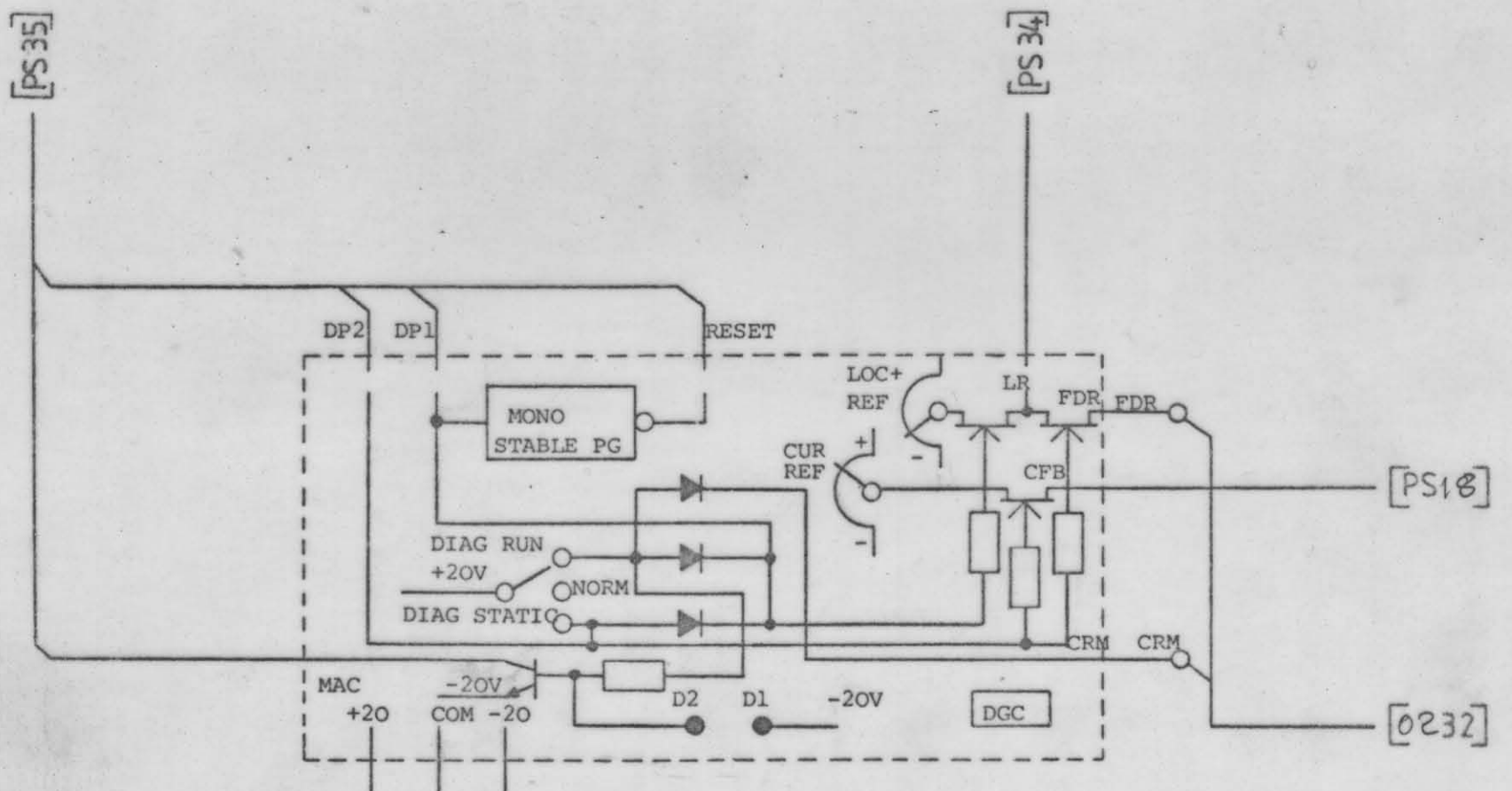
(NS/PS/TS) PS - PAST SHEET
 NS - NEXT SHEET
 TS - THIS SHEET

HENCE (PS - 12) DENOTES LOCATION ON PAST SHEET LINE 12. OTHER LOCATIONS ARE
 DENOTED BY SHEET NUMBER AND LINE? E.G. (1A16) SIGNIFIES LOCATION ON SHEET
 1A, LINE 16 ETC.

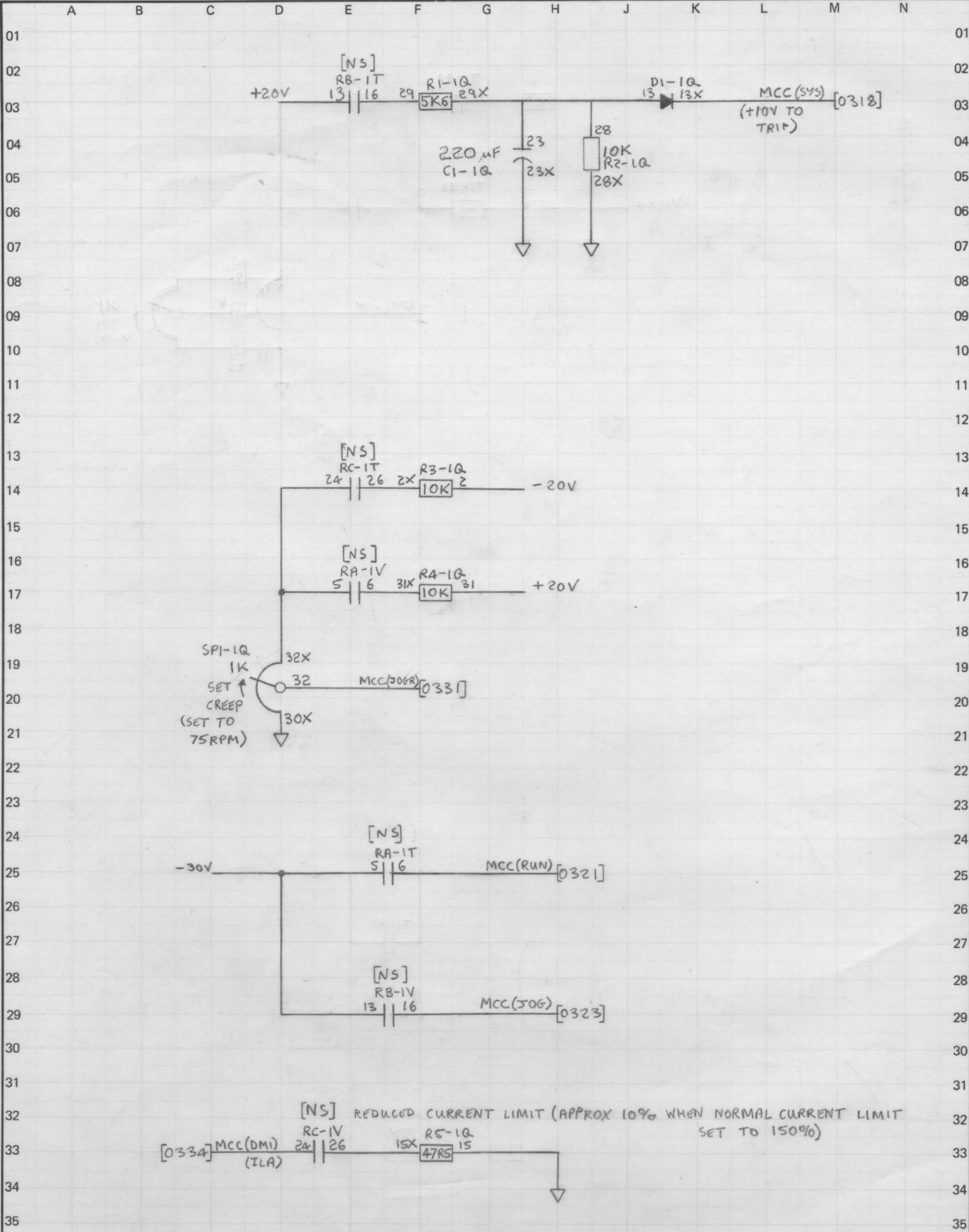
NOTE: FIELD EFFECT TRANSISTOR: THE
 CLOSED/OPEN (I/O) STATE OF THESE
 SWITCHED FOR "PRECONDITION" - "RUN"
 OR JOG - "DIAGNOSTIC STATIC" -
 "DIAGNOSTIC RUN" IS SHOWN BY A
 FOUR DIGIT WORD WITH STATE SEQUENCE.

◇ SUPPLIED BY AW - FITTED & WIRED BY CUSTOMER

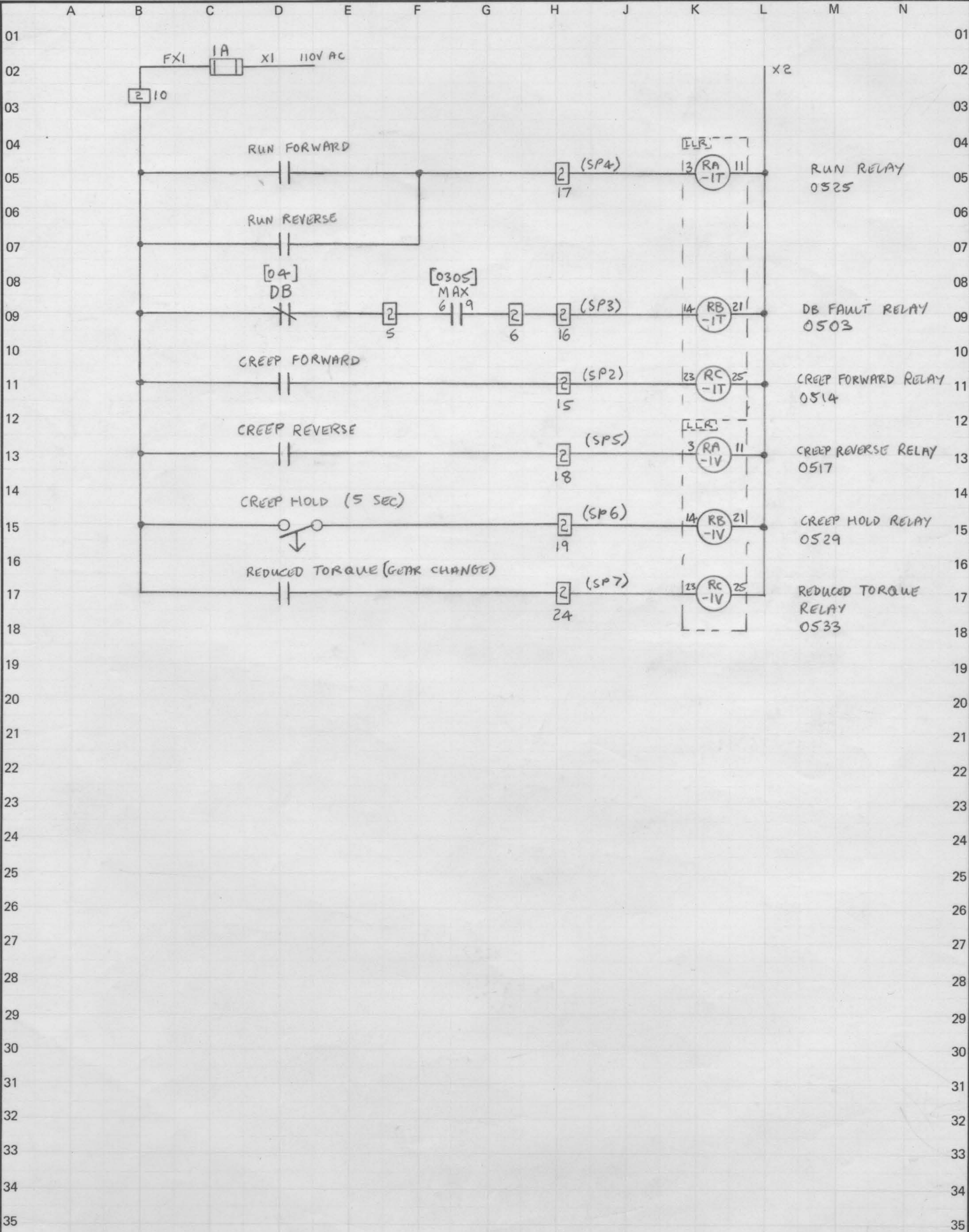
TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	28/1/81	 VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.	BDC 3064R, 110HP		IDENT		
AS SHIPPED - SEE SHEET 10						TECHN.			WEBSTER & BENNETT LTD		DR	SH	
5/6/81						ENG.	NGM		GO NUMBER	050N07	ELEMENTARY DIAGRAM	902M122XE	CONTD.
						APPD.							01



TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	2/2/81	ALLENWEST	BDC 3064R, 110HP	IDENT	DR	SH
				NGM				Simplex	WEBSTER & BENNETT			
								VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.	GO NUMBER 050N07	ELEMENTARY DIAGRAM 902M122XE	CONTD. 05	04



TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	ALLENWEST Simplex VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.		BDC 3064R, 110HP WEBSTER & BENNETT		IDENT DR SH	
			2	NGM	10/2/81	TECHN.			GO NUMBER	ELEMENTARY DIAGRAM	CONTD.	
			5/6/81	NGM		APPD.			050N07	902M122XE	06	05



TECHN. ENG. APPD.			TECHN. ENG. APPD.			DATE	BDC 3064R, 110HP		IDENT	
						11/2/81	WEBSTER + BENNETT		DR SH	
						TECHN.	GO NUMBER		06	
						ENG. NGM	ELEMENTARY DIAGRAM		07	
						APPD.	050N07		902M122XE	
							VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.			

	A	B	C	D	E	F	G	H	J	K	L	M	N	
01	A	B	C	D	E	F	G	H	J	K	L	M	N	01
02														02
03														03
04														04
05														05
06														06
07														07
08														08
09														09
10														10
11														11

ON PRINTED CIRCUIT CARDS USED IN THIS RACK THE LETTERS 'AA' AFTER BASIC CATALOGUE NUMBER INDICATES ORIGINAL DESIGN. SUBSEQUENT DESIGNS WITH THE SAME BASIC NUMBERS AND GROUP NUMBER WITH THE SECOND LETTER CHANGED, SUCH AS: AB, AC, AD, ETC., ARE DIRECTLY INTERCHANGEABLE AND MAY BE SUPPLIED IN PLACE OF THE 'AA' CARDS.

THE PRINTED CIRCUIT CARD SHOULD ALWAYS BE REMOVED WITH THE CARD EXTRACTOR WHICH IS ATTACHED ON TOP OF THE CARD RACK. SOME CARDS CONTAIN PARTS WHICH WILL BE THERMALLY HOT AFTER BEING IN OPERATION. CARE SHOULD BE EXERCISED IN HANDLING ALL CARDS AFTER REMOVAL UNTIL THESE PARTS HAVE COOLED. DO NOT REMOVE OR INSERT CARDS WITH POWER APPLIED.

FRONT VIEW OF 64 PIN
RECEPTACLE AS SEEN
IN RACK CLOSED
POSITION.

SYMBOLS:



TEST POST



POT ADJUSTMENT



INDICATING LIGHT

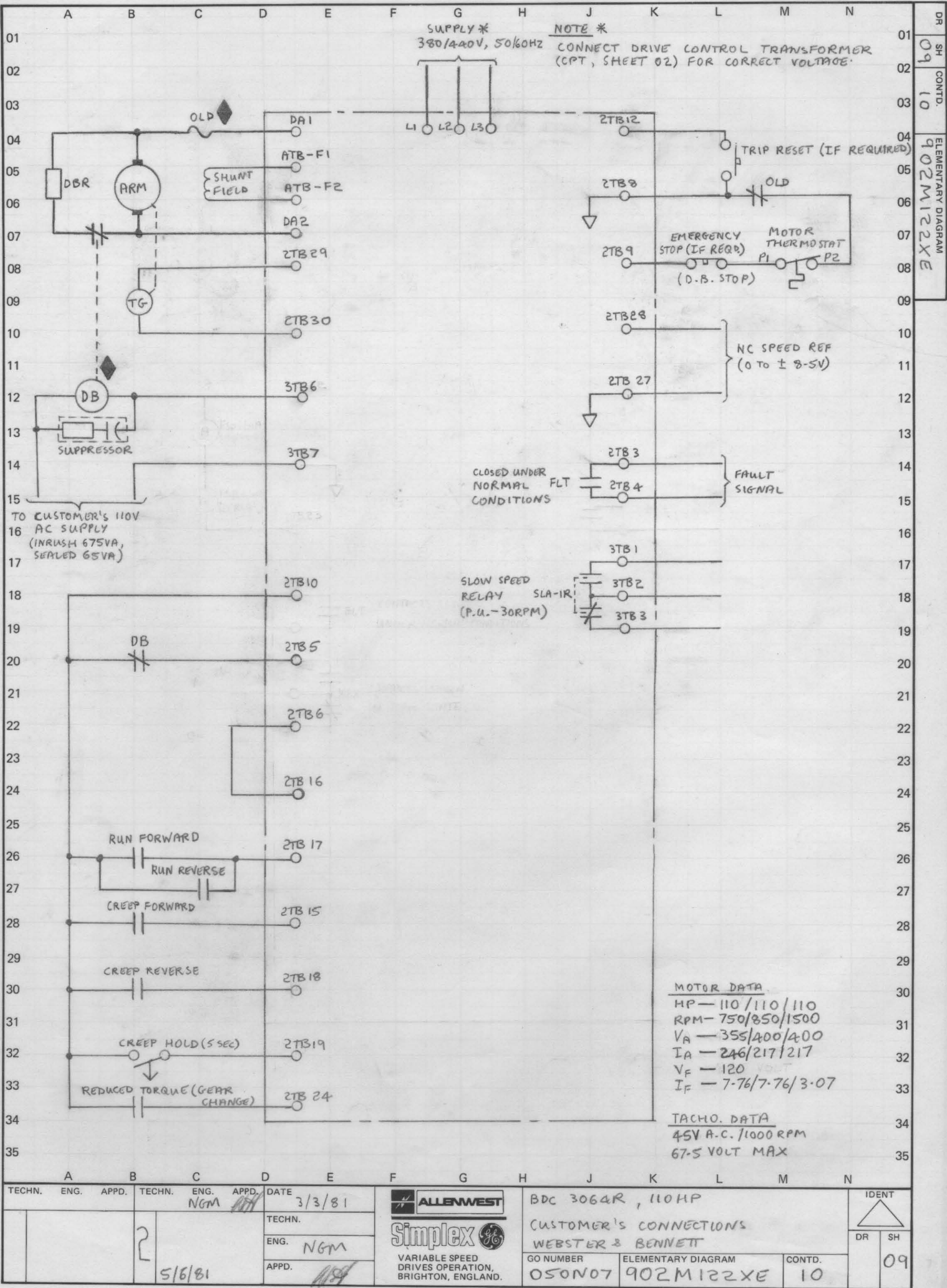
32	-	64	32	-	32X
31	-	63	31	-	31X
30	-	62	30	-	30X
29	-	61	29	-	29X
28	-	60	28	-	28X
27	-	59	27	-	27X
26	-	58	26	-	26X
25	-	57	25	-	25X
24	-	56	24	-	24X
23	-	55	23	-	23X
22	-	54	22	-	22X
21	-	53	21	-	21X
20	-	52	20	-	20X
19	-	51	19	-	19X
18	-	50	18	-	18X
17	-	49	17	-	17X
16	-	48	16	-	16X
15	-	47	15	-	15X
14	-	46	14	-	14X
13	-	45	13	-	13X
12	-	44	12	-	12X
11	-	43	11	-	11X
10	-	42	10	-	10X
9	-	41	9	-	9X
8	-	40	8	-	8X
7	-	39	7	-	7X
6	-	38	6	-	6X
5	-	37	5	-	5X
4	-	36	4	-	4X
3	-	35	3	-	3X
2	-	34	2	-	2X
1	-	33	1	-	1X

CARD RACK WIRE JUMPER TABLE			
RTB(-20V)-1Q02	MCC(SFB)-1R22	IV26-1Q15X	
1Q02-1R02	1R19-1R21	IV25-1V21	SP2-1T23
RTB(COM)-1Q15	MCC(DM2)-1R26	IV21-1V11	SP3-1T14
1Q15-1R15	1R28-1Q12	IV11-1T11	SP4-1T13
RTB(+20V)-1Q31	1R31-1T13	IT11-1T21	SP5-1V03
1Q31-1R31	IT16-1Q29	IT21-1T25	SP6-1T14
RTB(-30V)-1T05	1Q29X-1Q28	1Q12X-1Q15	SP7-1T23
1T05-1V13	1Q28-1Q23		2TB6-2TB16
RTB(X2)-1T25	1Q23-1Q13		SP11-1R29
	1Q28X-1Q23X		SP12-1R25
	1Q23X-1Q15		SP13-1R30
	1Q13X-MCC(SYS)		
	1Q2X-1T26		
	1Q31X-1V06		
	1V05-1T24		
	1T24-1Q32X		
	1Q30X-1Q28X		
	1Q32-MCC(SOGR)		
	1T06-MCC(RUN)		
	1V16-MCC(SOG)		
	MCC(DMI)-1V24		

NOTE: RECEPTACLE PINS MAY
BE NUMBERED AS SHOWN
IN EITHER SKETCH. (PIN
33 CORRESPONDS TO PIN
1X, 34 TO 2X, ETC.)

TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	11/2/81		BDC 3064R, 110HP		IDENT	
									WEBSTER & BENNETT		DR SH	
									GO NUMBER		ELEMENTARY DIAGRAM	
									050N07		902M122XE	
									CONTD.		08	
											07	

DR SH CONTD. 07 08 902M122XE ELEMENTARY DIAGRAM



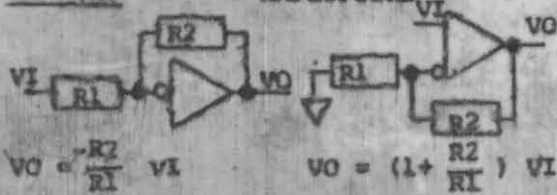
VOLTAGE POLARITIES SHOWN ARE FOR MOTORING DA1(+)

HARDWARE ABBREVIATIONS

MCC MAIN CONTROL CARD
 IFC INTERFACE CARD
 PSC POWER SUPPLY CARD
 SCR THYRISTOR ASSEMBLY
 DGC DIAGNOSTIC CARD
 MPC MOTOR FIELD CONTROL
 MFE MOTOR FIELD EXCITER
 MDR MODIFICATION RACK
 ACC AUXILIARY CONTROL CARD

SYMBOLS

AMPLIFIERS



CASE GROUND.

VO = SIGN () X ABSOLUTE VALUE OF VI

STAB ON TERMINAL

TERMINAL AT 2TB, 3TB, 4TB, RTB.

EX: 9 2 - 2TB9; X2 2 - RTB2

TERMINAL AT T.B.'s

POTENTIOMETER ARROWS ON THE CARD

ELEMENTARY DIAGRAMS INDICATE THE

WIPER DIRECTION AS THE POTENTIOMETER

SHAFT IS ROTATED CLOCKWISE TO INCREASE

FUNCTION.

THESE RESISTORS ARE CRIMPED IN WIRE

HARNESS.

FUNCTION USE LOC JUMPERS

60HZ

30HZ

100-400

-500

-300

SR5 - 9v

9 - 20v

JOG 10v

20v

LT. 3-75sec

2 - 60sec

VREG

DC TACHO

AC TACHO

TACHO FILT

TACHO V.

24-64vdc

27-71vac

60-160vdc

66-177vac

110-300vdc

120-300vac

G134 G256

1.3 1.7

2.1 3.1

2.4 5.0

4.0 8.0

7.0 13.1

11.3 25.1

L/R < .25S

INH RUN

INH DRV CL

FUSELESS

200% DR CL

REG. CL

FIELD ECON DLY

MCC MFC ZA-ZB (IF USED)

MCC HZA - PHA

(NONE)

IFC I - IHI

IFC I - ILO

(NONE)

MCC SRH - COM

(NONE)

MCC JH - COM

(NONE)

332Ω FROM LTI TO COM

NT-CEMF CC-COM

(NONE)

MCC AT1 - AT2

IFC TC - TC

IFC NT-NT1 PT - PT1

IFC NT-NT1 PT - PT1

IFC NT-NT2 PT - PT2

IFC NT-NT2 PT - PT2

IFC NT-NT3 PT - PT3

IFC NT-NT3 PT - PT3

IFC MFC OR MFE

ME NONE

ME VA - VD

ME VA - VB

ME VA - VB, VC - VD

ME VA - VC

ME VA - VC, VB - VD

MFC QA - QB

DGC D1-D2 (IF USED)

MCC DC1 - COM

ACC CFY - CFX

MCC DCX - DCY

MCC ILA - DM1

MCC FEA - DM3

SIGNAL DEFINITIONS AND LOCATIONS

* CEMF COUNTER EMF (16)
 * CFB CURRENT FEEDBACK (16)
 CMFA ABSOLUTE VALUE CEMF (08)
 CRM CROSSOVER MODIFY (11)
 DFP DELAYED FIRING POWER (25)
 * DR DRIVER REFERENCE (33)
 * EAO ERROR AMP OUTPUT (33)
 EST EXTERNAL FLT STOP INPUT (14)
 FALT FAULT (14)
 * FC FIELD CURRENT (NS26)
 FDR FIELD DIAGNOSTIC REFERENCE (08)
 FEA FIELD ECONOMY ADJUST (25)
 PF FIELD FAULT (28)
 IABS MOTOR CURRENT ABSOLUTE (09)
 ILA CURRENT LIMIT ADJUST (23)
 IMET CURRENT SIGNAL FOR METER (10)
 * IPU INITIAL PULSE (20)
 * LR LOCAL REF. FROM DGC (33)
 * JOG JOG SWITCH INPUT (23)
 * JOGR JOG REFERENCE INPUT (31)
 * MAC MAX/MA CONTROL SIGNAL (20)
 MSW MODE SWITCH (30)
 * OSC OSCILLATOR (17)
 * PCR PHASE CONTROL REF. (26)
 * PRE DRIVE PRECONDITION (21)
 ØSEQ PHASE SEQUENCE (14)
 RERR REGULATOR ERROR (27)
 RIJ INTEGRATOR SUMMING JUNCTION (27)
 RJ REGULATOR SUMMING JUNCTION (31)
 RRA REGULATOR RESPONSE ADJUST (30)
 RSET RESET (16)
 * RTR READY TO RUN (16)
 * RUN RUN SWITCH INPUT (21)
 * SA-C PHASE SYN OUTPUT (16)
 * SFB SPEED FEEDBACK (20)
 SMET SPEED SIGNAL FOR METER (12)
 * SR SYSTEM REFERENCE INPUT (29)
 * SYS SYSTEM FAULT TRIP (13)
 * TA OUTPUT FOR TACHO TRIP ADJUST (20)
 TF TACHO FAULT (NS28)
 * TFB TACHOMETER FEEDBACK (20)
 TPR AC TACHO FREQUENCY OUTPUT (13)
 * TR TIMED REFERENCE (33)
 * VFB VOLTAGE FEEDBACK (19)
 * WFR WEAK FIELD REFERENCE (20)

(* - TEST POINT ON DOOR FRONT)

MAPPING SYSTEM

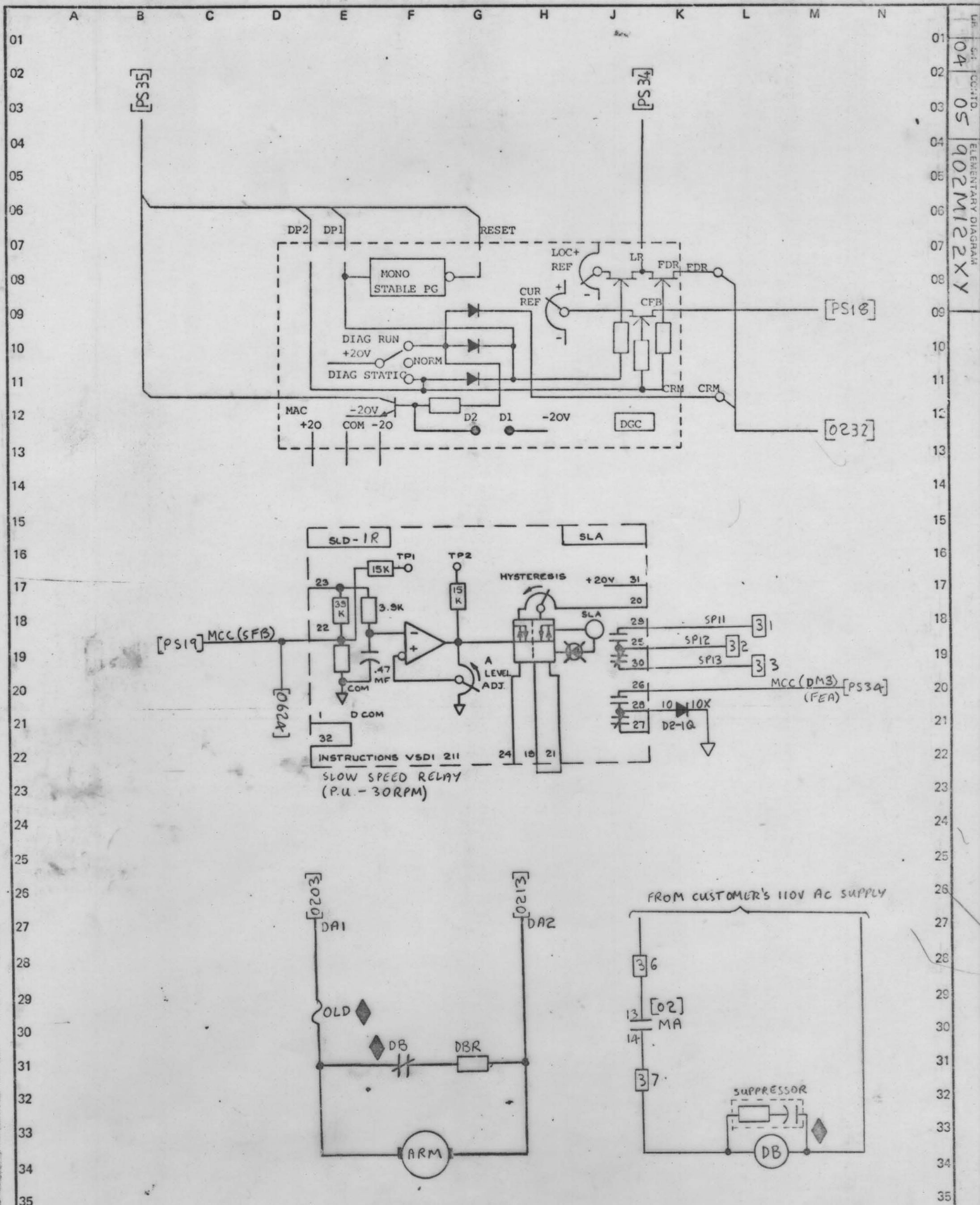
(NS/PS/TS) PS - PAST SHEET
 NS - NEXT SHEET
 TS - THIS SHEET

HENCE (PS - 12) DENOTES LOCATION ON PAST SHEET LINE 12. OTHER LOCATIONS ARE DENOTED BY SHEET NUMBER AND LINE? E.G. (1A16) SIGNIFIES LOCATION ON SHEET 1A, LINE 16 ETC.

NOTE: FIELD EFFECT TRANSISTOR: THE CLOSED/OPEN (I/O) STATE OF THESE SWITCHED FOR "PRECONDITION" - "RUN" OR JOG" - "DIAGNOSTIC STATIC" - "DIAGNOSTIC RUN" IS SHOWN BY A FOUR DIGIT WORD WITH STATE SEQUENCE.

SUPPLIED BY AW - FITTED & WIRED BY CUSTOMER

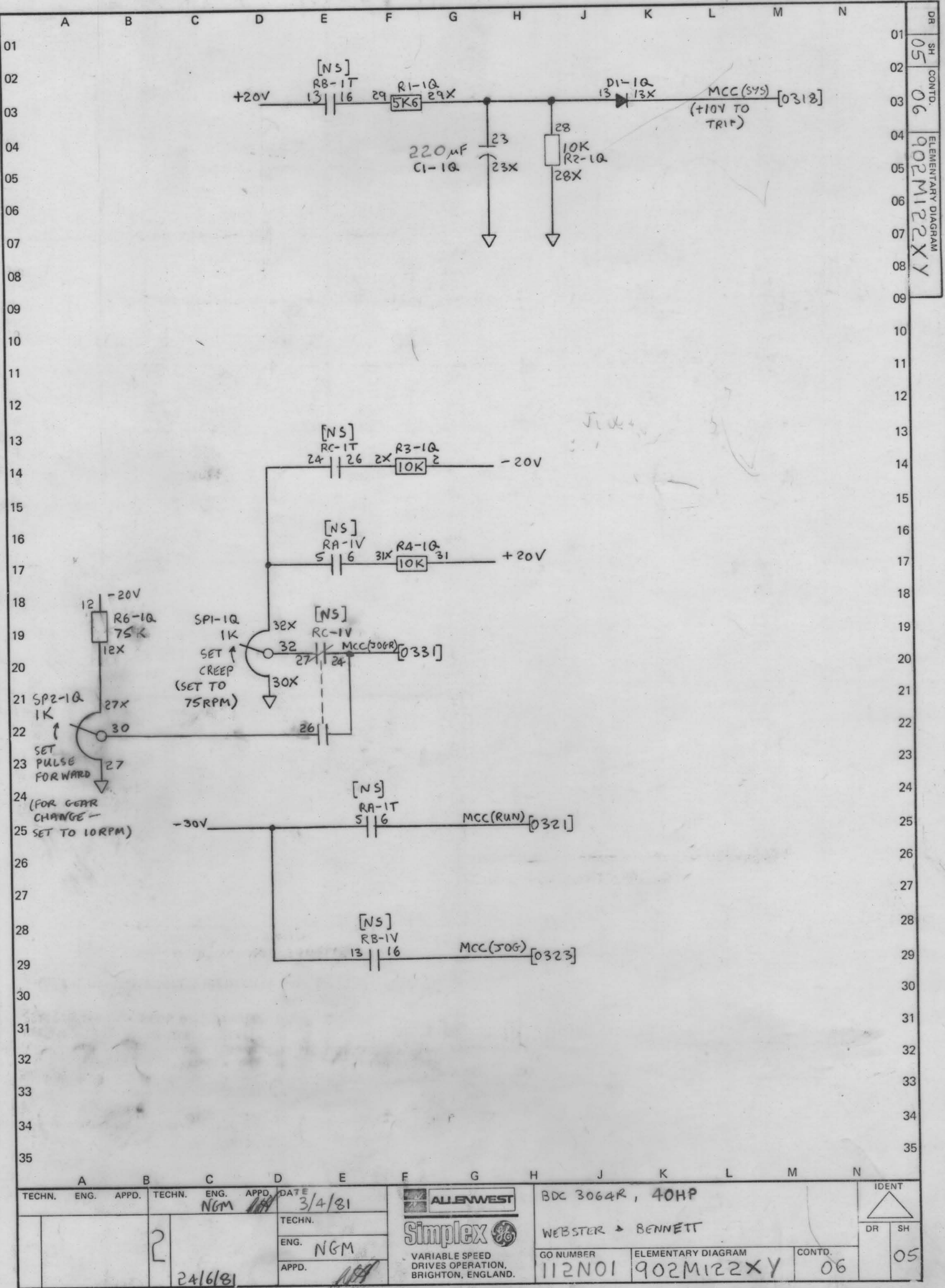
TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	3/4/81	IDENT	01
AS SHIPPED - SEE SHEET 10							TECHN.	01	
24/6/81							ENG.	01	
24/6/81							APPD.	01	
24/6/81							Simples		
24/6/81							VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.		
24/6/81							BDC 3064R, 40HP		
24/6/81							WEBSTER & BENNETT LTD		
24/6/81							GO NUMBER		
24/6/81							112N01		
24/6/81							ELEMENTARY DIAGRAM		
24/6/81							902M122XY		
24/6/81							CONTD.		
24/6/81							02		



TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	BDC 306AR, 40HP		IDENT
				NGM		3/4/81	WEBSTER & BENNETT		OR SH
2			24/6/81			TECHN.	GO NUMBER		04
			ENG. NGM			APPD.	ELEMENTARY DIAGRAM		
							112N01 902M122XY 05		

Simplex

VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.



	A	B	C	D	E	F	G	H	J	K	L	M	N	
01	A	B	C	D	E	F	G	H	J	K	L	M	N	
02										CC	SLD	LLR	LLR	
03										909W 317 AAG48	193W 277 AAG01	193W 279 AAG03	193W 279 AAG03	
04														
05														
06														
07														
08														
09														
10														
11														

ON PRINTED CIRCUIT CARDS USED IN THIS RACK THE LETTERS 'AA' AFTER BASIC CATALOGUE NUMBER INDICATES ORIGINAL DESIGN. SUBSEQUENT DESIGNS WITH THE SAME BASIC NUMBERS AND GROUP NUMBER WITH THE SECOND LETTER CHANGED, SUCH AS: AB, AC, AD, ETC., ARE DIRECTLY INTERCHANGEABLE AND MAY BE SUPPLIED IN PLACE OF THE 'AA' CARDS.

THE PRINTED CIRCUIT CARD SHOULD ALWAYS BE REMOVED WITH THE CARD EXTRACTOR WHICH IS ATTACHED ON TOP OF THE CARD RACK. SOME CARDS CONTAIN PARTS WHICH WILL BE THERMALLY HOT AFTER BEING IN OPERATION. CARE SHOULD BE EXERCISED IN HANDLING ALL CARDS AFTER REMOVAL UNTIL THESE PARTS HAVE COOLED. DO NOT REMOVE OR INSERT CARDS WITH POWER APPLIED.

FRONT VIEW OF 64 PIN
RECEPTACLE AS SEEN
IN RACK CLOSED
POSITION.

SYMBOLS:



TEST POST



POT ADJUSTMENT



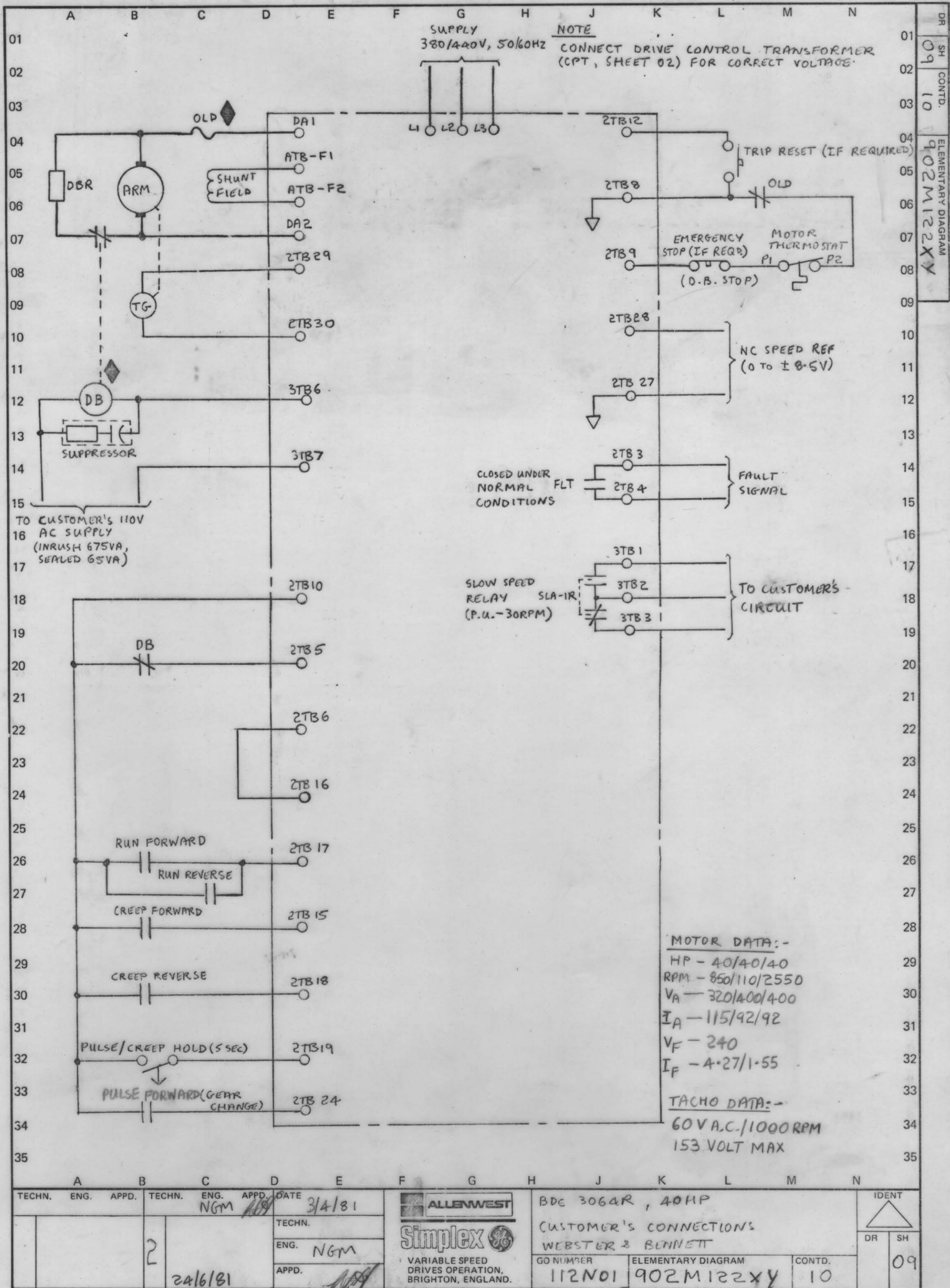
INDICATING LIGHT

CARD RACK WIRE JUMPER TABLE

RTB(-20V)-1Q02	MCC(SFB)-1R22	1R22-1R11	
1Q02-1R02	1R19-1R21	1V25-1V21	SP2-1T23
RTB(COM)-1Q15	MCC(DM3)-1R26	1V21-1V11	SP3-1T14
1Q15-1R15	1R28-1Q10	1V11-1T11	SP4-1T03
RTB(+20V)-1Q31	1R31-1T13	1T11-1T21	SP5-1V03
1Q31-1R31	1T16-1Q29	1T21-1T25	SP6-1V14
RTB(-30V)-1T05	1Q29X-1Q28	1Q02-1Q12	SP7-1V23
1T05-1V13	1Q28-1Q23	1Q12X-1Q27X	2TB6-2TB16
RTB(X2)-1T25	1Q23-1Q13	1Q30-1V26	SP11-1R29
	1Q28X-1Q23X	1Q27-1Q23X	SP12-1R25
	1Q23X-1Q15	1V24-MCC(506R)	SP13-1R30
	1Q13X-MCC(SYS)	1R08-1Q25	
	1Q2X-1T26	1Q25-1Q25X	
	1Q31X-1V06	1Q22-1Q15X	
	1V05-1T24	1R13-1R17	MA13-3TB6
	1T24-1Q32X	1Q25X-MCC(DM2)	MA14-3TB7
	1Q30X-1Q28X	1Q10X-1Q27	
	1Q32-1V27		
	1T06-MCC(RUN)		
	1V16-MCC(506)		
	MCC(DMI)-1R05		

NOTE: RECEPTACLE PINS MAY
BE NUMBERED AS SHOWN
IN EITHER SKETCH. (PIN
33 CORRESPONDS TO PIN
1X, 34 TO 2X, ETC.)

TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	3/4/81		BDC 3064R, 40HP		IDENT		
				NGM	APP				WEBSTER & BENNETT		07		
						TECHN.							
						ENG.	NGM						
						APPD.	APP						
						VARIABLE SPEED DRIVES OPERATION. BRIGHTON, ENGLAND.		GO NUMBER 112 N01		ELEMENTARY DIAGRAM 902M122XY		CONT'D 08	



A B C D E F G H J K L M N

VOLTAGE POLARITIES SHOWN ARE FOR MOTORING DA1(+)

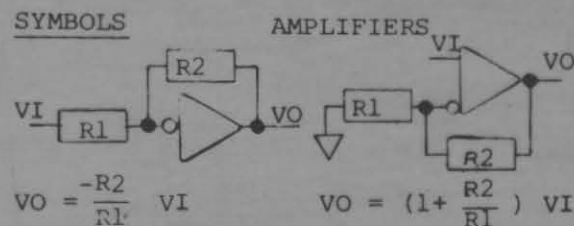
SIGNAL DEFINITIONS AND LOCATIONS

HARDWARE ABBREVIATIONS

MCC MAIN CONTROL CARD
 IFC INTERFACE CARD
 PSC POWER SUPPLY CARD
 SCR THYRISTOR ASSEMBLY
 DGC DIAGNOSTIC CARD
 MFC MOTOR FIELD CONTROL

MDR. MODIFICATION RACK

SYMBOLS



CASE GROUND

VO = SIGN () X ABSOLUTE VALUE OF VI

STAB ON TERMINAL

TERMINAL AT 2TB, 3TB, 4TB, RTB.

EX: 9 [2] - 2TB9; X2 [R] - RTBX2

TERMINAL AT T.B.'s

POTENTIOMETER ARROWS ON THE CARD
 ELEMENTARY DIAGRAMS INDICATE THE
 WIPER DIRECTION AS THE POTENTIOMETER
 SHAFT IS ROTATED CLOCKWISE TO INCREASE
 FUNCTION.

THESE RESISTORS ARE CRIMPED IN WIRE
 HARNESS.

MTD. ON PANEL

FUNCTION	USE	LOC	JUMPERS
60HZ		MCC	AA-AS, BA-BS, CA-CS
		MFC	ZA-ZB (IF USED)
50HZ	X	MCC	AA-AF, BA-BF, CA-CF
IOC-400%	X		NONE
-500%		IFC	I-IHI
-300%		IFC	I-ILO
SR5 - 9v			(NONE)
9 - 20v	X	MCC	SRH-COM
JOG 10v			(NONE)
20v	X	MCC	JH - COM
LT. 3-7sec	X		(NONE)
2 - 60sec		MCC	332Ω FROM LT1 TO COM
VREG		IFC	NT-CEMF, CC-COM
DC TACHO	X		(NONE)
AC TACHO		MCC	AT1-AT2
TACHO FILT		IFC	TC-TC
TACHO V.			
24-64vdc		IFC	NT-NT1, PT-PT1
27-71vac		IFC	NT-NT1, PT-PT1
60-160vdc		IFC	NT-NT2, PT-PT2
66-177vac		IFC	NT-NT2, PT-PT2
110-300vdc	X	IFC	NT-NT3, PT-PT3
120-300vac		IFC	NT-NT3, PT-PT3
G134 G256			
1.8 1.7		MFC	NONE
1.3 2.8		MFC	YB-YD
2.4 5.0		MFC	YA-YB
4.0 8.0		MFC	YA-YB, YC-YD
7.0 13		MFC	YA-YC
13 25	X	MFC	YA-YC, YB-YD
L/R < .25S		MFC	QA-QB
INH RUN		DGC	D1-D2 (IF USED)

* CEMF COUNTER EMF (3.16)
 * CFB CURRENT FEEDBACK (3.16)
 CMFA ABSOLUTE VALUE CEMF (3.08)
 CRM CROSSOVER MODIFY (4.11)
 DFP DELAYED FIRING POWER (3.25)
 * DR DRIVER REFERENCE (3.33)
 * EAO ERROR AMP OUTPUT (3.33)
 EST EXTERNAL FLT STOP INPUT (3.14)
 FALT FAULT (3.14)
 * FC FIELD CURRENT (NS26)
 FDR FIELD DIAGNOSTIC REFERENCE (4.08)
 FEA FIELD ECONOMY ADJUST (3.25)
 FF FIELD FAULT (2.28)
 IABS MOTOR CURRENT ABSOLUTE (3.09)
 ILA CURRENT LIMIT ADJUST (3.23)
 IMET CURRENT SIGNAL FOR METER (3.10)
 * IPU INITIAL PULSE (3.20)
 * LR LOCAL REF. FROM DGC (3.33)
 * JOG JOG SWITCH INPUT (3.23)
 * JOGR JOG REFERENCE INPUT (3.31)
 * MAC MAX/MA CONTROL SIGNAL (3.20)
 MSW MODE SWITCH (3.30)
 * OSC OSCILLATOR (3.17)
 * PCR PHASE CONTROL REF. (3.26)
 * PRE DRIVE PRECONDITION (3.21)
 ØSEQ PHASE SEQUENCE (3.14)
 RERR REGULATOR ERROR (3.27)
 RIJ INTEGRATOR SUMMING JUNCTION (3.27)
 RJ REGULATOR SUMMING JUNCTION (3.31)
 RRA REGULATOR RESPONSE ADJUST (3.30)
 RSET RESET (3.16)
 * RTR READY TO RUN (3.16)
 * RUN RUN SWITCH INPUT (3.21)
 * SA-C PHASE SYN OUTPUT (3.16)
 * SFB SPEED FEEDBACK (3.20)
 SMET SPEED SIGNAL FOR METER (3.12)
 * SR SYSTEM REFERENCE INPUT (3.29)
 * SYS SYSTEM FAULT TRIP (3.13)
 * TA OUTPUT FOR TACHO TRIP ADJUST (3.20)
 TF TACHO FAULT (NS28)
 * TFB TACHOMETER FEEDBACK (3.20)
 TFR AC TACHO FREQUENCY OUTPUT (3.13)
 * TR TIMED REFERENCE (3.33)
 * VFB VOLTAGE FEEDBACK (3.19)
 * WFR WEAK FIELD REFERENCE (3.20)

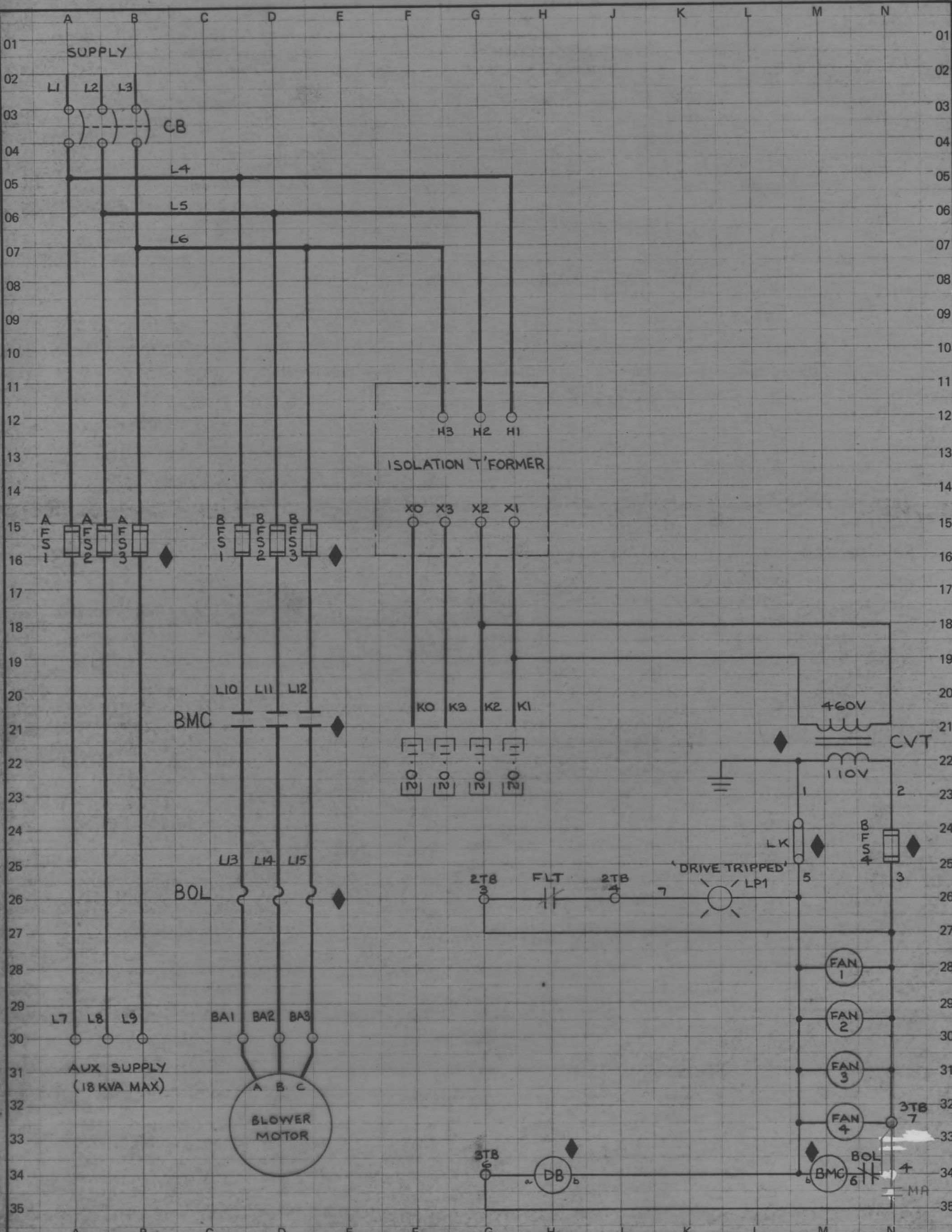
* - TEST POINT ON DOOR FRONT)

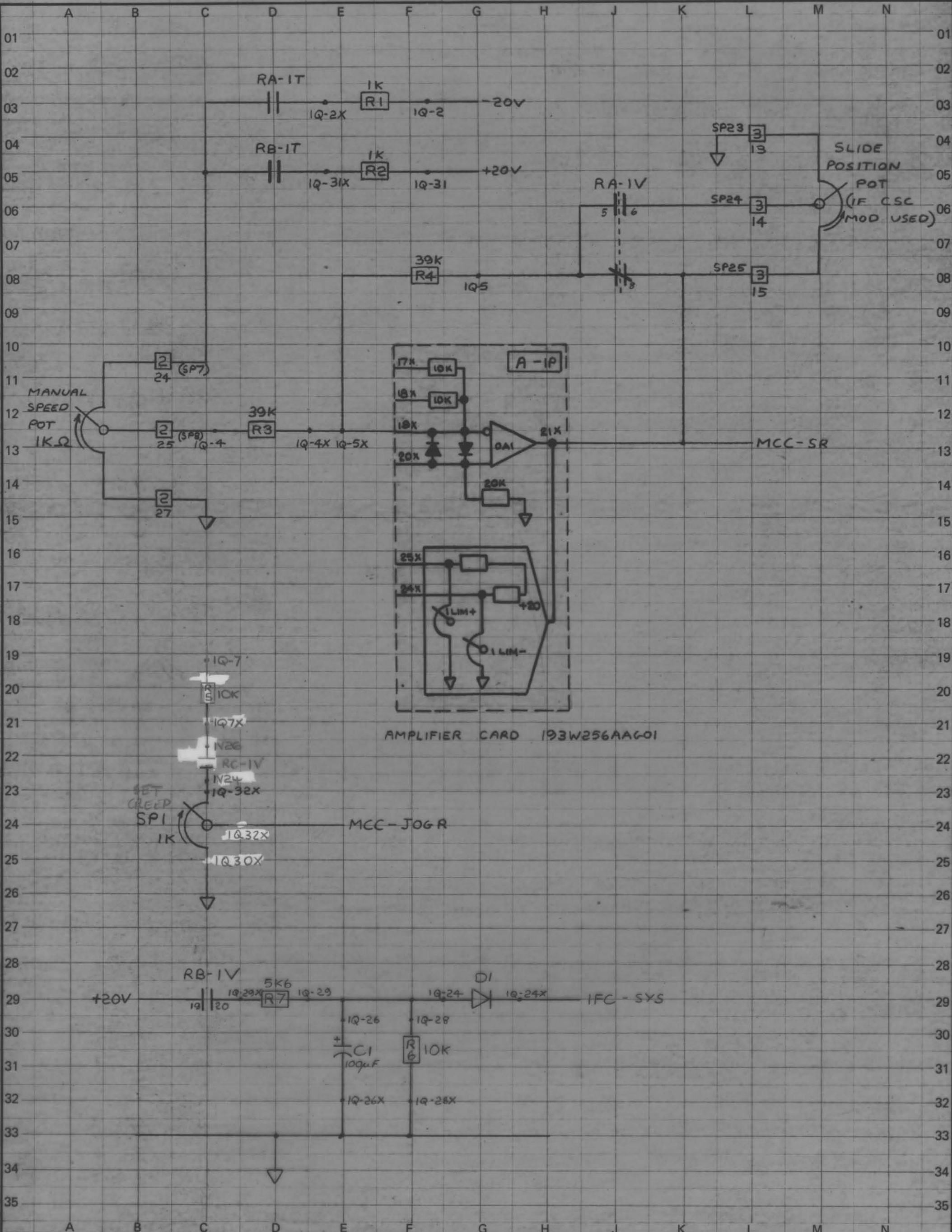
MAPPING SYSTEM

(NS/PS/TS) PS - PAST SHEET
 NS - NEXT SHEET
 TS - THIS SHEET

NOTE: FIELD EFFECT TRANSISTOR: THE
 CLOSED/OPEN (I/O) STATE OF THESE
 SWITCHED FOR "PRECONDITION" - "RUN"
 OR JOG" - "DIAGNOSTIC STATIC" -
 "DIAGNOSTIC RUN" IS SHOWN BY A
 FOUR DIGIT WORD WITH STATE SEQUENCE.

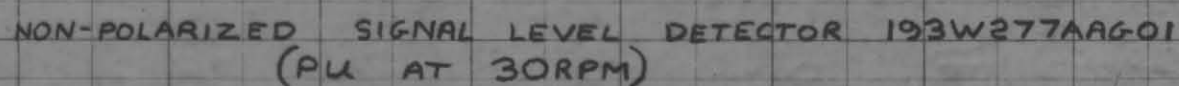
TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	3-7-79		WEBSTER & BENNETT LTD.		IDENT		
NGM	NGM	NGM	NGM	NGM	NGM	3-7-79			75HP BDC3034R SPINDLE DRIVE.		DR SH		
SEE SHEET 13	SEE SHEET 13	SEE SHEET 13	SEE SHEET 13	SEE SHEET 13	SEE SHEET 13	TECHN.	AP		ELEMENTARY DIAGRAM		1		
6-8-80	25/1/80	25/1/80	25/1/80	25/1/80	25/1/80	ENG.	AP		GO NUMBER		921N07		
						APPD.			ELEMENTARY DIAGRAM		902M1228R		
										CONTD		2	

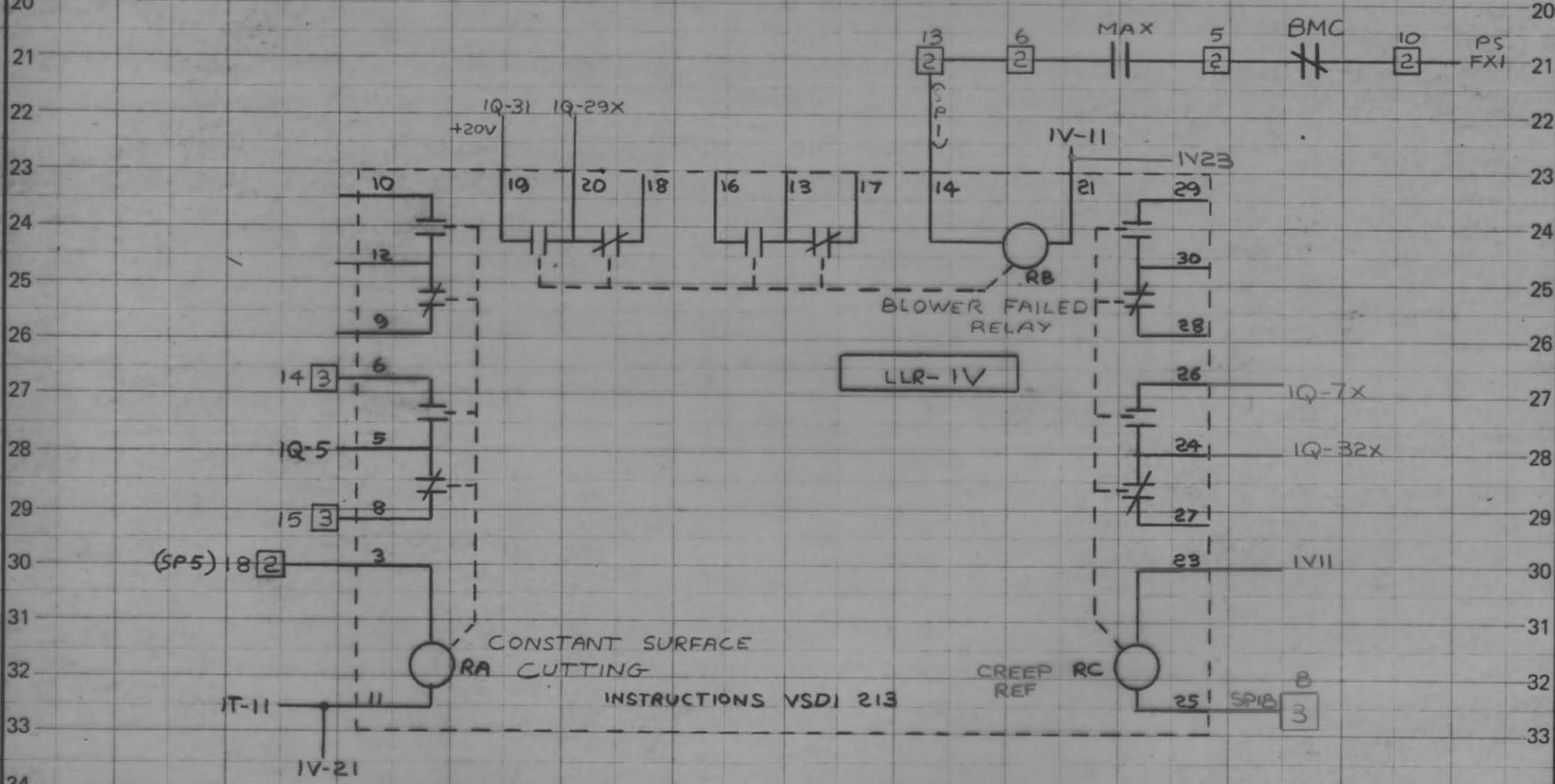
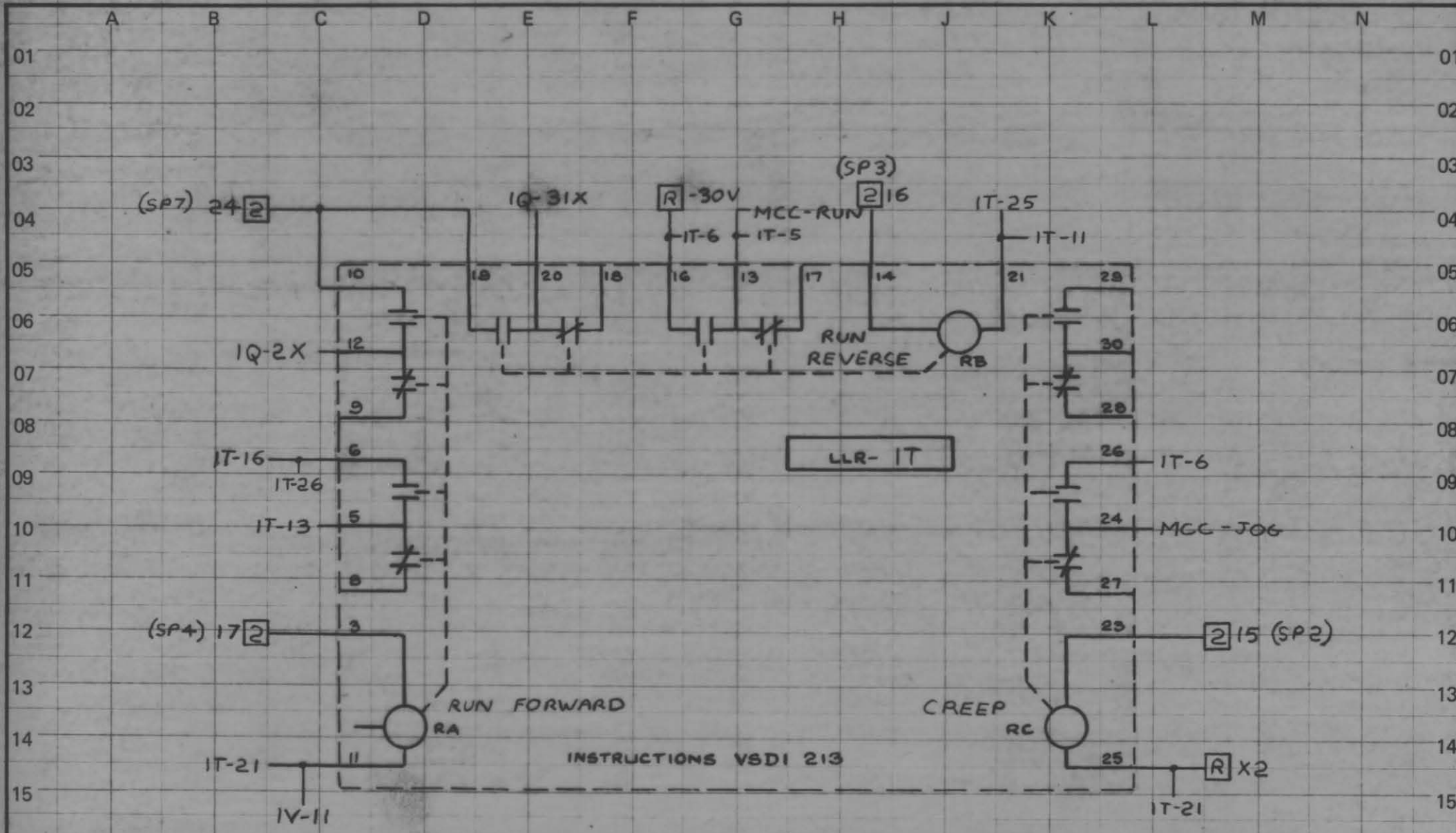




TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE			WEBSTER & BENNETT LTD. 75HP BDC 3034R SPINDLE DRIVE, ELEMENTARY DIAGRAM.		IDENT DR SH	
			3	NGM		3-7-79			GO NUMBER 941V07		ELEMENTARY DIAGRAM 902M122BR	
			SEE SHEET 13			TECHN.			CONTD. 7		6	
			G-B-80			ENG.	VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.					
						APPD.						

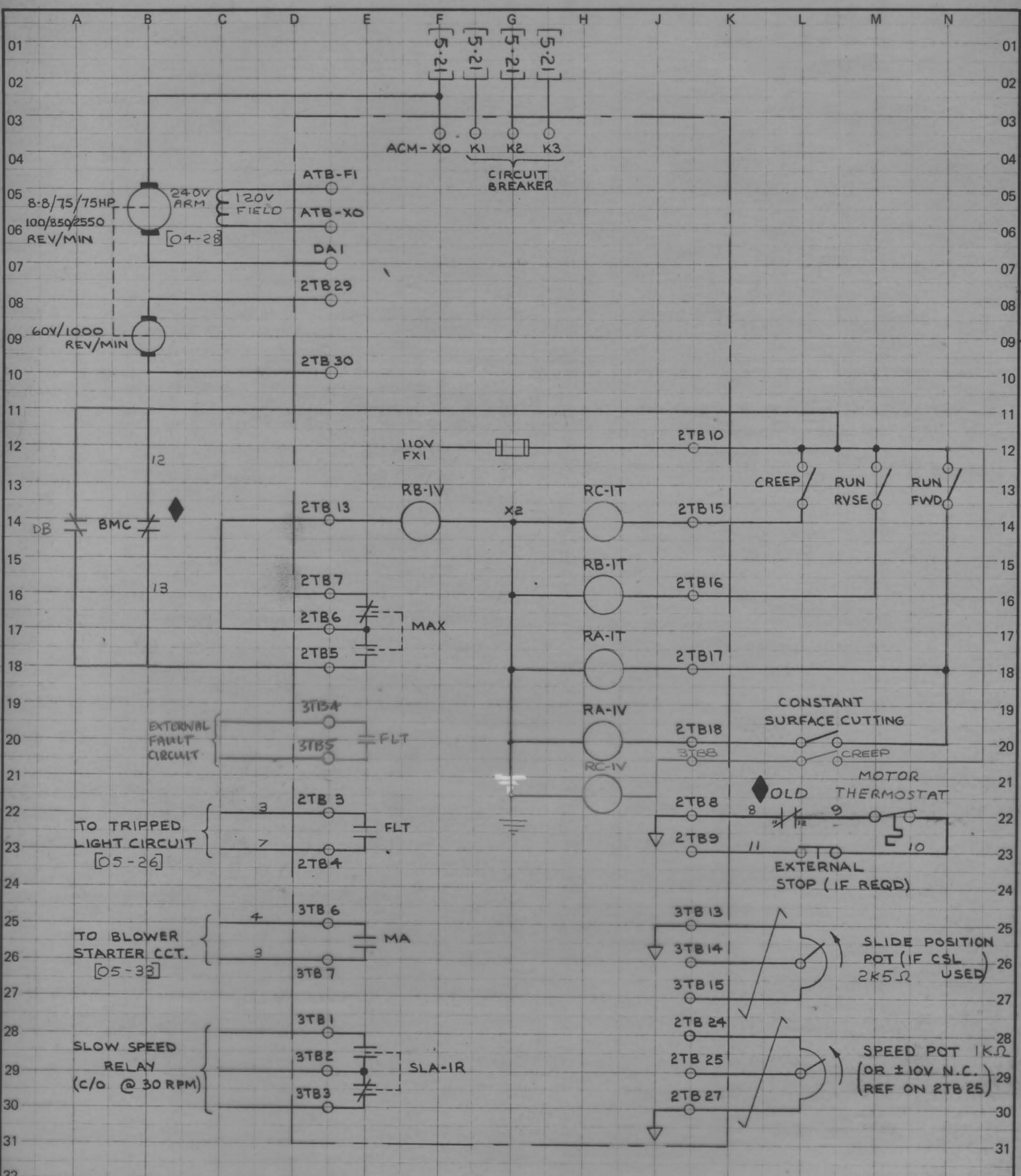
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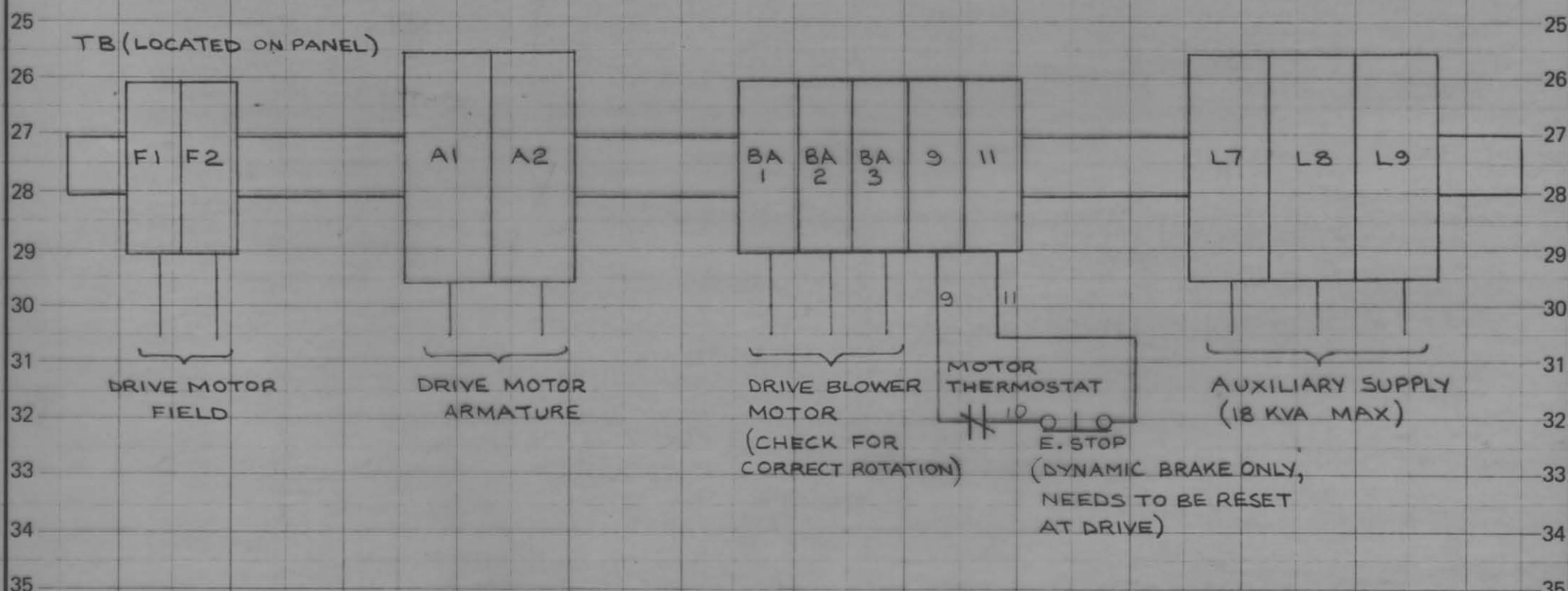
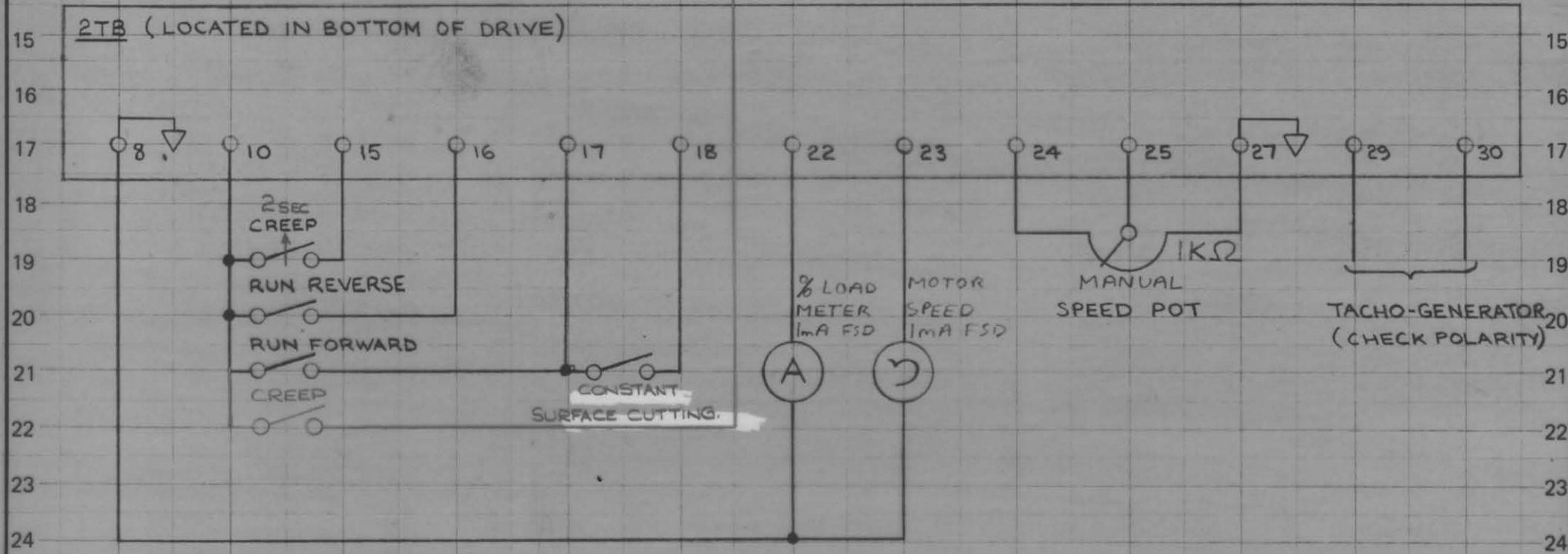
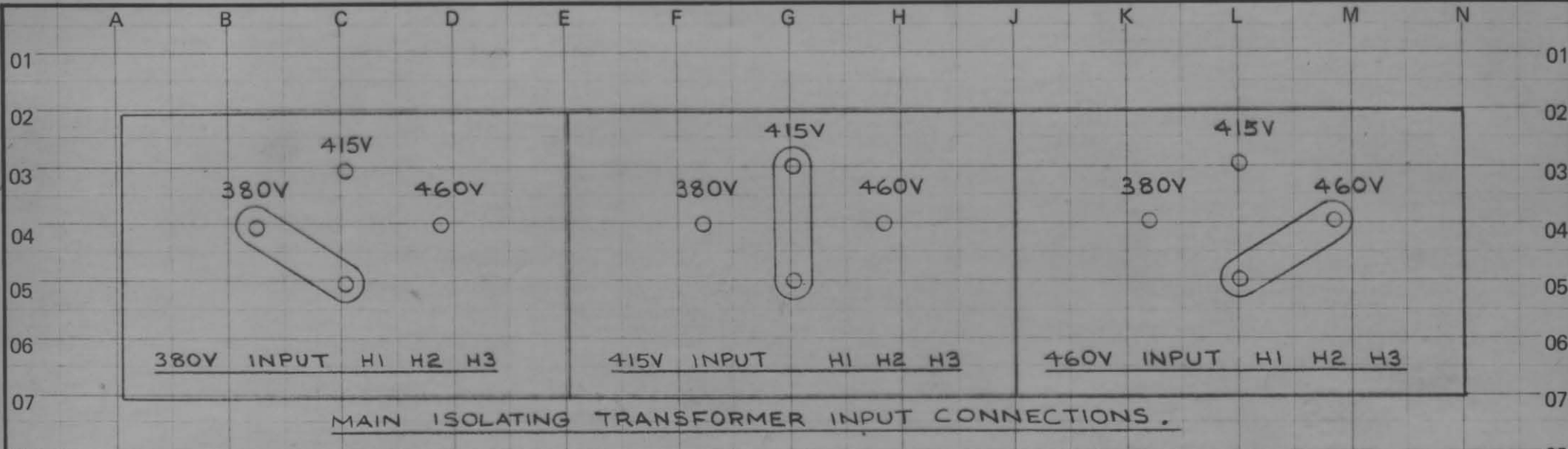
TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE			WEBSTER & BENNETT LTD. 75HP BDC 3034R SPINDLE DRIVE ELEMENTARY DIAGRAM.		IDENT 	
						3-7-79			GO NUMBER 921N07		ELEMENTARY DIAGRAM 902M122BR	
							VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.		CONTD. 9		DR SH 8	
SEE SHEET 13 6-8-80			APPD. 									

Disclaimer Statement The trade mark is the trade mark of General Electric Company of U.S.A., which is not connected with the English Company of a similar name.



CONTROLLER CONNECTIONS ONLY

TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE			WEBSTER AND BENNETT LTD. 75HP BDC3034R SPINDLE DRIVE, ELEMENTARY DIAGRAM		IDENT
						3/7/79					
			SEE SHEET 13				VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.		GO NUMBER 921N07		ELEMENTARY DIAGRAM 902M122BR
			3						CONTD. 12		DR SH 11
			6-8-80								



A B C D E F G H J K L M N

VOLTAGE POLARITIES SHOWN ARE FOR MOTORING DA1(+)

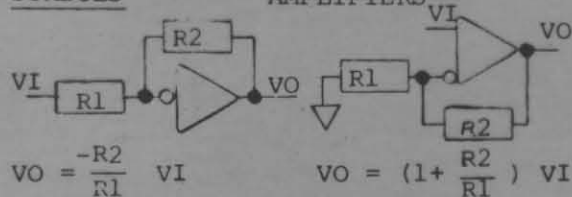
SIGNAL DEFINITIONS AND LOCATIONS

HARDWARE ABBREVIATIONS

MCC MAIN CONTROL CARD
IFC INTERFACE CARD
PSC POWER SUPPLY CARD
SCR THYRISTOR ASSEMBLY
DGC DIAGNOSTIC CARD
MFC MOTOR FIELD CONTROL
MDR MODIFICATION RACK

SYMBOLS

AMPLIFIERS



CASE GROUND

VO = SIGN () X ABSOLUTE VALUE OF VI

STAB ON TERMINAL

TERMINAL AT 2TB, 3TB, 4TB, RTB.

EX: 9 [2] - 2TB9; X2 [R] - RTBX2

TERMINAL AT T.B.'s

POTENTIOMETER ARROWS ON THE CARD
ELEMENTARY DIAGRAMS INDICATE THE
WIPER DIRECTION AS THE POTENTIOMETER
SHAFT IS ROTATED CLOCKWISE TO INCREASE
FUNCTION.

THESE RESISTORS ARE CRIMPED IN WIRE
HARNES.

MTD. ON PANEL

FUNCTION	USE	LOC	JUMPERS
60HZ	X	MCC	AA-AS, BA-BS, CA-CS
	X	MFC	ZA-ZB (IF USED)
50HZ		MCC	AA-AF, BA-BF, CA-CF
IOC-400%	X		NONE
-500%		IFC	I-IHI
-300%		IFC	I-ILO
SR5 - 9v			(NONE)
9 - 20v	X	MCC	SRH-COM
JOGR 10v			(NONE)
20v	X	MCC	JH - COM
LT. 3-7sec.	X		(NONE)
2 - 60sec		MCC	332Ω FROM LT1 TO COM
VREG		IFC	NT-CEMF, CC-COM
DC TACHO	X		(NONE)
AC TACHO		MCC	AT1-AT2
TACHO FILT		IFC	TC-TC
TACHO V.			
24-64vdc		IFC	NT-NT1, PT-PT1
27-71vac		IFC	NT-NT1, PT-PT1
60-160vdc		IFC	NT-NT2, PT-PT2
66-177vac		IFC	NT-NT2, PT-PT2
110-300vdc	X	IFC	NT-NT3, PT-PT3
120-300vac		IFC	NT-NT3, PT-PT3
G134 G256			
1.3 1.7		MFC	NONE
2.1 2.8		MFC	YB-YD
2.4 5.0		MFC	YA-YB
4.0 8.0		MFC	YA-YB, YC-YD
7.0 13		MFC	YA-YC
13 25	X	MFC	YA-YC, YB-YD
L/R < .25S		MFC	QA-QB
INH RUN		DGC	D1-D2 (IF USED)

* CEMF COUNTER EMF (3.16)
* CFB CURRENT FEEDBACK (3.16)
CMFA ABSOLUTE VALUE CEMF (3.08)
CRM CROSSOVER MODIFY (4.11)
DFP DELAYED FIRING POWER (3.25)
* DR DRIVER REFERENCE (3.33)
* EAO ERROR AMP OUTPUT (3.33)
EST EXTERNAL FLT STOP INPUT (3.14)
FALT FAULT (3.14)
* FC FIELD CURRENT (NS26)
FDR FIELD DIAGNOSTIC REFERENCE (4.08)
FEA FIELD ECONOMY ADJUST (3.25)
FF FIELD FAULT (2.28)
IABS MOTOR CURRENT ABSOLUTE (3.09)
ILA CURRENT LIMIT ADJUST (3.23)
IMET CURRENT SIGNAL FOR METER (3.10)
* IPU INITIAL PULSE (3.20)
* LR LOCAL REF. FROM DGC (3.33)
* JOG JOG SWITCH INPUT (3.23)
* JOGR JOG REFERENCE INPUT (3.31)
* MAC MAX/MA CONTROL SIGNAL (3.20)
MSW MODE SWITCH (3.30)
* OSC OSCILLATOR (3.17)
* PCR PHASE CONTROL REF. (3.26)
* PRE DRIVE PRECONDITION (3.21)
ØSEQ PHASE SEQUENCE (3.14)
RERR REGULATOR ERROR (3.27)
RIJ INTEGRATOR SUMMING JUNCTION (3.27)
RJ REGULATOR SUMMING JUNCTION (3.31)
RRA REGULATOR RESPONSE ADJUST (3.30)
RSET RESET (3.16)
* RTR READY TO RUN (3.16)
* RUN RUN SWITCH INPUT (3.21)
* SA-C PHASE SYN OUTPUT (3.16)
* SFB SPEED FEEDBACK (3.20)
SMET SPEED SIGNAL FOR METER (3.12)
* SR SYSTEM REFERENCE INPUT (3.29)
* SYS SYSTEM FAULT TRIP (3.13)
* TA OUTPUT FOR TACHO TRIP ADJUST (3.20)
TF TACHO FAULT (NS28)
* TFB TACHOMETER FEEDBACK (3.20)
TFR AC TACHO FREQUENCY OUTPUT (3.13)
* TR TIMED REFERENCE (3.33)
* VFB VOLTAGE FEEDBACK (3.19)
* WFR WEAK FIELD REFERENCE (3.20)

(* - TEST POINT ON DOOR FRONT)

MAPPING SYSTEM

(NS/PS/TS) PS - PAST SHEET
NS - NEXT SHEET
TS - THIS SHEET

NOTE: FIELD EFFECT TRANSISTOR: THE
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SWITCHED FOR "PRECONDITION" - "RUN"
OR JOG" - "DIAGNOSTIC STATIC" -
"DIAGNOSTIC RUN" IS SHOWN BY A
FOUR DIGIT WORD WITH STATE SEQUENCE.

A B C D E F G H J K L M N

TECHN. ENG. APPD. TECHN. ENG. APPD. DATE

3-7-79

TECHN.

ENG.

APPD.

AP

ALLENWEST

Simplex

VARIABLE SPEED
DRIVES OPERATION
BRIGHTON, ENGLAND

WEBSTER & BENNETT LTD.
75HP BDC3034R SPINDLE DRIVE.
ELEMENTARY DIAGRAM.

GO NUMBER

921N09

ELEMENTARY DIAGRAM

902M122BY

CONTD

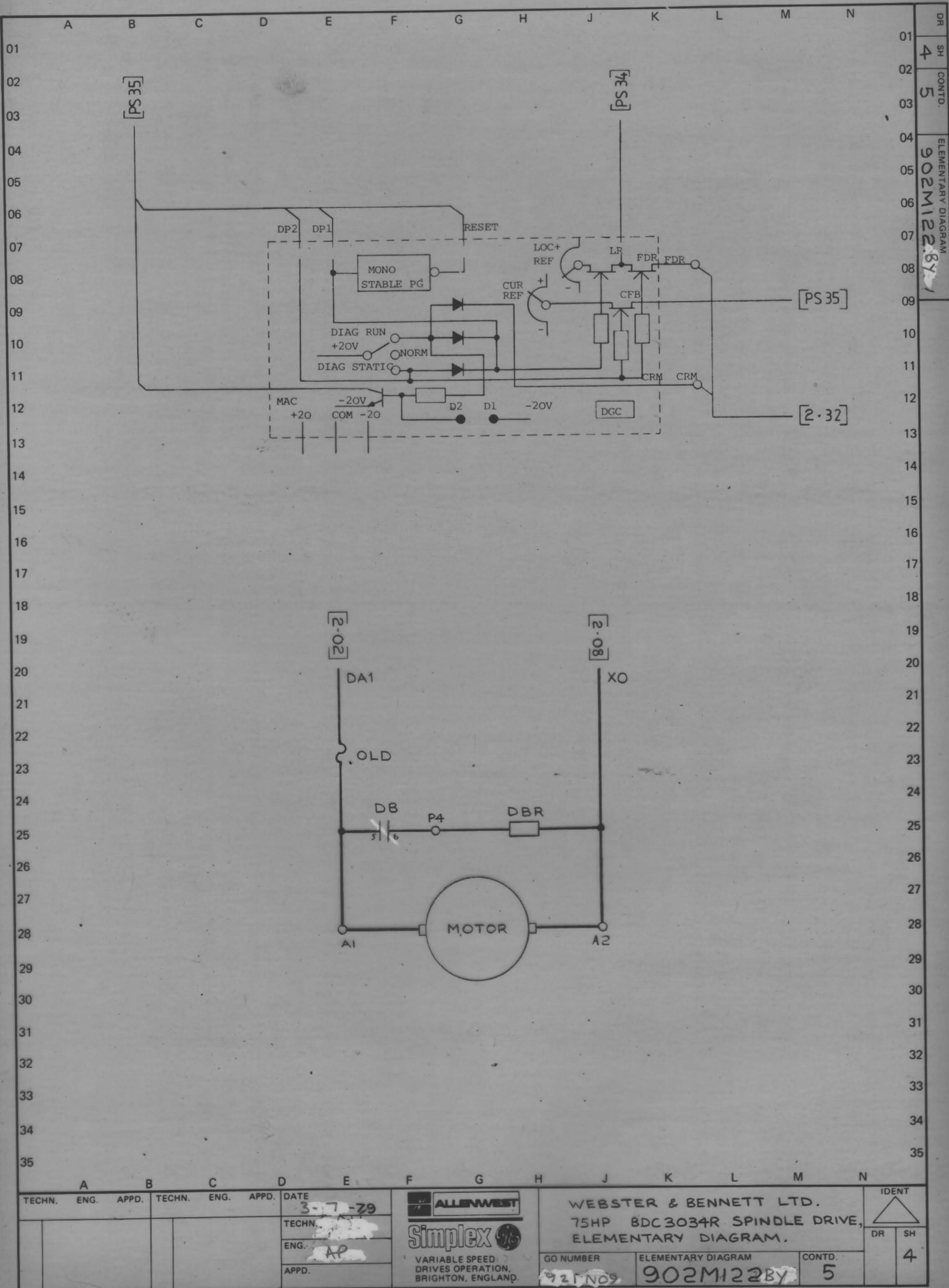
2

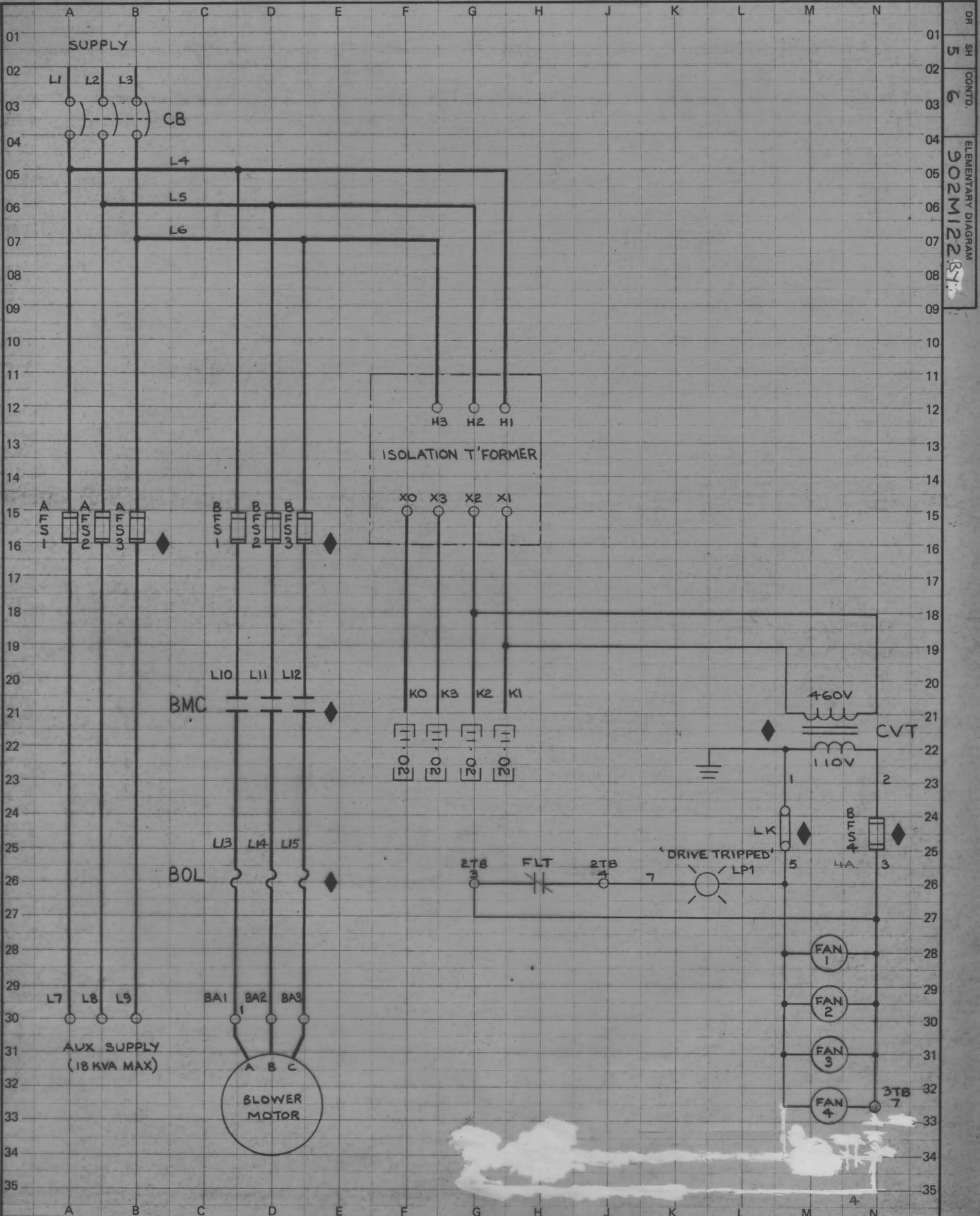
IDENT

DR

SH

1

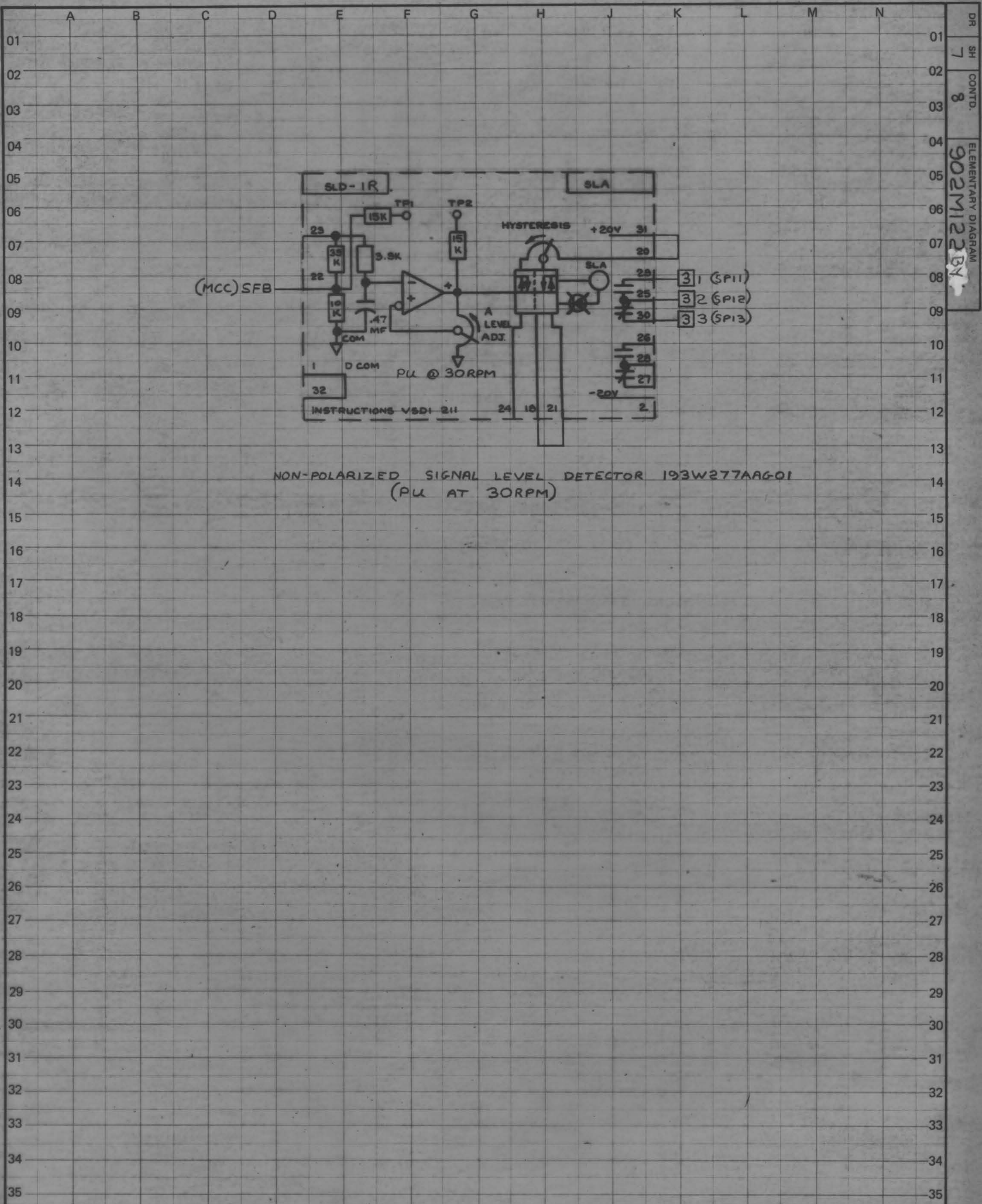




TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	3-7-79	 Simplex VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.	WEBSTER & BENNETT LTD. 75HP BDC3034R SPINDLE DRIVE, ELEMENTARY DIAGRAM.		GO NUMBER 921N09		ELEMENTARY DIAGRAM 902M122BY		CONTD. 6		IDENT DR SH 5	

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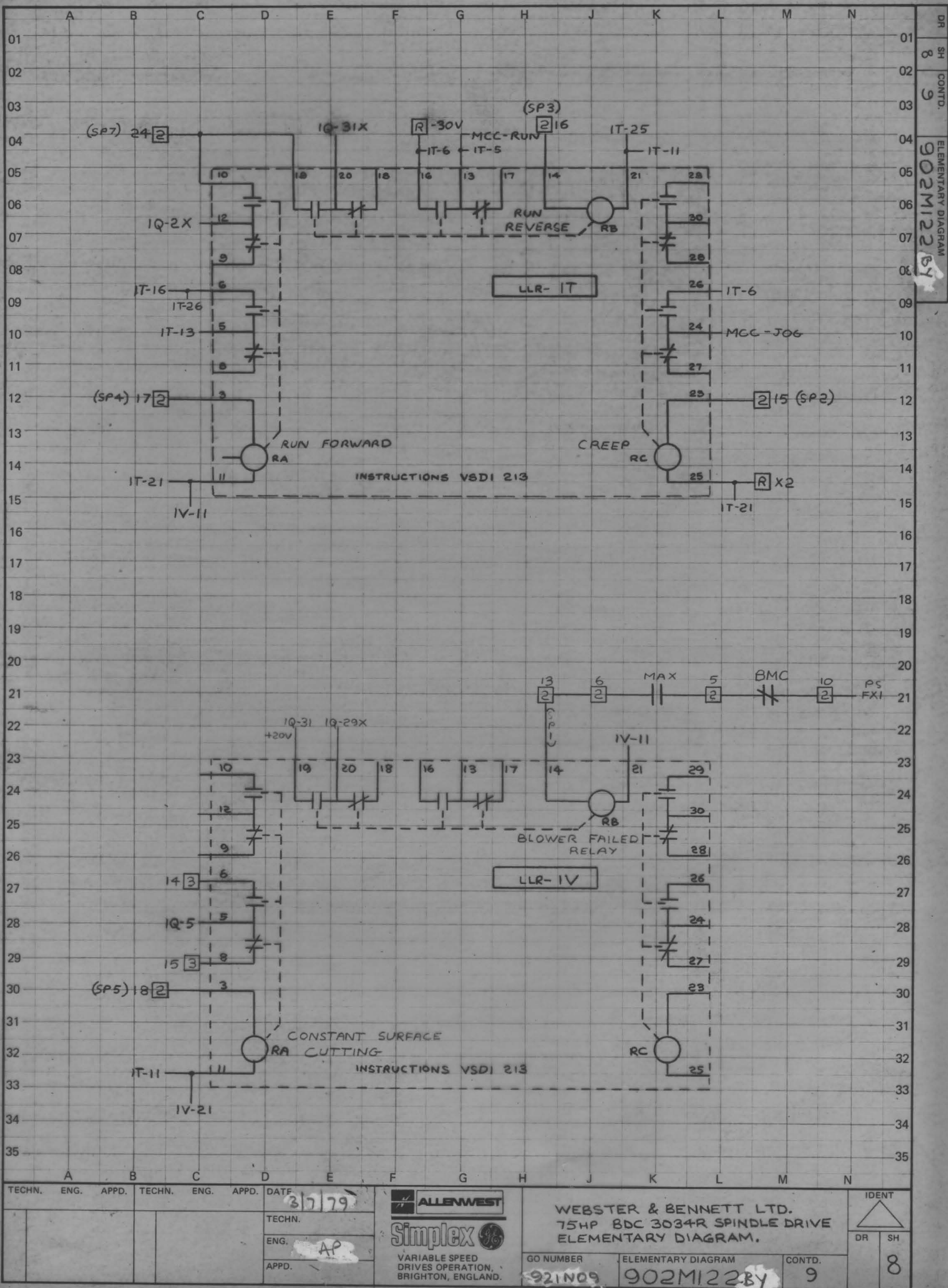


NON-POLARIZED SIGNAL LEVEL DETECTOR 193W277AAG-01
(PU AT 30RPM)

DR SH CONTD. 8
ELEMENTARY DIAGRAM
902M122BY

TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	3-7-79	 Simplex VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.	WEBSTER & BENNETT LTD. 75HP BDC3034R SPINDLE DRIVE ELEMENTARY DIAGRAM.			IDENT 	
						TECHN.			GO NUMBER	ELEMENTARY DIAGRAM	CONTD.	DR	SH
						ENG.	AP		921N09	902M122BY	8		7
						APPD.							

Disclaimer Statement The trade mark is the trade mark of General Electric Company of U.S.A., which is not connected with the English Company of a similar name.

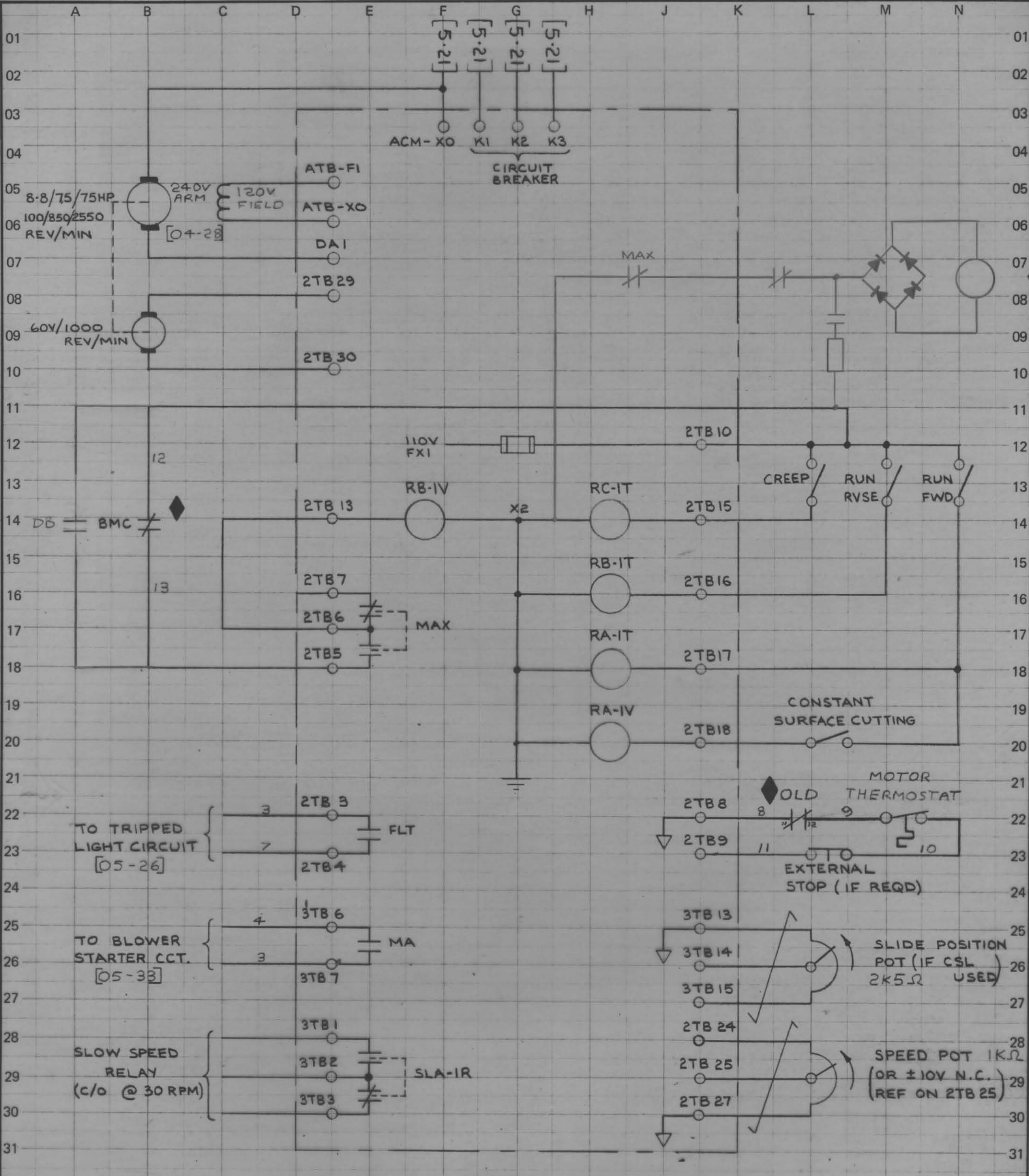


THE PRINTED CIRCUIT CARD SHOULD ALWAYS BE REMOVED WITH THE CARD EXTRACTOR WHICH IS ATTACHED ON TOP OF THE CARD RACK. SOME CARDS CONTAIN PARTS WHICH WILL BE THERMALLY HOT AFTER BEING IN OPERATION. CARE SHOULD BE EXERCISED IN HANDLING ALL CARDS AFTER REMOVAL UNTIL THESE PARTS HAVE COOLED. DO NOT REMOVE OR INSERT CARDS WITH POWER APPLIED.

INDICATING LIGHT

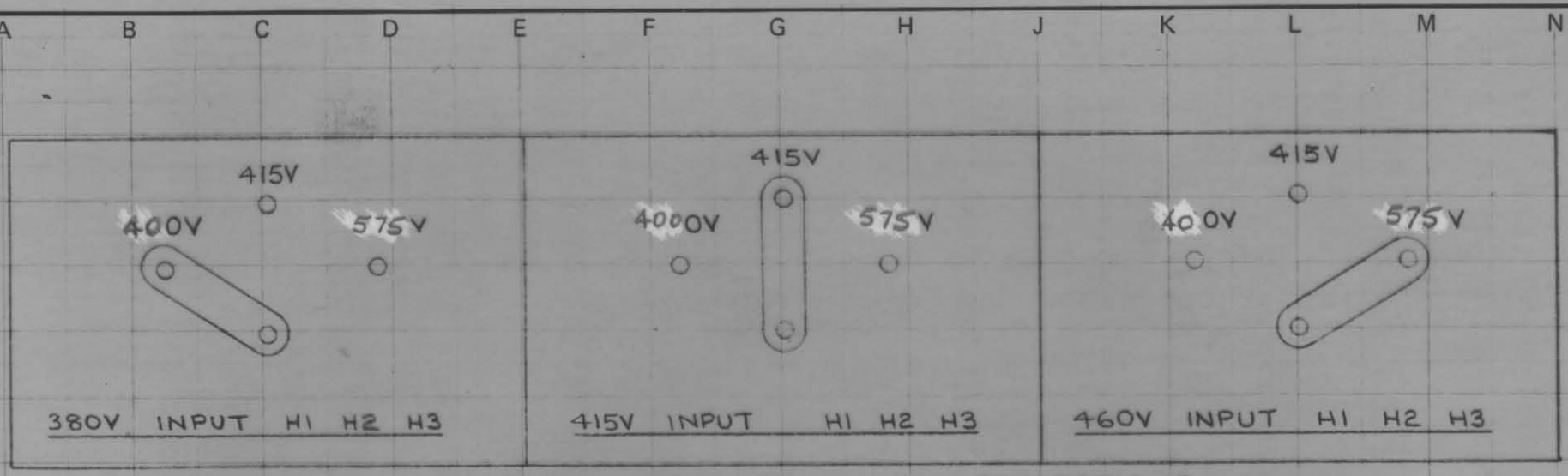
1Q-2X - 1T-12	1T-10 - 1T-19	2TB6 - 2TB13	SP1 - 1V-14
1Q-4X - 1Q-5X	1T-11 - 1T-21	1Q-24 - 1Q-26	SP2 - 1T-23
1Q-5X - 1P-19X	1T-21 - 1T-25	1Q-26 - 1Q-28	SP3 - 1T-14
1Q-5 - 1V-5	1T-25 - RTB(X2)	1Q-28 - 1Q-29	SP4 - 1T-3
1Q-7 - 1Q-2	1T-11 - 1V-11	1Q-28X - 1Q-26X	SP5 - 1V-3
1Q-7X - 1Q-32X	1T-5 - 1T-13	1Q-26X - 1Q-15	
1Q-30X - 1Q-15	1T-13 - MCC(RUN)	1Q-29X - 1V-20	SP7 - 1T-10
1Q-32 - (JOG ^{MCC} R)	1T-26 - 1T-6	1Q-24X - (SYS ^{IFC})	SP8 - 1Q-4
1Q-31X - 1T-20	1T-6 - 1T-16		SP11 - 1R-29
	1T-16 - RTB(-30V)		SP12 - 1R-25
1R-22 - MCC(SFB)	1T-24 - MCC(JOG)		SP13 - 1R-30
1R-19 - 1R-21			SP23 - 1P-15
1R-20 - 1R-31			SP24 - 1V-6
1V-8 - 1P-21X			SP25 - 1V-8
1P-21X - MCC(SR)			
1V-11 - 1V-21			
1V-19 - 1Q-31			
BUS ALL PINS 2 1P - 1R	1R-2 - RTB (-20V)		3TB6 - MA11
BUS ALL PINS 15 1P - 1R	1R-15 - RTB (COM)		3TB7 - MA12
BUS ALL PINS 31 1P - 1R	1R-31 - RTB (+20V)		

A		B		C		D		E		F		G		H		J		K		L		M		N							
TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE			WEBSTER & BENNETT LTD. 75HP BDC 3034R SPINDLE DRIVE, ELEMENTARY DIAGRAM.															IDENT 							
						3/7/79			GO NUMBER 921N09															ELEMENTARY DIAGRAM 902M122BY		CONTD. 11		DR 10		SH 10	
							VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.																								



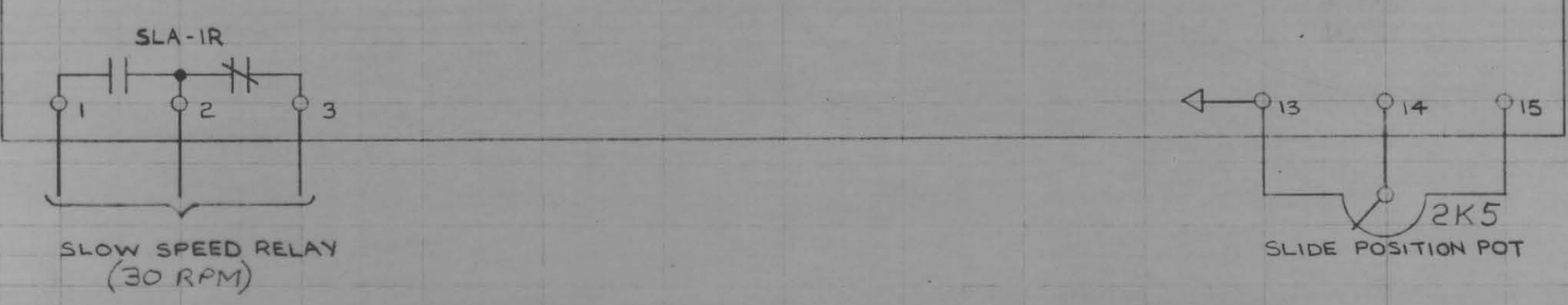
CONTROLLER CONNECTIONS ONLY

TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	3/7/79	ALLENWEST	WEBSTER AND BENNETT LTD.	IDENT
								Simplex	75HP BDC3034R SPINDLE DRIVE,	DR SH
								VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.	ELEMENTARY DIAGRAM	11
								GO NUMBER 921N67	902M122BY	12

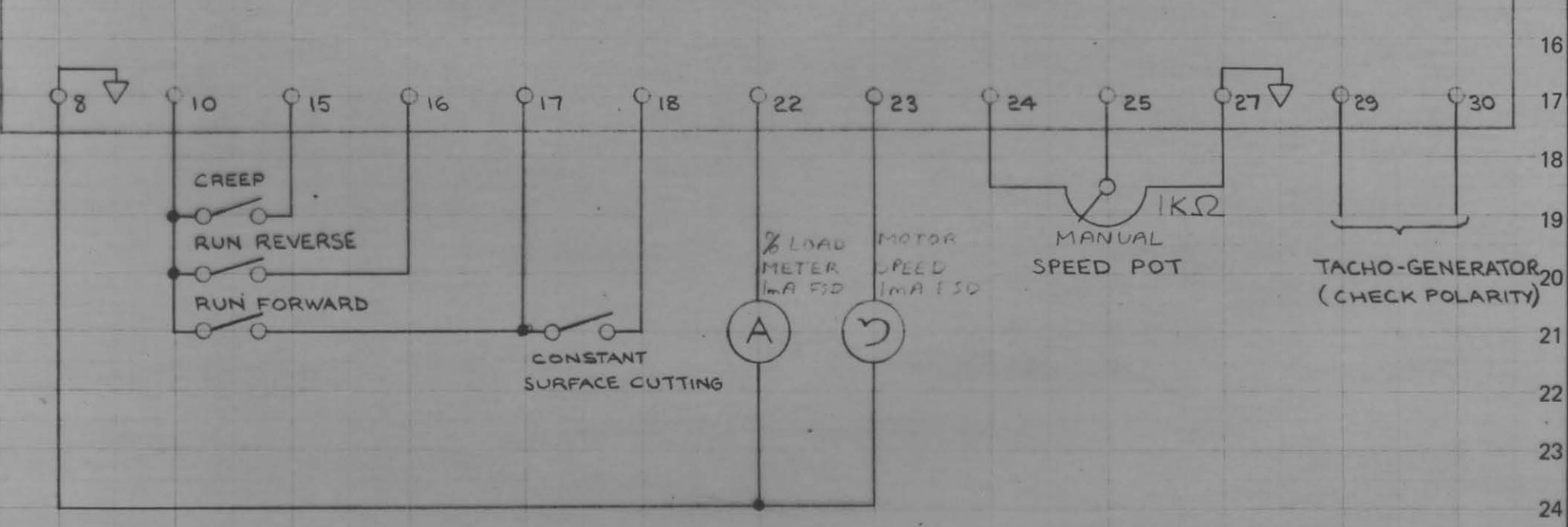


MAIN ISOLATING TRANSFORMER INPUT CONNECTIONS.

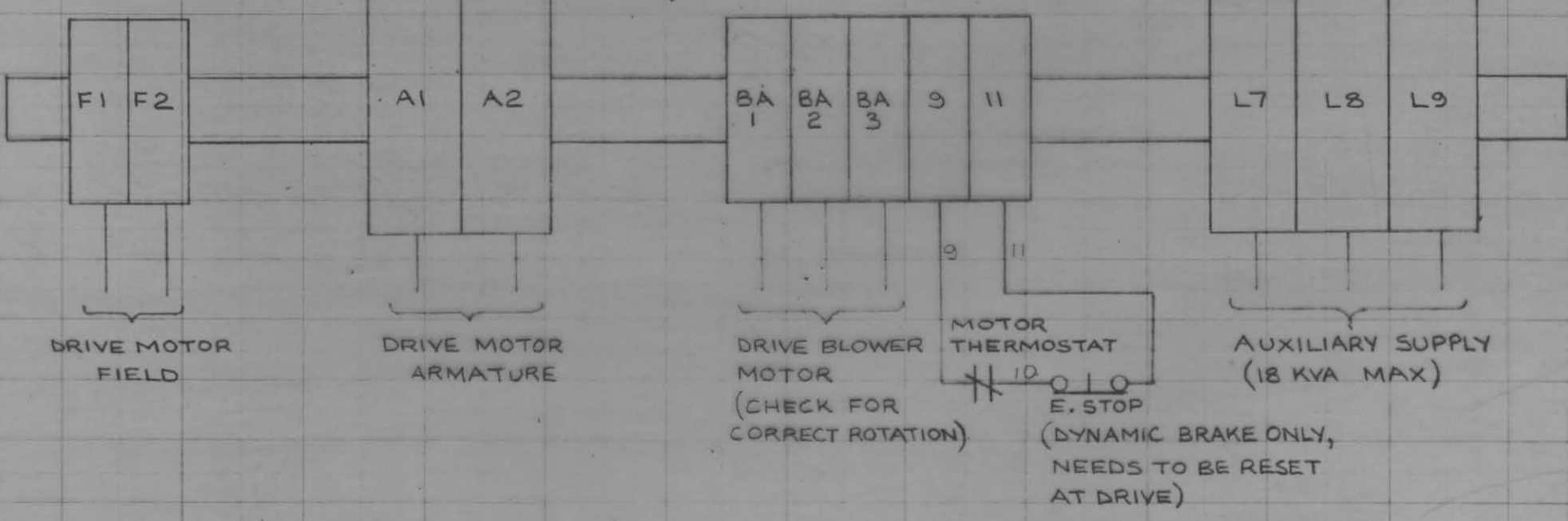
3TB (LOCATED IN DRIVE ABOVE 2TB)



2TB (LOCATED IN BOTTOM OF DRIVE)



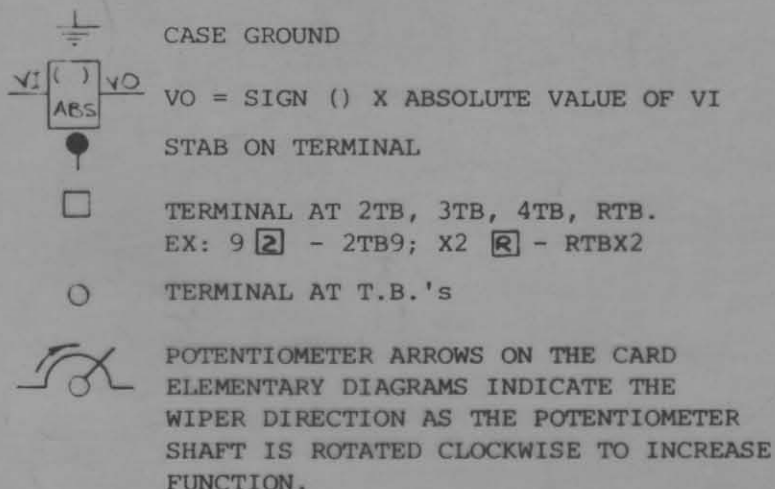
TB (LOCATED ON PANEL)



SIGNAL DEFINITIONS AND LOCATIONS

* CEMF	COUNTER EMF (3-16)
* CFB	CURRENT FEEDBACK (3-16)
CMFA	ABSOLUTE VALUE CEMF (3-08)
CRM	CROSSOVER MODIFY (4-11)
DFP	DELAYED FIRING POWER (3-25)
* DR	DRIVER REFERENCE (3-33)
* EAO	ERROR AMP OUTPUT (3-33)
EST	EXTERNAL FLT STOP INPUT (3-14)
FALT	FAULT (3-14)
* FC	FIELD CURRENT (NS26)
FDR	FIELD DIAGNOSTIC REFERENCE (4-08)
FEA	FIELD ECONOMY ADJUST (3-25)
FF	FIELD FAULT (2-28)
IABS	MOTOR CURRENT ABSOLUTE (3-09)
ILA	CURRENT LIMIT ADJUST (3-23)
IMET	CURRENT SIGNAL FOR METER (3-10)
* IPU	INITIAL PULSE (3-20)
* LR	LOCAL REF. FROM DGC (3-33)
* JOG	JOG SWITCH INPUT (3-23)
* JOGR	JOG REFERENCE INPUT (3-31)
* MAC	MAX/MA CONTROL SIGNAL (3-20)
MSW	MODE SWITCH (3-30)
* OSC	OSCILLATOR (3-17)
* PCR	PHASE CONTROL REF. (3-26)
* PRE	DRIVE PRECONDITION (3-21)
ØSEQ	PHASE SEQUENCE (3-14)
RERR	REGULATOR ERROR (3-27)
RIJ	INTEGRATOR SUMMING JUNCTION (3-27)
RJ	REGULATOR SUMMING JUNCTION (3-31)
RRA	REGULATOR RESPONSE ADJUST (3-30)
RSET	RESET (3-16)
* RTR	READY TO RUN (3-16)
* RUN	RUN SWITCH INPUT (3-21)
* SA-C	PHASE SYN OUTPUT (3-16)
* SFB	SPEED FEEDBACK (3-20)
SMET	SPEED SIGNAL FOR METER (3-12)
* SR	SYSTEM REFERENCE INPUT (3-29)
* SYS	SYSTEM FAULT TRIP (3-13)
* TA	OUTPUT FOR TACHO TRIP ADJUST (3-20)
TF	TACHO FAULT (NS28)
* TFB	TACHOMETER FEEDBACK (3-20)
TFR	AC TACHO FREQUENCY OUTPUT (3-13)
* TR	TIMED REFERENCE (3-33)
* VFB	VOLTAGE FEEDBACK (3-19)
* WFR	WEAK FIELD REFERENCE (3-20)

(* - TEST POINT ON DOOR FRONT)




THESE RESISTORS ARE CRIMPED IN WIRE HARNESS.

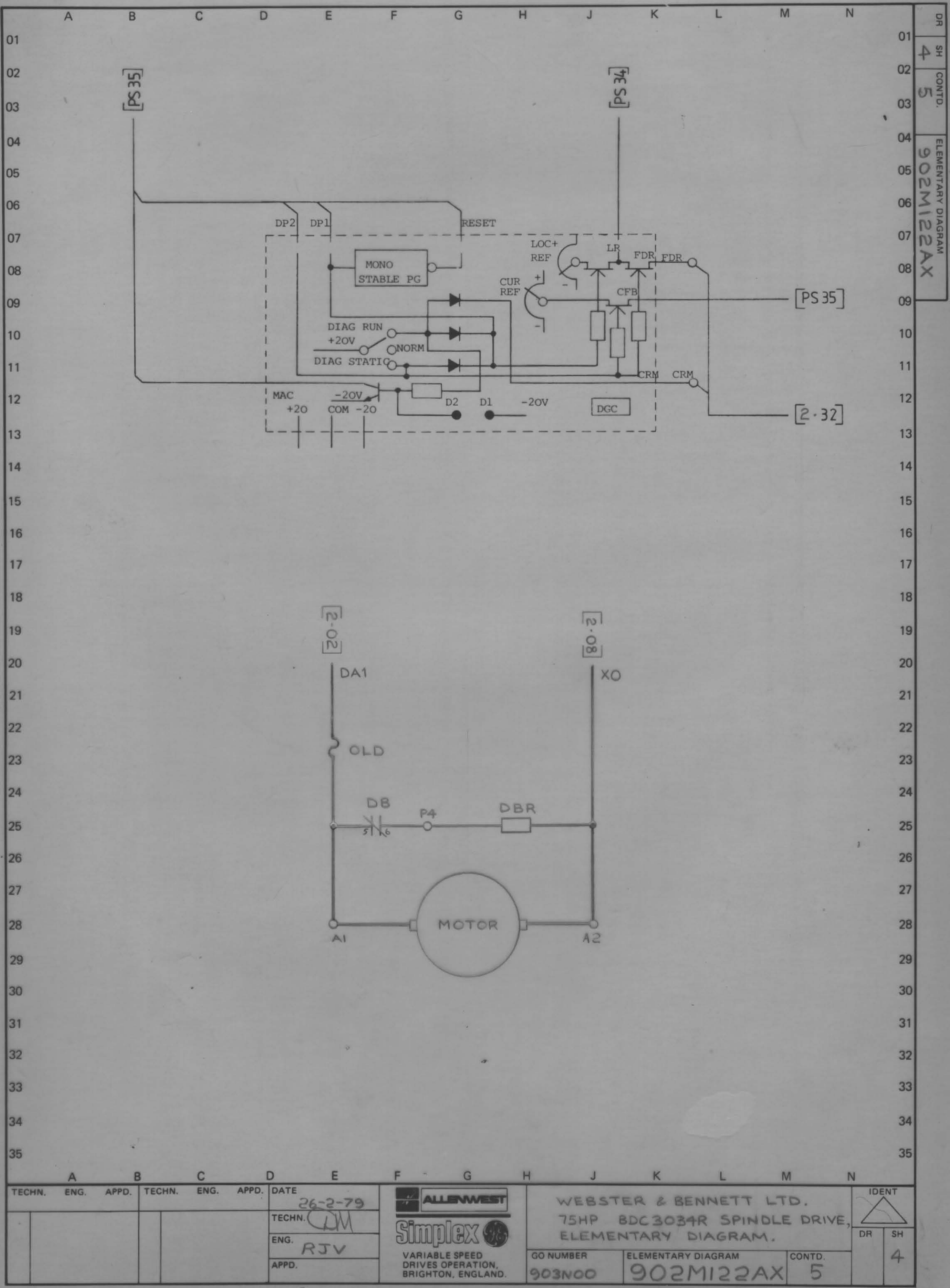
MTD. ON PANEL

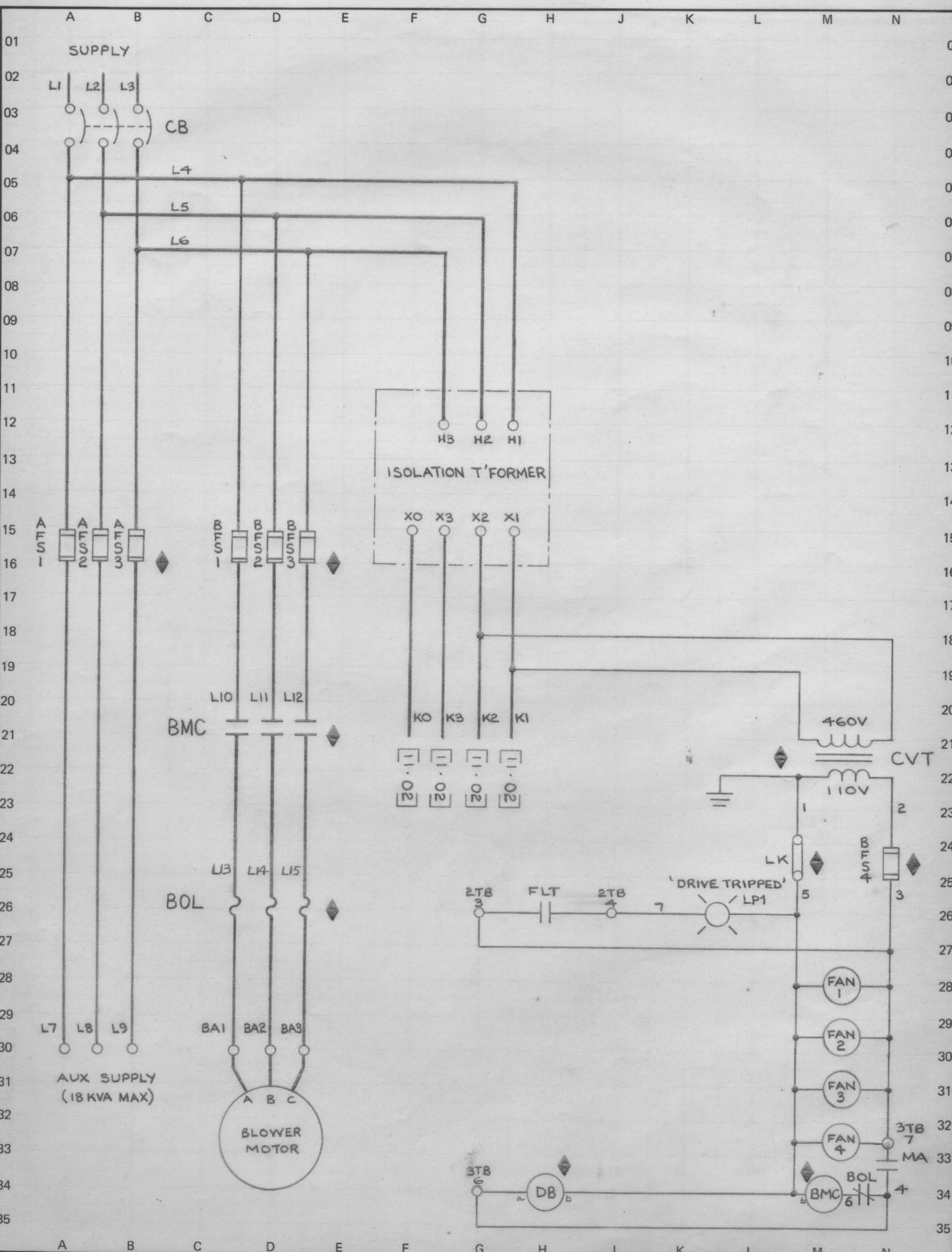
FUNCTION	USE	LOC	JUMPERS
60HZ		MCC	AA-AS, BA-BS, CA-CS MFC ZA-ZB (IF USED)
50HZ	X	MCC	AA-AF, BA-BF, CA-CF
IOC-400%	X		NONE
-500%		IFC	I-IHI
-300%		IFC	I-ILO
SR5 - 9v			(NONE)
9 - 20v	X	MCC	SRH-COM
JOGR 10v			(NONE)
20v	X	MCC	JH - COM
LT.3-7sec.	X		(NONE)
2 - 60sec		MCC	332ΩFROM LT1TOCOM
VREG		IFC	NT-CEMF, CC-COM
DC TACHO	X		(NONE)
AC TACHO		MCC	AT1-AT2
TACHO FILT		IFC	TC-TC
TACHO V. 24-64vdc		IFC	NT-NT1, PT-PT1
27-71vac		IFC	NT-NT1, PT-PT1
60-160vdc		IFC	NT-NT2, PT-PT2
66-177vac		IFC	NT-NT2, PT-PT2
110-300vdc	X	IFC	NT-NT3, PT-PT3
120-300vac		IFC	NT-NT3, PT-PT3
G134 G256			
1.8 1.7		MFC	NONE
1.3 2.8		MFC	YB-YD
2.4 5.0		MFC	YA-YB
4.0 8.0		MFC	YA-YB, YC-YD
7.0 13		MFC	YA-YC
13 25	X	MFC	YA-YC, YB-YD
L/R < .25S		MFC	QA-QB
INH RUN		DGC	D1-D2 (IF USED)

MAPPING SYSTEM

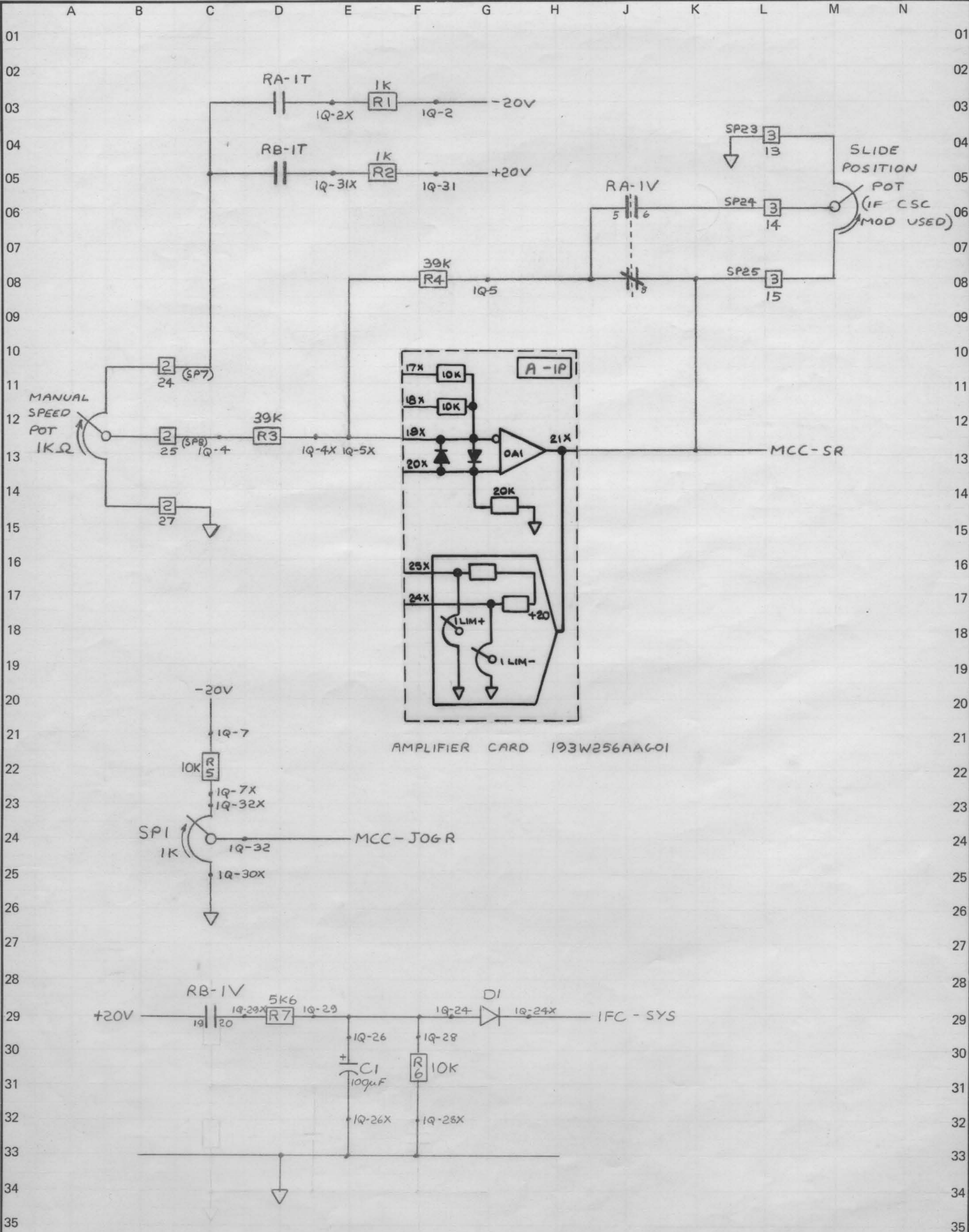
(NS/PS/TS) PS - PAST SHEET
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SWITCHED FOR "PRECONDITION" - "RUN"
OR JOG" - "DIAGNOSTIC STATIC" -
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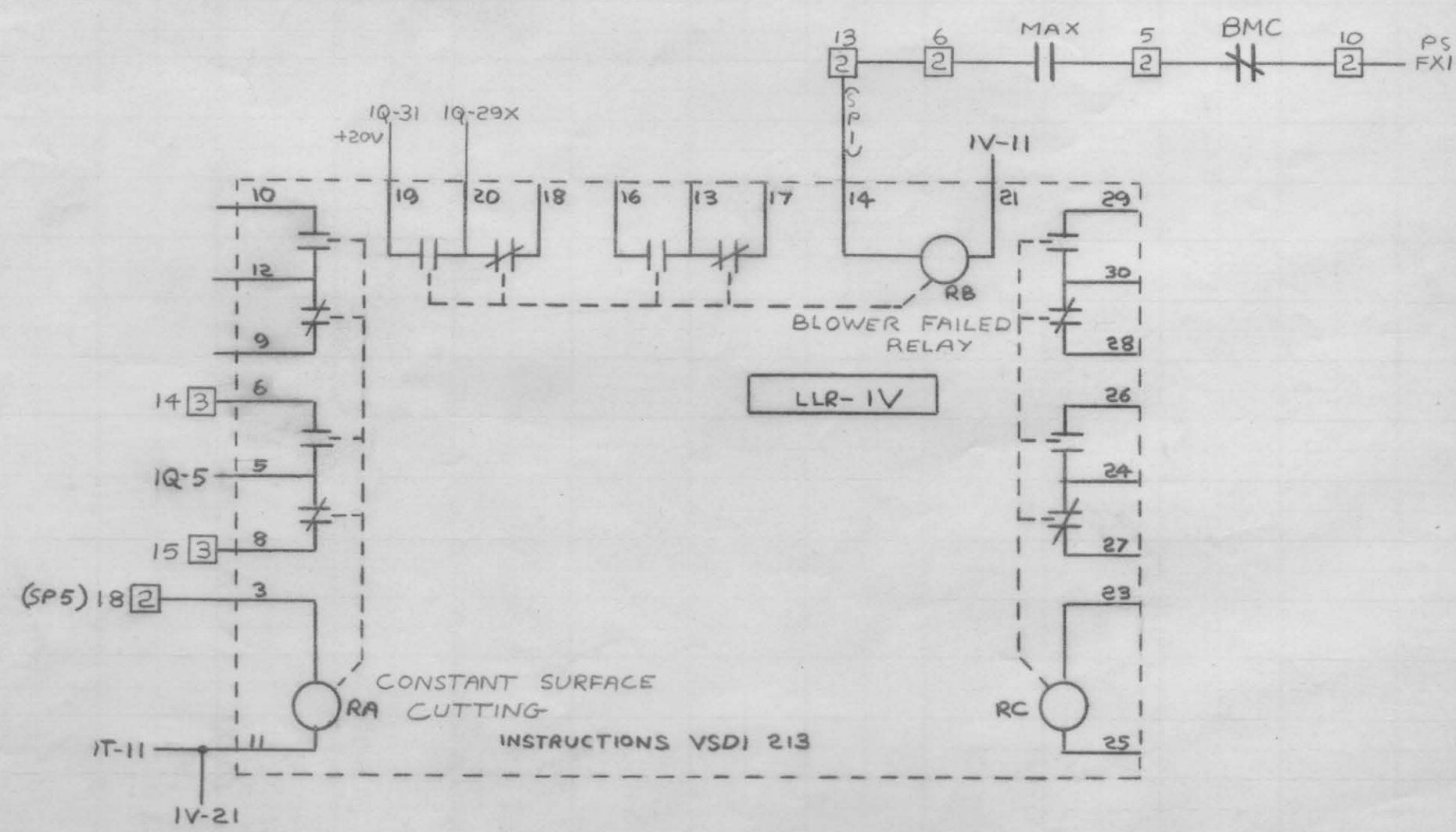
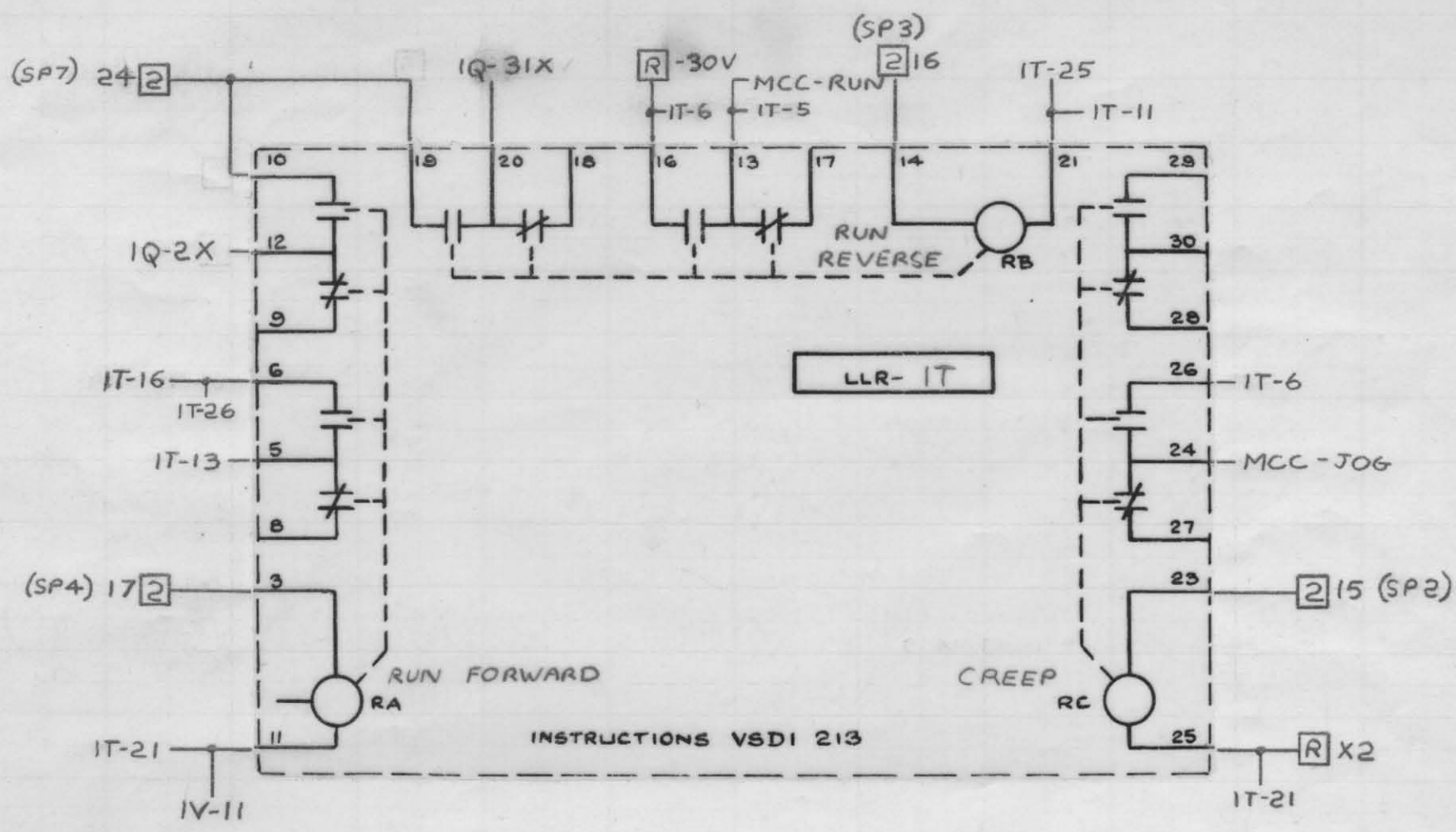
TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	14-3-79	 Simplex VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.	WEBSTER & BENNETT LTD. 75HP BDC3034R SPINDLE DRIVE, ELEMENTARY DIAGRAM.			IDENT
						TECHN.	DM		GO NUMBER 903N00	ELEMENTARY DIAGRAM 902M122AX	CONTD. 6	DR SH 5
						ENG.	RJV					
						APPD.						



TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	ALLENWEST Simplex VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.		WEBSTER & BENNETT LTD. 75HP BDC 3034R SPINDLE DRIVE, ELEMENTARY DIAGRAM.		IDENT DR SH 6	
						26-2-79	GO NUMBER 903N00		ELEMENTARY DIAGRAM 902M122AX		CONTD. 7	
						RTV						

Disclaimer Statement The trade mark is the trade mark of General Electric Company of U.S.A.

A B C D E F G H J K L M N															DR	SH	CONTD.	ELEMENTARY DIAGRAM
															7		8	902M122AX



	A	B	C	D	E	F	G	H	J	K	L	M	N	P	Q	R	S	T	U	V	
01																					
02															A	CC	SLD		LLR		LLR
03															193W 256 AA G-01	909W 317 AA G-02	193W 277 AA G-01	193W279 AA G-03	193W279 AA G-03		
04																					
05																					
06																					
07																					
08																					
09																					
10																					
11															AMPL- IFIER	SELECT COMP	SIG- LEV DET	LOW LEVEL RELAY	LOW LEVEL RELAY		

ON PRINTED CIRCUIT CARDS USED IN THIS RACK THE LETTERS 'AA' AFTER BASIC CATALOGUE NUMBER INDICATES ORIGINAL DESIGN. SUBSEQUENT DESIGNS WITH THE SAME BASIC NUMBERS AND GROUP NUMBER WITH THE SECOND LETTER CHANGED, SUCH AS: AB, AC, AD, ETC., ARE DIRECTLY INTERCHANGEABLE AND MAY BE SUPPLIED IN PLACE OF THE 'AA' CARDS.

THE PRINTED CIRCUIT CARD SHOULD ALWAYS BE REMOVED WITH THE CARD EXTRACTOR WHICH IS ATTACHED ON TOP OF THE CARD RACK. SOME CARDS CONTAIN PARTS WHICH WILL BE THERMALLY HOT AFTER BEING IN OPERATION. CARE SHOULD BE EXERCISED IN HANDLING ALL CARDS AFTER REMOVAL UNTIL THESE PARTS HAVE COOLED. DO NOT REMOVE OR INSERT CARDS WITH POWER APPLIED.

FRONT VIEW OF 64 PIN
RECEPTACLE AS SEEN
IN RACK CLOSED
POSITION.

SYMBOLS :

● TEST POST



POT ADJUSTMENT





INDICATING LIGHT

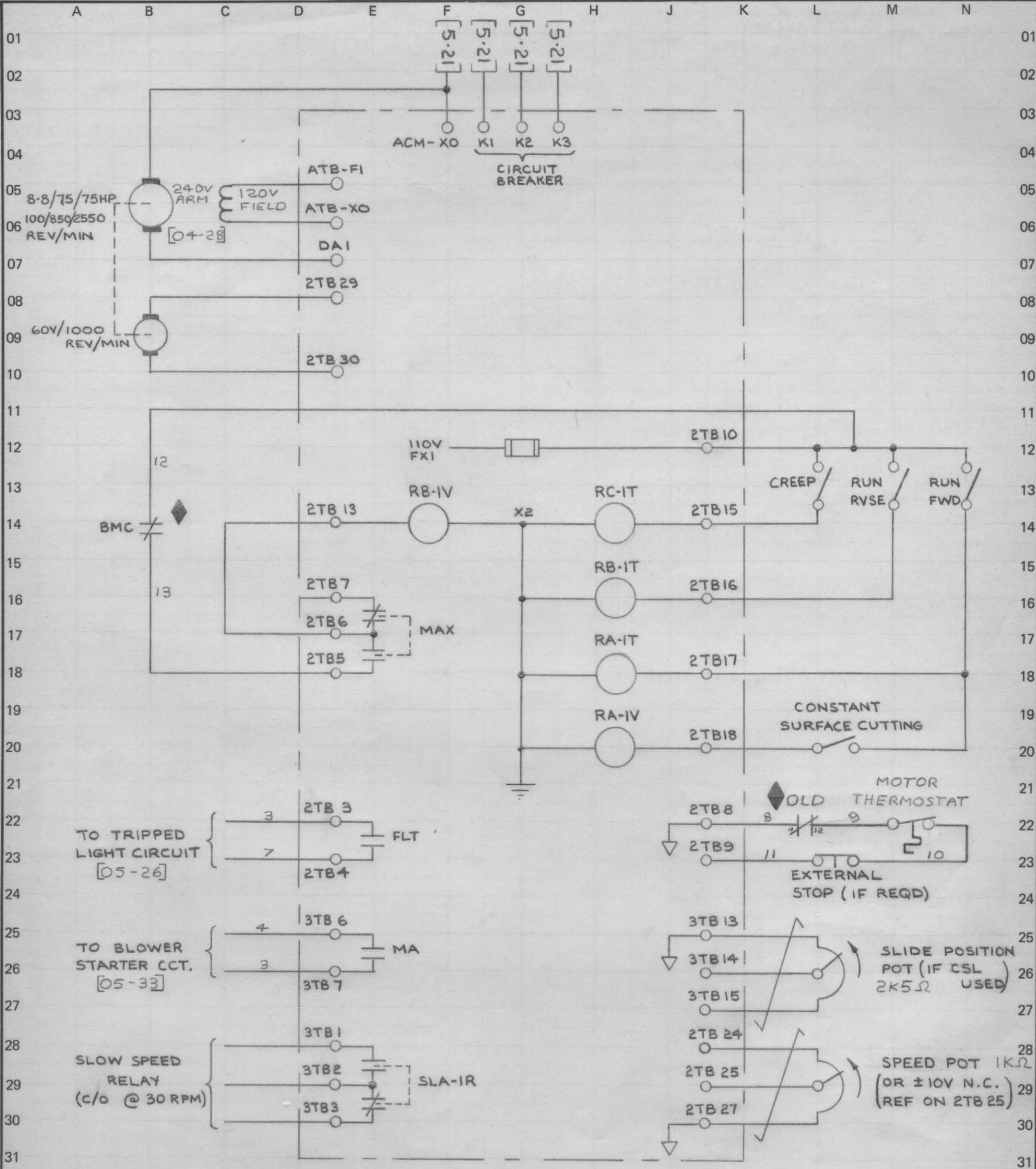
18	32 - 64	32 - 32X
	31 - 63	31 - 31X
19	30 - 62	30 - 30X
	29 - 61	29 - 29X
20	28 - 60	28 - 28X
	27 - 59	27 - 27X
21	26 - 58	26 - 26X
	25 - 57	25 - 25X
22	24 - 56	24 - 24X
	23 - 55	23 - 23X
23	22 - 54	22 - 22X
	21 - 53	21 - 21X
24	20 - 52	20 - 20X
	19 - 51	19 - 19X
25	18 - 50	18 - 18X
	17 - 49	17 - 17X
26	16 - 48	16 - 16X
	15 - 47	15 - 15X
27	14 - 46	14 - 14X
	13 - 45	13 - 13X
28	12 - 44	12 - 12X
	11 - 43	11 - 11X
29	10 - 42	10 - 10X
	9 - 41	9 - 9X
30	8 - 40	8 - 8X
	7 - 39	7 - 7X
31	6 - 38	6 - 6X
	5 - 37	5 - 5X
32	4 - 36	4 - 4X
	3 - 35	3 - 3X
33	2 - 34	2 - 2X
	1 - 33	1 - 1X

CARD RACK WIRE JUMPER TABLE

1Q-2X - 1T-12	1T-10 - 1T-19	2TB6 - 2TB13	SP1 - 1V-14
1Q-4X - 1Q-5X	1T-11 - 1T-21	1Q-24 - 1Q-26	SP2 - 1T-23
1Q-5X - 1P-19X	1T-21 - 1T-25	1Q-26 - 1Q-28	SP3 - 1T-14
1Q-5 - 1V-5	1T-25 - RTB(X2)	1Q-28 - 1Q-29	SP4 - 1T-3
1Q-7 - 1Q-2	1T-11 - 1V-11	1Q-28X - 1Q-26X	SP5 - 1V-3
1Q-7X - 1Q-32X	1T-5 - 1T-13	1Q-26X - 1Q-15	
1Q-30X - 1Q-15	1T-13 - MCC(RUN)	1Q-29X - 1V-20	SP7 - 1T-10
1Q-32 - (JOG ^{MCC} R)	1T-26 - 1T-6	1Q-24X - (SY ^{IFC} S)	SP8 - 1Q-4
1Q-31X - 1T-20	1T-6 - 1T-16		SP11 - 1R-29
	1T-16 - RTB(-30V)		SP12 - 1R-25
1R-22 - MCC(SFB)	1T-24 - MCC(JOG)		SP13 - 1R-30
1R-19 - 1R-21			SP23 - 1P-15
1R-20 - 1R-31			SP24 - 1V-6
1V-8 - 1P-21X			SP25 - 1V-8
1P-21X - MCC(SR)			
1V-11 - 1V-21			
1V-19 - 1Q-31			
BUS ALL PINS 2	1R-2 - RTB		
1P - 1R	(-20V)		
BUS ALL PINS 15	1R-15 - RTB		
1P - 1R	(COM)		
BUS ALL PINS 31	1R-31 - RTB		
1P - 1R	(+20V)		

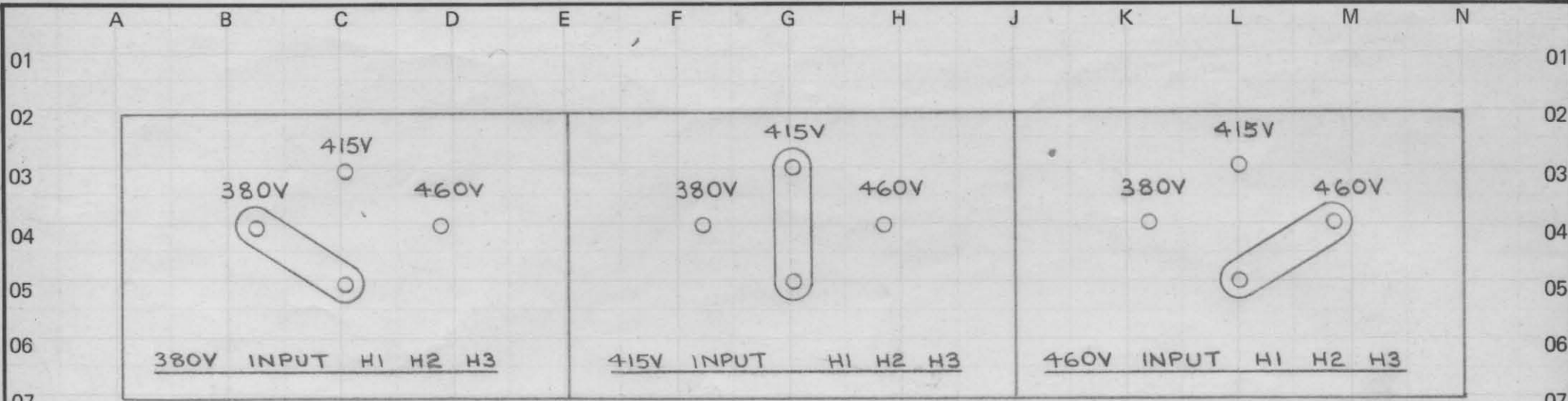
NOTE: RECEPTACLE PINS MAY
BE NUMBERED AS SHOWN
IN EITHER SKETCH. (PIN
33 CORRESPONDS TO PIN
1X, 34 TO 2X, ETC.)

A			B			C			D			E			F			G			H			J			K			L			M			N		
TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE				WEBSTER & BENNETT LTD. 75HP BDC 3034R SPINDLE DRIVE, ELEMENTARY DIAGRAM.									IDENT 																			
						26-2-79													DR			SH																
						TECHN.																																
						ENG.	RTV			Simplex 																												
						APPD.				VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.									GO NUMBER			ELEMENTARY DIAGRAM			CONTD.													
																			903N00			902M122AX			11													
																									10													

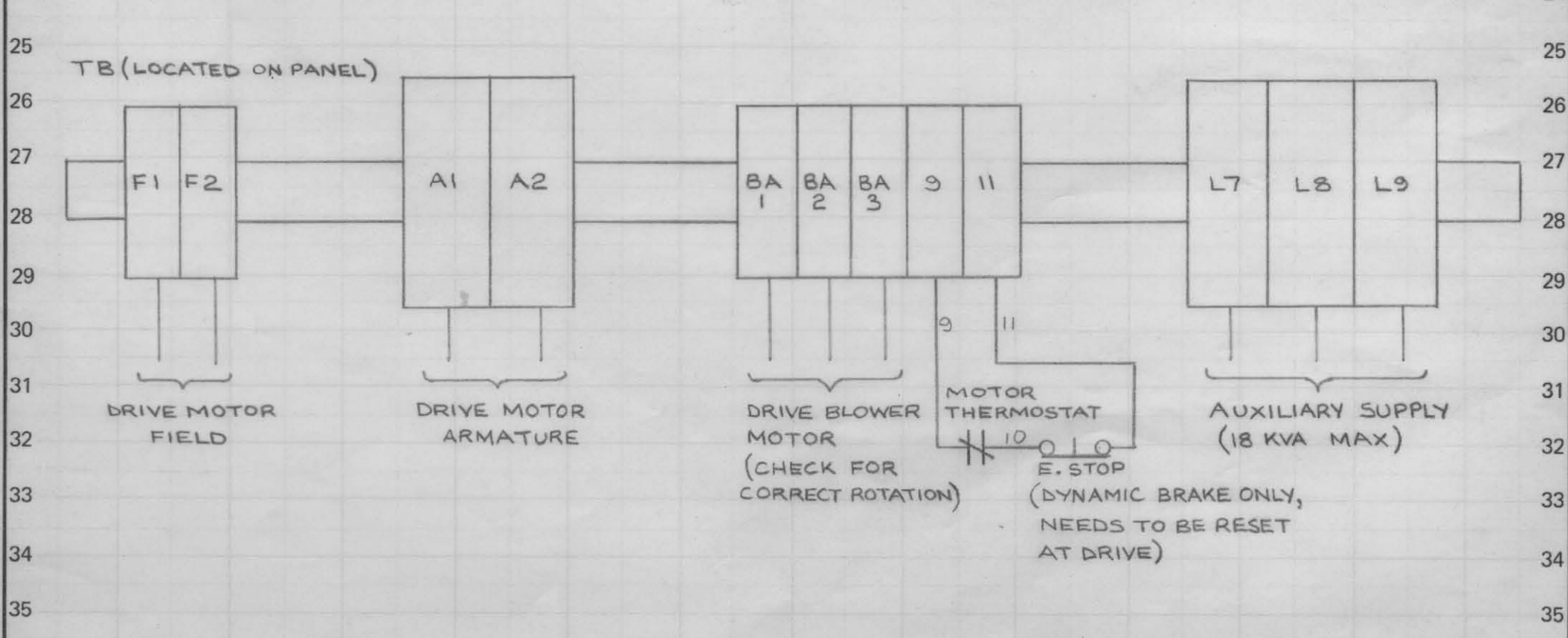
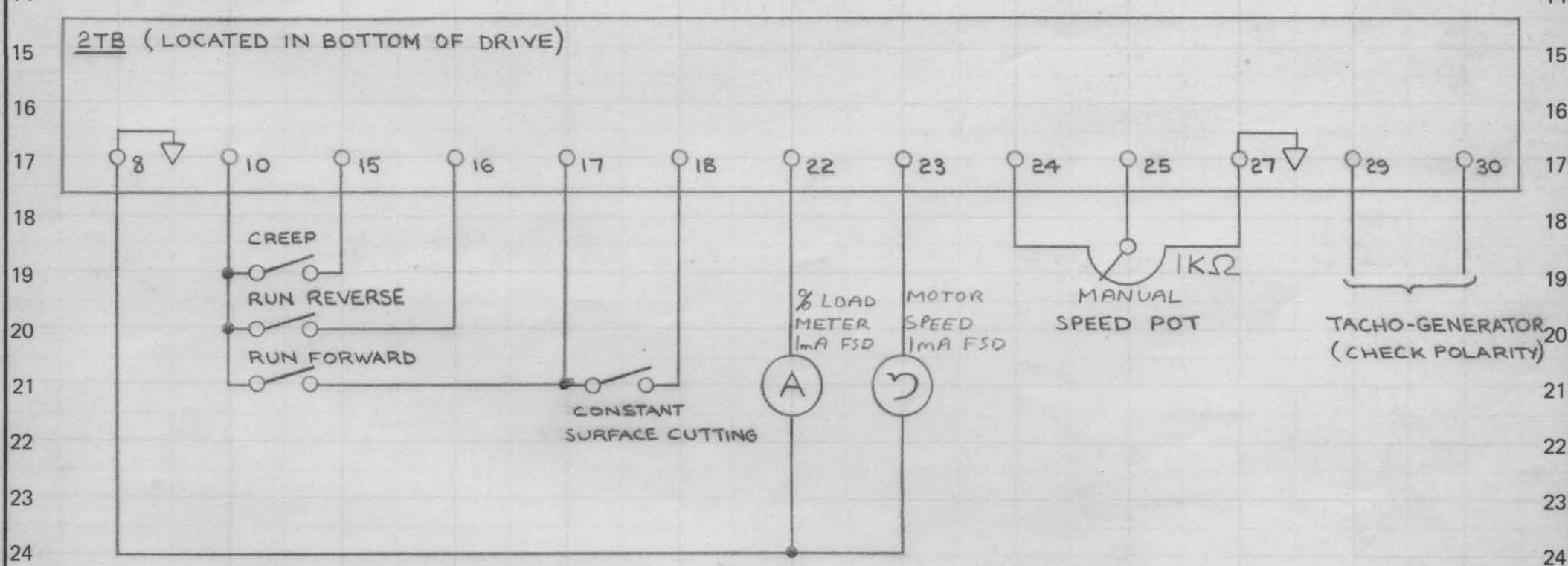


CONTROLLER CONNECTIONS ONLY

TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE					WEBSTER AND BENNETT LTD. 75HP BDC3034R SPINDLE DRIVE, ELEMENTARY DIAGRAM		IDENT 	
						14-3-79								
						TECHN. DM								
						ENG. RSV								
						APPD.	VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.		GO NUMBER 903N00		ELEMENTARY DIAGRAM 902M122AX		CONTD. 12	



MAIN ISOLATING TRANSFORMER INPUT CONNECTIONS.



TECHN.	ENG.	APPD.	TECHN.	ENG.	APPD.	DATE	16-3-79	 Simplex VARIABLE SPEED DRIVES OPERATION, BRIGHTON, ENGLAND.	WEBSTER & BENNETT LTD. 75HP BDC3034R SPINDLE DRIVE. CUSTOMERS CONNECTIONS.			IDENT DR SH 12		
						TECHN.	QJM		GO NUMBER	903N00	ELEMENTARY DIAGRAM	902M122AX	CONTD.	13
						ENG.	RJV							
						APPD.								